

				A			(	B			G	D	<b>(3</b>	F
L	С	D	-	<u>s</u>	4	0	1	С	4	0	<u>T</u>	<u>F</u>	-	1
L Q U D	C R Y S T A L	D I S P L A Y		O T PE EM RP AE T A T U R E		M O D E L	N U M B E R				F L U I D T Y P E	P O L A R I Z E R	DISPLAY MODE	VIEWING DIR

A= OPERATING TEMPERATURE

S = Standard (0 $^{\circ}$  TO 50 $^{\circ}$  C)

H = High Temp (-30 $^{\circ}$  to 75 $^{\circ}$  C)

UH = Ultra High Temp (-40° to 85° C)

**B**= MODEL NUMBER

FOR: ALPHANUMERIC AND NUMERIC

A = Alphanumeric <1.0"

B = Alphanumeric ≥1.0"

C = Numeric <1.0"

D = Numeric ≥1.0"

M = Custom Display <1.0"

N = Custom Display ≥1.0"

401C40 = 4 Character x 1 line, 0.40"

4x1C45 = 4.5 Character x 1 line, 0.45"

x - represents 1/2 column

101D23 = 1 Character x 1 Line, 2.3"

101D40 = 1 Character x 1 Line, 4.0"

FOR: MATRIX AND GRAPHIC MODEL

01602D = 16 Characters x 2 Lines, Dot Matrix

12864G = 128 Columns x 64 Rows, Graphics

C= FLUID TYPE

T = Twisted Nematic (TN)

S = Super Twisted Nematic (STN)

D= POLARIZER MODE

R = Reflective

F = Transflective

M = Transmissive

\*\*\* ADDITIONAL PART NUMBERING SEQUENCE FOR NON DEFAULT SETTING\*\*\*

**=** DISPLAY MODE

Default = Positive image (no letter), or if more Description follows, use "-"

N = Negative image

**=** VIEWING DIRECTION

DEFAULT = 6 O'CLOCK (NO NUMBER)

1 = 12 O'CLOCK

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1	Part Numbering ~ LCM ~ Character and LCD Modules ~ Graphi	c
	Fait Numbering * Low * Character and Lob Modules * Grapin	U

				A			(	B		0	0	<b>(3</b>	<b>(3</b>	G
L	С	M	-	<u>s</u>	0	1	6	0	2	D	T	F		1
L Q U I D	C R Y S T A L	M O D U L E		O T P E M R P A E R I N U R E		M O D E L	N U M B E R			D I S P L A Y T Y P	F L U I D T Y P E	P O L A R I Z E R	D I S P L A Y M O D	>
										E			E	1 (

A= OPERATING TEMPERATURE

S = Standard (0 $^{\circ}$  to 50 $^{\circ}$  C) H = High Temp (-20 $^{\circ}$  to 70 $^{\circ}$  C)

B= MODEL NUMBER

FOR: DOT MATRIX AND GRAPHIC MODEL 01602 = 16 Character x 2 Lines 12864 = 128 Columns x 64 Rows

FOR: ALPHANUMERIC AND NUMERIC 401C40 = 4 Character x 1 line, 0.40" 4x1C45 = 4.5 Character x 1 line, 0.45" x - represents 1/2 column

C= DISPLAY TYPE

D = Dot Matrix G = Graphic

M = Custom

D= FLUID TYPE

T = Twisted Nematic (TN) S = Super Twisted Nematic (STN)

W = Film Compensated (FSTN)

**=** POLARIZER MODE

R = Reflective F = Transflective M = Transmissive

\*\*\* ADDITIONAL PART NUMBERING SEQUENCE FOR NON DEFAULT SETTING\*\*\*

DISPLAY MODE

Default = Positive Image (NO Letter), or if more Description follows use "-" N = Negative Image

G= VIEWING DIRECTION

DEFAULT = 6 O'CLOCK (NO NUMBER) 1 = 12 O'CLOCK



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#### **LCD Panels ~ Custom Design Guidelines**

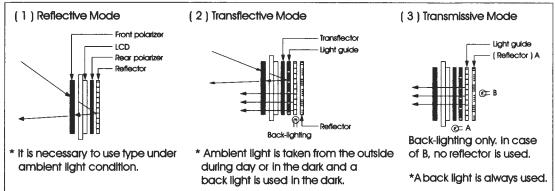
#### **Lighting and Mounting Methods**

### Types of Display

Positive Type

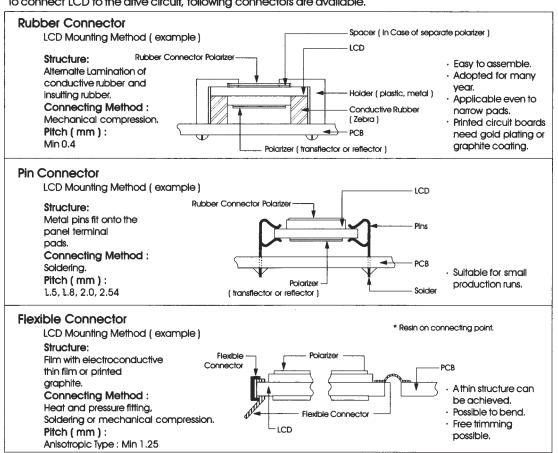


## **Lighting Methods**



#### Connector and LCD Mounting Method

To connect LCD to the drive circuit, following connectors are available.



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## **LCD Panels ~ Custom Design Guidelines**

## **Reliability Test**

Item	Temp	erature	Test Condition	Judgement
Storage temperature upper spec. limit lower spec. limit	Special 100°C -40°C	Normal 70°C -10°C		
Operation temperature upper spec. limit lower spec. limit	85°C -40°C	50°C 0°C		
Low temperature / test			Lower spec. limit of storage temp. for 4hr	LCD can be used normally
High temperature / test High temp & humidity			Upper spec. limit of operation temp. 60~65°C, 90~95% RH 240HR	LCD can be used normally LCD can be used normally after test
Temperature Cycling Test	100°C -40°C	70°C -10°C	LSL of storage temp. for 30 min 1°C/1 min increase to USL of storage temp. for 30 min 1°C/1 min decrease to LSL of storage temp. for 30 min 5 cycle	LCD can be used normally after test
Thermal shock upper spec. limit lower spec. limit	100°C -40°C	70°C -10°C	LSL of storage temp. for 30 min  USL of storage temp. within 10s  USL of storage temp. for 30 min  Reach LSL of storage temp. within 10s 5 cycle	LCD can be used normally after test

## **Typical Operating Characteristics**

Classifi	cation	Driv	ve Duty	Sta	tic	1/2 [	Outy	1/3 [	Outy	1/4 Duty	1/8 Duty	1/16 Duty	1/32 Duty	1/64 Duty	1/80 Duty
	ification	<u>lem</u>	O. Grade	Commercial	High Temp.	Commercial	High Temp.	Commercial	High Temp.	Commercial	Commercial	Commercial	Commercial	Commercial	Commercial
Ор	erating Volt		Vop	3.0	5.0	3.0	5.0	3.0	5.0	3.0	4.5	5.0	8.0	10.0	13.0
Ope	rating Frequ	ency	Hz	32-100	32-100	32-100	32-100	32-100	32-100	32-100	32-100	32-100	32-100	32-100	32-100
Pow	er Consump	tion	nA/mm²	5	10	5	10	5	10	5	5	5	5	5	5
	Capacitance		PF/mm²	18	18	18	18	18	18	18	18	18	18	18	18
	Turn on	0℃	ms	300	150	300	150	300	150	300	300	300	300	300	300
Time	Time	25°C	ms	100	50	100	50	100	50	100	100	100	100	100	100
Response Time	Turn off	0°C	ms	350	200	350	200	350	200	350	350	350	350	350	350
Res	Time	25°C	ms	150	100	150	100	150	100	150	150	150	150	150	150
ing	Vertical		degree	+15 -30	+10 -30	+0 -30	+0 -30	+0 -30	+0 -30						
Viewing Angle	Horizontal		degree	±45	±45	±45	±45	±45	±45	±45	±45	±45	±45	±45	±45
Ot	perating Ten	np.	℃	-5~60	-20~80	-5~60	-20~80	-5~60	-20~80	-5~60	-5~50	-0~50	-5~50	-5~50	-0~50
S	torage Temp	).	℃	-20~80	-40~85	-20~80	-40~85	-20~80	-40~85	-20~80	-20~80	-20~80	-20~80	-20~80	-20~80
	Contrast Rati	0		20:1	20:1	20:1	20:1	20:1	20:1	20:1	20:1	10:1	10:1	10:1	10:1

Note: Data shown above can be tailored to customer specification.



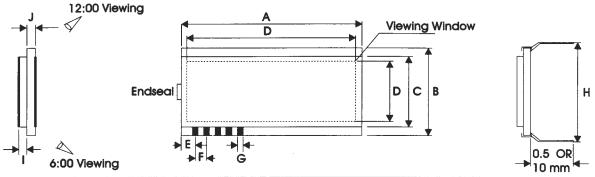
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## **Custom Display Guidelines**



DESCRIPTION	DIMENSION	DESIGN PARAMETER				
A. Overall glass length		Optimum length 6.8", 4.5", 3.4", 2.7", 2.25", 1.94"				
B. Overall glass width		Optimum width 3.3", 2.2", 1.65", 1.1"				
C. Back plane glass width (smaller glass plate)		When determining back plane glass dimension, always allow a minimum of 0.10" on each side of the contact ledge for a pin type display and 0.075" or more for elastomeric configuration.				
	Height	Viewing window should be located a minimum of 0.05" away				
D. Viewing window	Width	from the image area and 0.10" inside of the back plane glass.				
E. Glass edge to centre of 1st contact pad		Avoid placing contact pad at glass corners.				
F. Pitch of contacts		0.10" for standard pin package.				
G. Contact pad width		Pad width should approximately equal pad spacing.				
I. Back glass thickness						
J. Front glass thickness						

#### **OPERATIONAL SPECIFICATIONS**

Viewing angle:		of sight to the display. Th	ne viewing angle is the ang	Viewing angle is determined by gle at which maximum contrast off angle but not at the perpend	is achieved. Keep in
2. Operating temp:	°C to	°c			
3. Storage temp:	°C to	°C			
4. Drive method:	Static / Mux				
5. Drive voltage:					
6. Number of:	A) digit	B) 14 or 16 Alphanumeric	C) Dot matrix char	D) Dot matrix graphic	E) Others (symbols)
7. Viewing mode:	Reflective / tra	nsflective / Transmissive			

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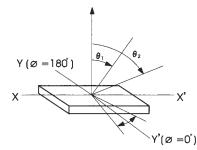


#### **TN type ~ Twisted Nematic LCD Modules** Item Symbol Min. Тур. Max. Unit Condition Note $\theta_2 - \theta_1$ Viewing 40 Cr = 2.01,2 deg. Angle Ø $\theta = 20^{\circ}$ Contrast 3 Cr4 Ø= Ratio 0° $\theta = 20^{\circ}$ Response 110 Tr ms 4 Time (rise) $\emptyset = 0^{\circ}$ $= 20^{\circ}$ Response Tf 110 ms 4 Time (fall) $\varnothing = 0^{\circ}$

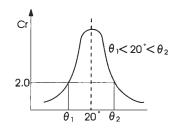
				51	и туре ~	Super iwisted	Nematic
Item	Symbol	Min.	Тур.	Max.	Unit	Condition	Note
Viewing Angle	$\frac{\theta_2 - \theta_1}{\varnothing}$	70 -90		+90	deg.	Cr = 2.0	1,2
Contrast Ratio	Cr	_	4	_	_	$\theta = 20^{\circ}$ $\emptyset = 0^{\circ}$	3
Response Time ( rise )	Tr	_	110	_	ms	$\theta = 20^{\circ}$ $\emptyset = 0^{\circ}$	4
Response Time ( fall )	Tf	_	110	_	ms	$\theta = 20^{\circ}$ $\emptyset = 0^{\circ}$	4

#### **Definitions**

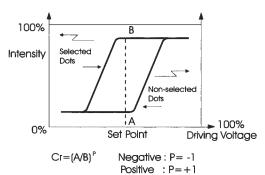
1 - Definition of angle  $\,\theta\,$  &  $\,\varnothing\,$ 



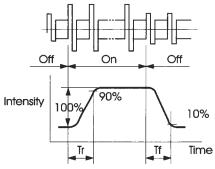
2 - Definition of viewing angle  $\theta_1 \& \varnothing_2$ 







4 - Definition of Optical Response

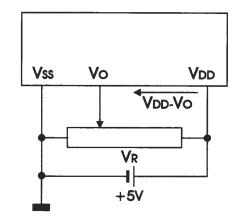


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## **LCD Modules ~ Power Supply Schematics**

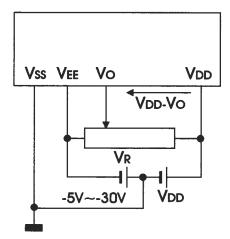
## **Single Source**



VDD-VO: LCD DRIMNG VOLTAGE

VR: 10k-20k

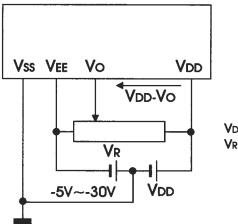
#### **Double Source**



VDD-VO: LCD DRIMNG VOLTAGE

VR: 10k-20k

## **Negative Power Supply Included**



VDD-VO: LCD DRIVING VOLTAGE

VR: 10k-20k

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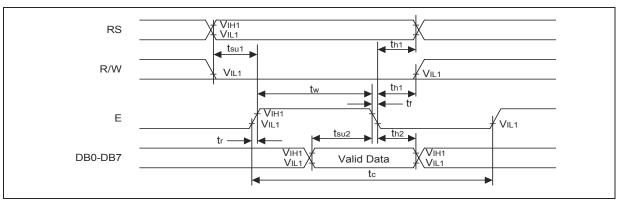
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## **LCD Modules ~ Timing Characteristics**

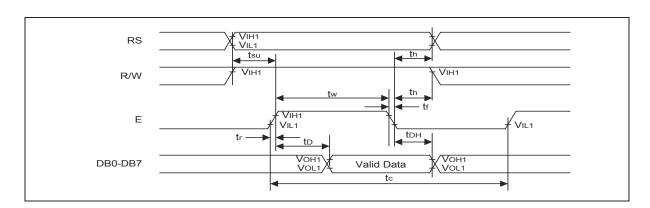
## Writing Data from MPU to Controller



 $(V_{DD} = 4.5 \text{ to } 5.5 \text{V}, \text{ Ta} = -30 \text{ to } +85^{\circ} \text{ C})$ 

Mode	Characteristics	Symbol	Min	Тур	Max	Unit
	E Cycle Time	tc	500	-	-	
	E Rise / Fall Time	$t_R$ , $t_F$	-	-	20	
NAZZI o NA o do	E Pulse Width (High, Low)	tw	230	-		
Write Mode	R/W and RS Setup Time	tsu1	40	1	-	ns
	R/W and RS Hold Time	t <sub>H1</sub>	10	-	-	
	Data Setup Time	tsu2	80	-	-	
	Data Hold Time	t <sub>H2</sub>	10	ı	-	

## **Reading Out Data from MPU to Controller**



 $(V_{DD} = 4.5 \text{ to } 5.5 \text{V}, \text{ Ta} = -30 \text{ to } +85^{\circ} \text{ C})$ 

Mode	Characteristics	Symbol	Min	Тур	Max	Unit
	E Cycle Time	tc	500	-	-	
	E Rise / Fall Time	t <sub>R</sub> , t <sub>F</sub>	-	-	20	
Read Mode	E Pulse Width (High, Low)	tw	230	-	-	
Read Mode	R/W and RS Setup Time	tsu	40	-	-	ns
	R/W and RS Hold Time	t <sub>H</sub>	10	-	-	
	Data Output Delay Time	tD	-	-	120	
	Data Hold Time	tDH	5	-	-	

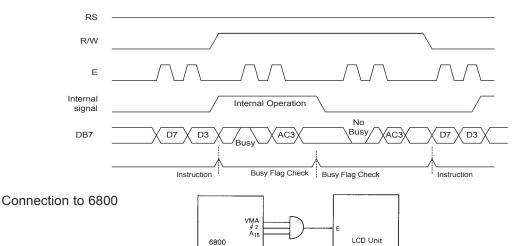




#### LCD Modules ~ MPU Interface

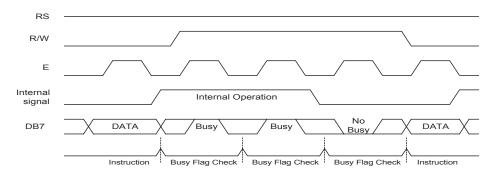
#### Interface to 4-Bit MPU

When interfacing data length is 4-bit, only 4 ports, from DB4 to DB7, are used as data bus. At first higher 4-bit (in case of 8-bit bus mode, the contents of DB4 - DB7) are transferred, and then lower 4-bit (in case of 8-bit bus mode, the contents of DB0 - DB3) are transferred. So transfer is performed by two times. Busy Flag outputs "High" after the second transfer are ended.

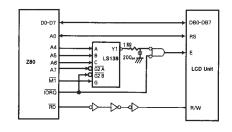


#### Interface to 8-Bit MPU

When interfacing data length is 8-bit, transfer is performed at a time through 8 ports, from DB0 to DB7.



Connection to Z80



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8- Bit Interface

R/W

0

R/W

0

R/W

0

R/W

0

0

RS

0

RS

0

RS

0

DB7

0

DB7

0

DB7

0

DB7

0

DB6

0

DB6

0

DB6 DB5

0

0

DB6

0

0

1

0

1

0

1

D/L

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LCD Modules ~ Initialization Instructions

Power On

Wait for more than 30ms after VDD rises to 4.5 V.

Wait for more than 40ms after VDD rises to 2.7 V.

**Function Set** 

Wait for more than 39µs

Display ON/OFF Control

Wait for more than 39µs

Display Clear

Wait for more than 1.53ms

Entry Mode Set

DB5 DB4

Initialization End

0

DB4

DB4

DB3

DB3

DB3

0

4-bit mode

8-bit mode

1-line mode

2-line mode

Display off

Display on

DB5

DB4

1

DB2

DB2

DB2

DB2

1

DB1

Х

DB1

С

DB1

0

DB1

I/D

DB0

Х

DB0

В

DB0

DB0

SH

DB5

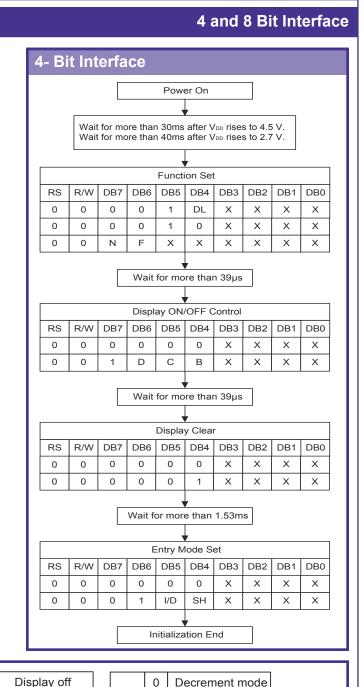




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- 1	D		-1 7 -	I/D	~			
		1	Display on	1/0	1	Increment mode		
	С	0	Cursor off	SH	0	Entire shift off		
	C	1	Cursor on		1	Entire shift on		
	В	0	Blink off					
	ъ	1	Blink on					





#### **LCD Modules ~ Initialization Instructions**

## **Instruction Codes**

lo store di co				Ins	truct	ion C	Code	Description	Execution time				
Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Instruction Code	(fsoc=270kHz)	
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC.	1.53ms	
Return Home	0	0	0	0	0	0	0	0	1	x	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.53ms	
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	SH	Assign cursor moving direction and make shift of entire display enable.	39µs	
Display ON/OFF Control	0	0	0	0	0	0	1	D	С	В	Set display(D), cursor(C), and blinking of cursor(B) on/off control bit.	39µs	
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	Х	Х	Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.	39µs	
Function Set	0	0	0	0	1	DL	N	F	x	x	Set interface data length (DL : 4-bit/8-bit), numbers of display line (N : 1-line/2-line), display font type(F : 5 X 8 dots/ 5 X 11 dots)	39µs	
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	39µs	
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter.	39µs	
Read Busy Flag and Address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0µs	
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	43µs	
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	43µs	

NOTE: When an MPU program with checking the Busy Flag (DB7) is made, it must be necessary 1/2 fosc is necessary for executing the next instruction by the falling edge of the 'E' signal after the Busy Flag (DB7) goes to "LOW".

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## **LCD Modules ~ Codes and Patterns**

Higher 4 Bits Lower 4 Bits	0000 (NOTE 1)	0010	0011	0100	0101	0110	0111	1010	1011	1100	1101	1110 (NOTE 1)	1111 (NOTE 1)	0001 (NOTE 2)
x x x x 0000	CG RAM (1)					*•	1			• • • • • • • • • • • • • • • • • • • •	***	Ø		
X X X X 0001	(2)								1.	****	<u></u>			
X X X X 0010	(3)	11				i	<b>!</b>	-	-1	ij	×			
X X X X 0011	(4)	#			*****								424	
X X X X 0100	(5)					!		••.						
X X X X 0101	(6)	:::	*	1000			1	==						
X X X X 0110	(7)				!			*****		***				
XXXXOIII	(8)	:	***************************************				i,,i				*****			
X X X X 1000	(1)						×				ļ.			
X X X X 1001	(2)											!	•	
X X X X 1010	(3)	*	**							•				
X X X X 1011	(4)		:				•		***************************************			**		
X X X X1100	(5)								··i	*****		-		
XXXXIIOI	(6)		*****					•••		•••				
XXXXIIIO	(7)	==			"						•."-			
xxxxiiii	(8)				*****	::		•	<b>.</b> .!		2.1			

\* NOTE 1 : FOR CHARACTER TYPE ONLY \* NOTE 2 : FOR GRAPHIC TYPE ONLY

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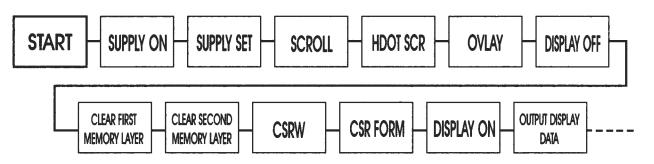
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#### **Initialization Example**

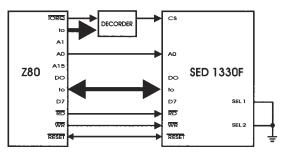
The Initialization example shown in figure is for a SED 1330F with an 8-bit microprocessor interface bus an Epson EG 4810-AR Display Unit(512x128 Pixels)



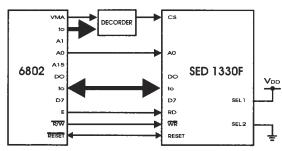
**INITIALIZATION PROCEDURE** 

#### **Interface to MPU**

#### Z80 TO SED 1330F INTERFACE



#### 6802 TO SED 1330F INTERFACE



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<b>LCD Modules</b>	~ Graphic
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#### Instructions

Class							CC		Command Description	Number of					
Ciaco		RD	WR	AO	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Command Description	Read Bytes
System	SYSTEM SET	1	0	1	0	1	0	0	0	0	0	0	40	Initialize device and display	8
Control	SLEEP IN	1	0	1	0	1	0	1	0	0	1	1	53	Enter standby mode	0
	DISPLAY ON/OFF	1	0	1	0	1	0	1	1	0	0	D	58 59	Enable and disable display and display flashing(D=0:Display OFF, D=1: Display ON)	1
	SCROLL	1	0	1	0	1	0	0	0	1	0	0	44	Set display start address and display regions	10
	CSRFORM	1	0	1	0	1	0	1	1	1	0	1	5D	Set cursor type	2
Display Control	CGRAM ADR	1	0	1	0	1	0	1	1	1	0	0	5C	Set start address of character generator RAM	2
	CSRDIR	1	0	1	0	1	0	0	1	1	C2	C1	4C to 4F	C         C2         C1         Shift Direction           4CH         0         0         Right           4DH         0         1         Left           4EH         1         0         Up           4FH         1         1         Down	0
	HDOT SCR	1	0	1	0	1	0	1	1	0	1	0	5A	Set horizontal scroll position	1
	OVLAY	1	0	1	0	1	0	1	1	0	1	1	5B	Set display overlay format	1
Drawing	CSRW	1	0	1	0	1	0	0	0	1	1	0	46	Set cursor address	2
Control	CSRR	1	0	1	0	1	0	0	0	1	1	1	47	Read cursor address	2
Memory	MWRITE	1	0	1	0	1	0	0	0	0	1	0	42	Write to display memory	-
Control	MREAD	1	0	1	0	1	0	0	0	0	1	1	43	Read from display memory	-

#### [Notes]

1. In general, the internal registers of the SED1330F are modified as each command parameter is input.

However, the microprocessor does not have to set all the parameters of a command and may send a new command before all parameters have been input. The internal registers for the parameters that have been input will have been changed but the remaining parameter registers are unchanged.

2byte parameters (where two bytes are treated as 1 data item) are handled as follows:

a. CSRW, CSRR:Each byte is processed individually. The microprocessor may read or write just the low byte of the cursor address. b. SYSTEM SET, SCROLL, CGRAM ADR:Both parameter bytes are processed together.

If the command is changed after half of the parameter has been input, the single byte is ignored.

