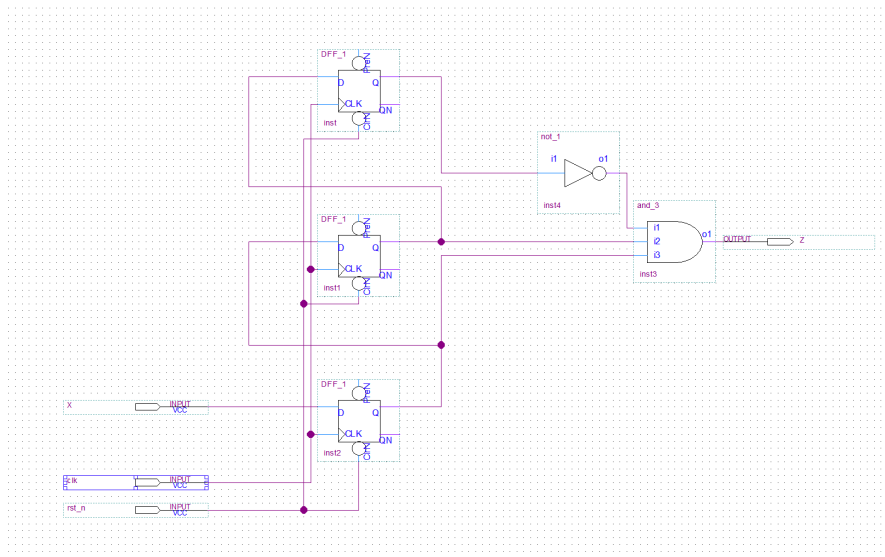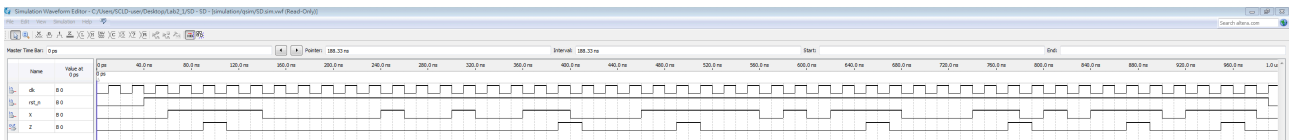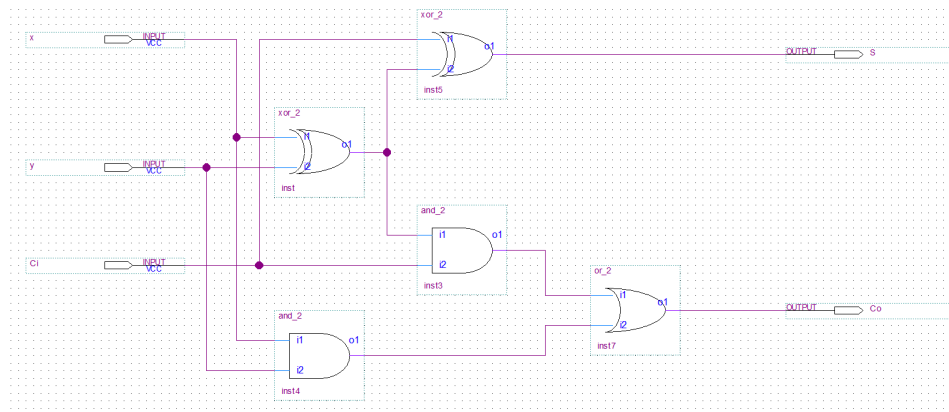1. Sequence Detector:

SD:

SD waveform:
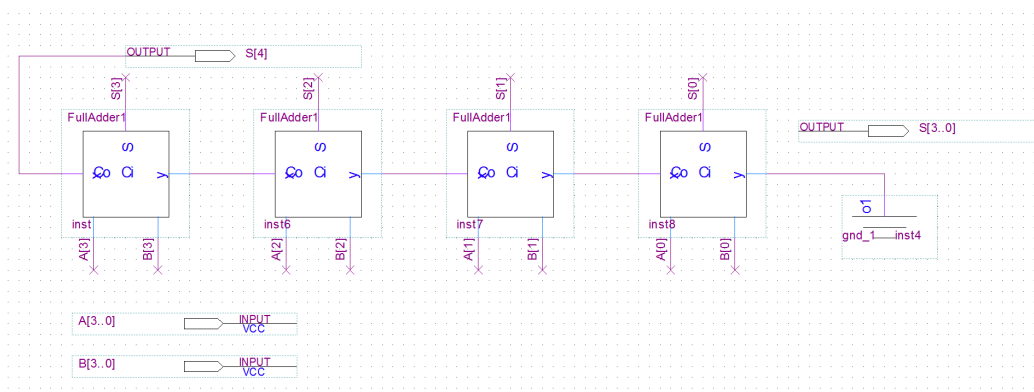

2. Accumulator:

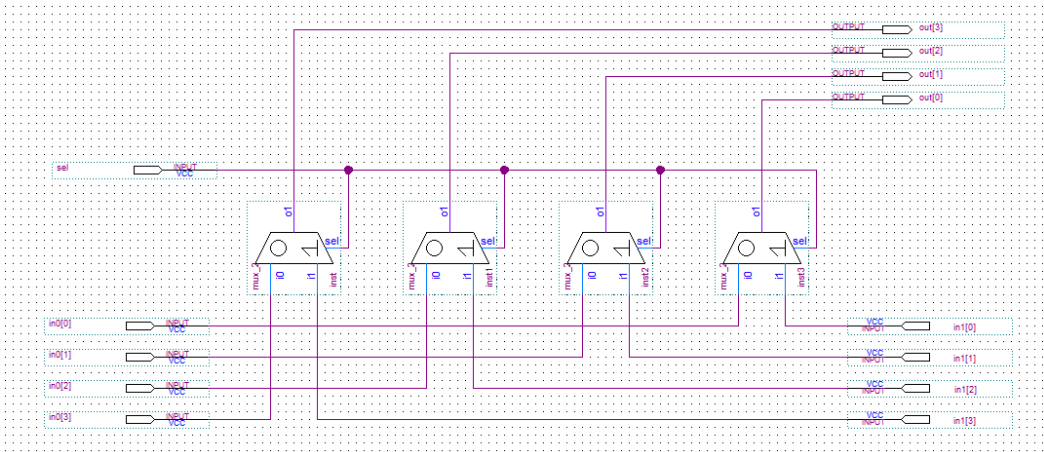FA1:

FA4:
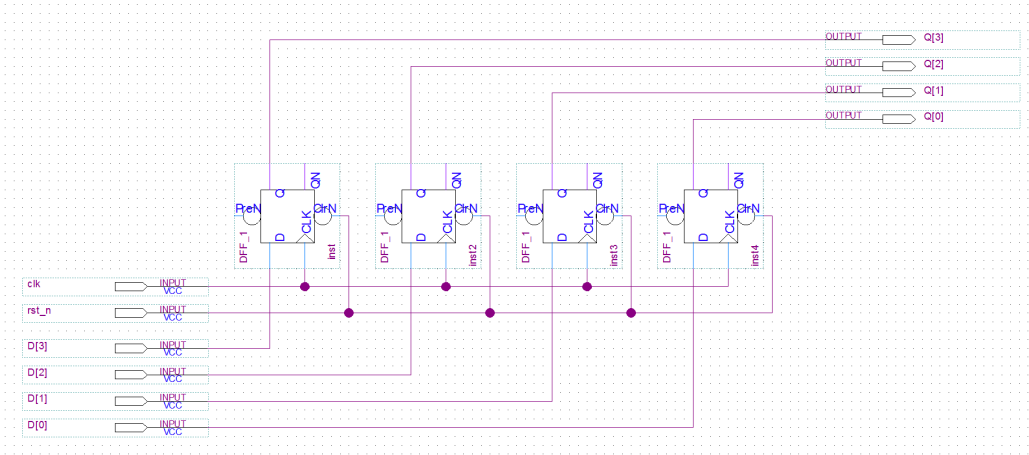
MUX4:

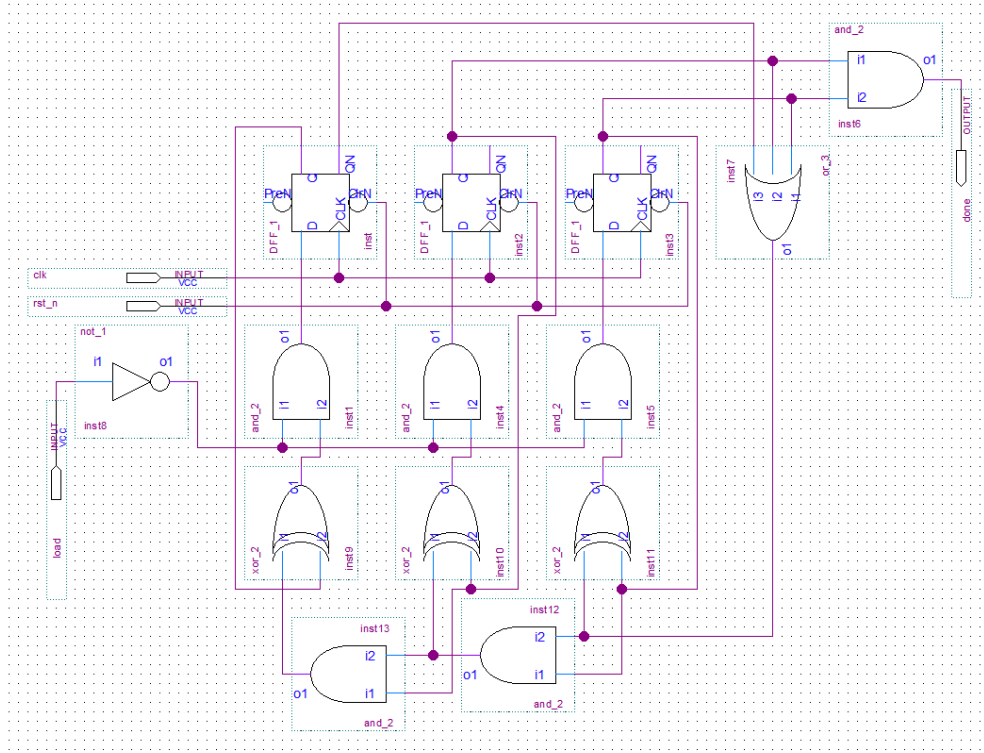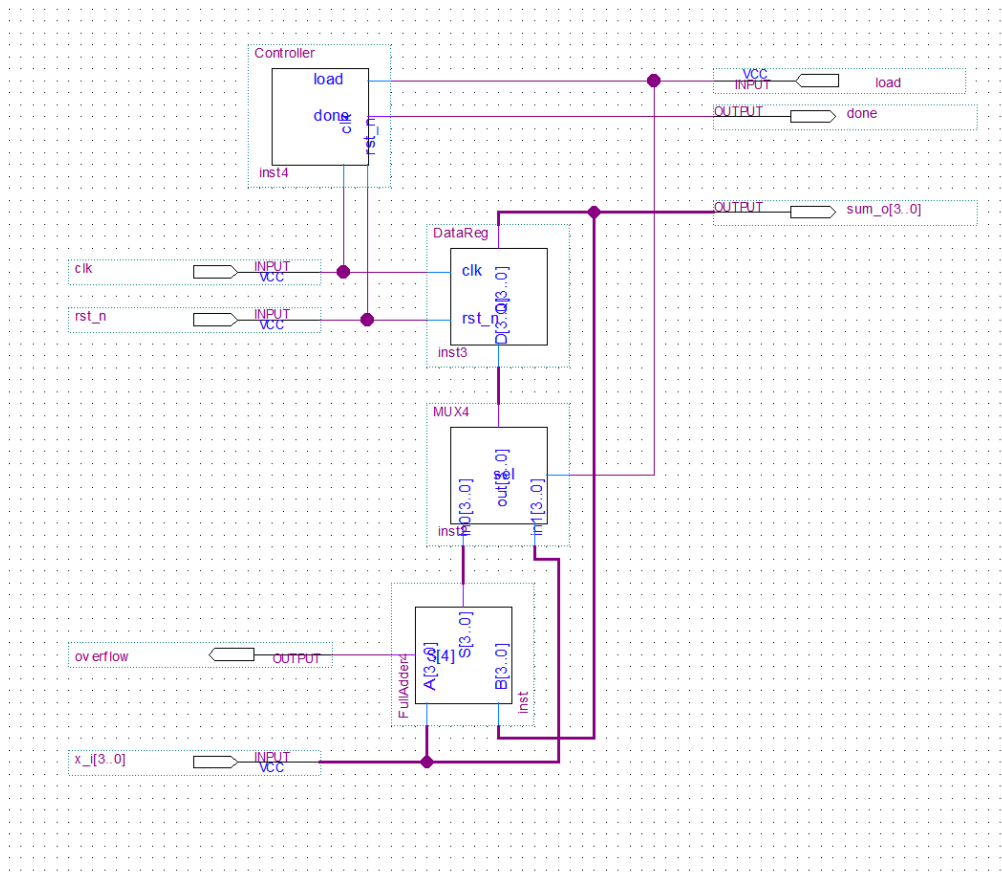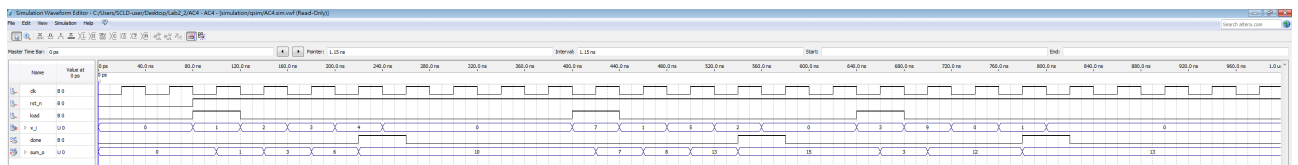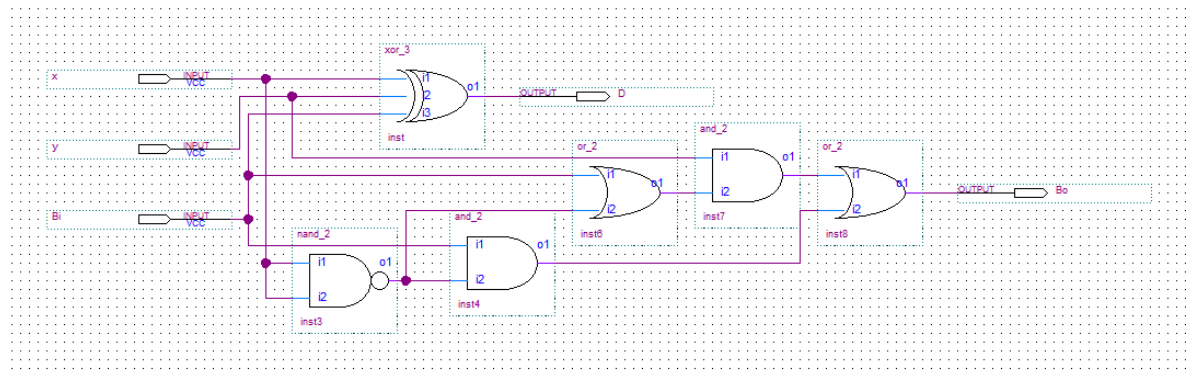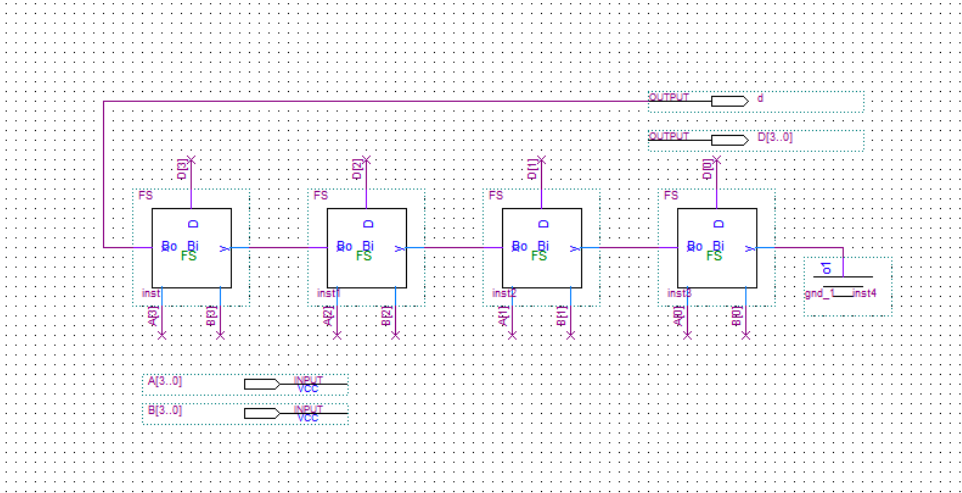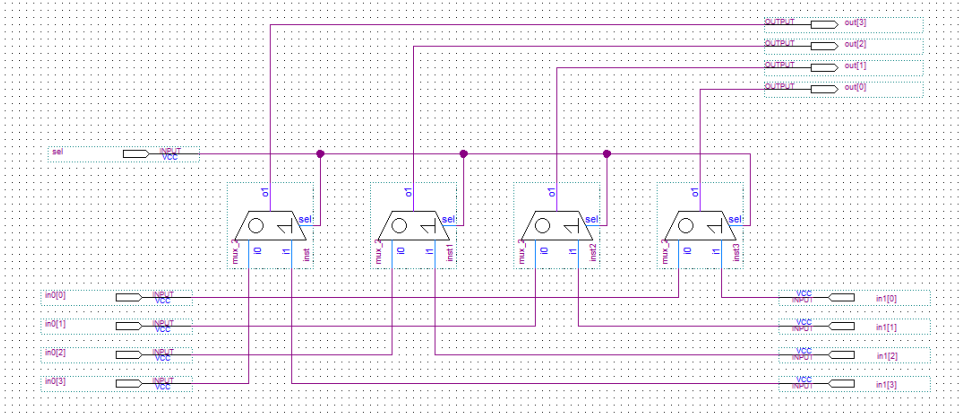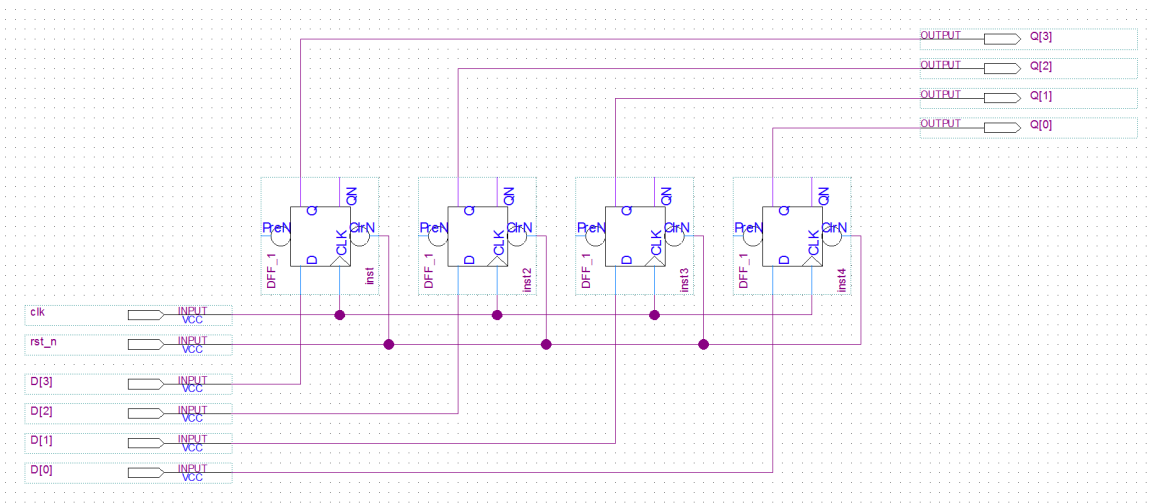## DataReg:

Controller(AC4):

AC4:

AC4 waveform:

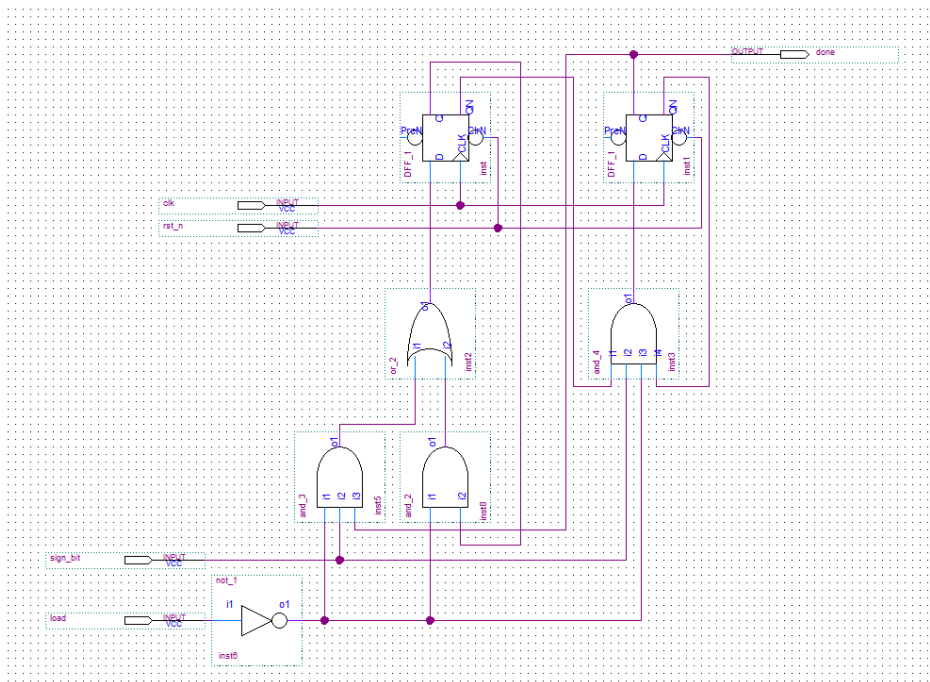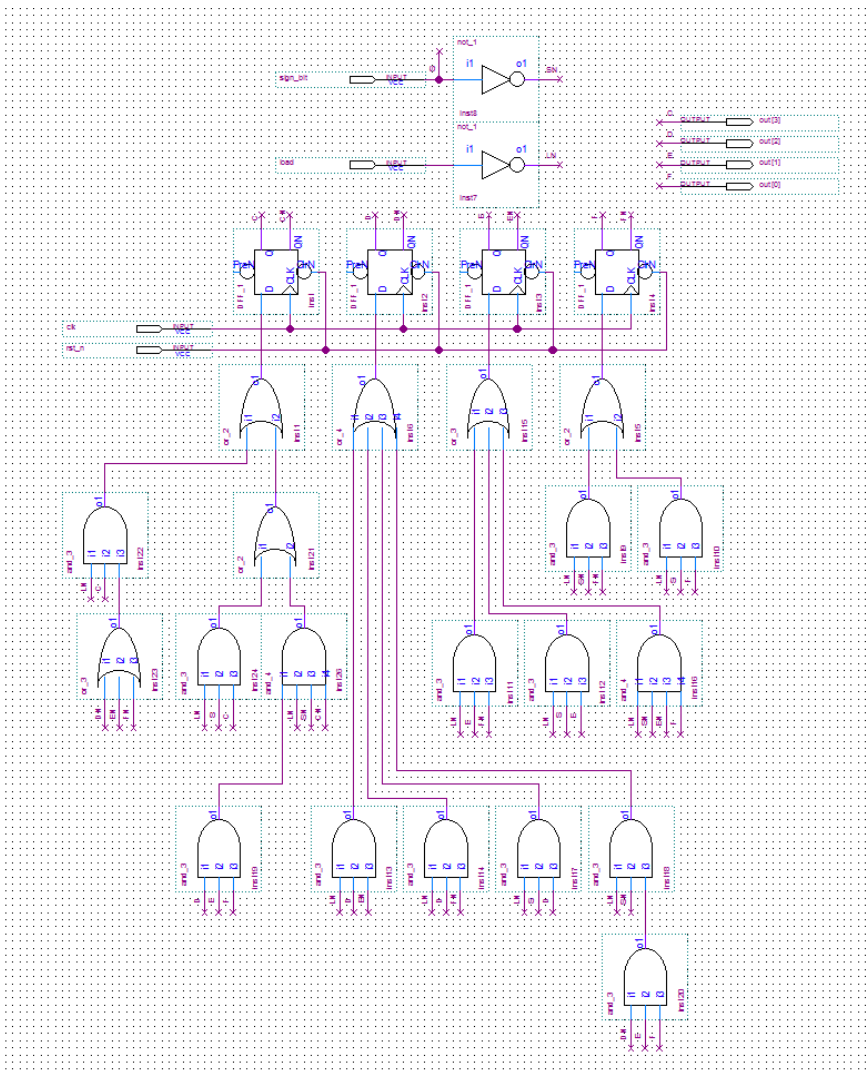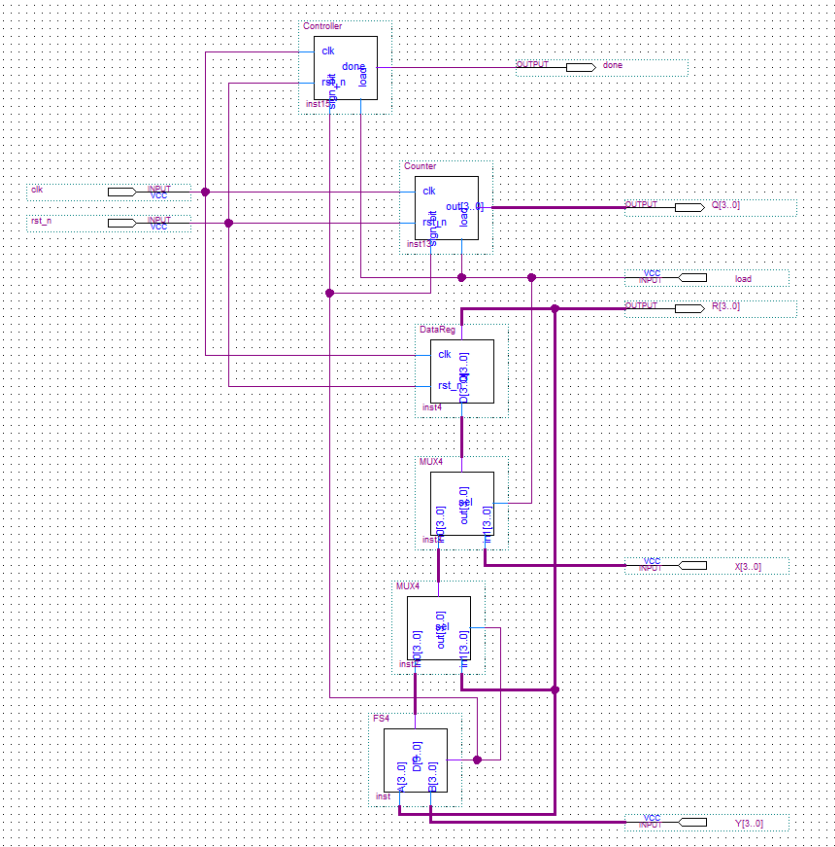3. Divider:

FS1:

FS4:

MUX4:

DataReg:

Controller(DV4):

Counter:

DV4:

DV4 waveform: