## **MULLW**

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MULLW		Multiply Literal with W			
Synt	ax:	[ label ]	MULLW k	(	
Operands:		$0 \le k \le 25$	$0 \le k \le 255$		
Operation:		$(W) \times k \to PRODH : PRODL$			
Status Affected:		None			
Enco	oding:	0000	1101 k)	kkk kkkk	
Description:		ried out be W and the 16-bit rese PRODH: PRODH of W is unch None of the affected. Note that carry is po-	An unsigned multiplication is carried out between the contents of W and the 8-bit literal 'k'. The 16-bit result is placed in PRODH:PRODL register pair. PRODH contains the high byte. W is unchanged. None of the status flags are affected. Note that neither overflow nor carry is possible in this operation. A zero result is possible but not detected.		
Words:		1			
Cycles:		1	1		
Q Cycle Activity:					
	Q1	Q2	Q3	Q4	
	Decode	Read literal 'k'	Process Data	Write registers PRODH: PRODL	

Example: MULLW 0xC4

## Before Instruction

 $\begin{array}{lll} W & = & 0xE2 \\ PRODH & = & ? \\ PRODL & = & ? \end{array}$ 

## After Instruction

 $\begin{array}{lll} W & = & 0xE2 \\ PRODH & = & 0xAD \\ PRODL & = & 0x08 \end{array}$ 

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