BRA

< Previous instruction: BOV | Instruction index | Next instruction: BSF >

BRA	Unconditional Branch

Syntax:	[*label*] BRA n
Operands:	$-1024 \leq n \leq 1023$
Operation:	$(PC) + 2 + 2n \rightarrow PC$
Status Affected:	None

Encoding:

1101	0nnn	nnnn	nnnn

Description: Add the 2's complement number '2n' to the PC. Since the PC will have incremented to fetch the next instruction, the new address will be PC+2+2n. This instruction is a two-cycle instruction.

Words: 1

Cycles: 2

Q Cycle Activity:

Q1	Q2	Q3	Q4
Decode	Read literal 'n'	Process Data	Write to PC
No operation	No operation	No operation	No operation

Example: HERE   BRA   Jump

Before Instruction
    PC   =   address (HERE)
After Instruction
    PC   =   address (Jump)

< Previous instruction: BOV | Instruction index | Next instruction: BSF >