

RETLW

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RETLW	Return Literal to W				
Syntax:	[<i>label</i>] RETLW k				
Operands:	$0 \leq k \leq 255$				
Operation:	$k \rightarrow W$, (TOS) \rightarrow PC, PCLATU, PCLATH are unchanged				
Status Affected:	None				
Encoding:	<table><tr><td>0000</td><td>1100</td><td>kkkk</td><td>kkkk</td></tr></table>	0000	1100	kkkk	kkkk
0000	1100	kkkk	kkkk		
Description:	W is loaded with the eight-bit literal 'k'. The program counter is loaded from the top of the stack (the return address). The high address latch (PCLATH) remains unchanged.				
Words:	1				
Cycles:	2				
Q Cycle Activity:					

Q1	Q2	Q3	Q4
Decode	Read literal 'k'	Process Data	pop PC from stack, Write to W
No operation	No operation	No operation	No operation

Example:

```
CALL TABLE ; W contains table
              ; offset value
              ; W now has
              ; table value
:
TABLE
  ADDWF PCL ; W = offset
  RETLW k0 ; Begin table
  RETLW k1 ;
:
:
  RETLW kn ; End of table
```

Before Instruction

W = 0x07

After Instruction

W = value of kn

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