

LFSR

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LFSR	Load FSR												
Syntax:	[<i>label</i>] LFSR f,k												
Operands:	$0 \leq f \leq 2$ $0 \leq k \leq 4095$												
Operation:	$k \rightarrow \text{FSRf}$												
Status Affected:	None												
Encoding:	<table><tr><td>1110</td><td>1110</td><td>00ff</td><td>k₁₁kkk</td></tr><tr><td>1111</td><td>0000</td><td>k₇kkk</td><td>kkkk</td></tr></table>	1110	1110	00ff	k ₁₁ kkk	1111	0000	k ₇ kkk	kkkk				
1110	1110	00ff	k ₁₁ kkk										
1111	0000	k ₇ kkk	kkkk										
Description:	The 12-bit literal 'k' is loaded into the file select register pointed to by 'f'.												
Words:	2												
Cycles:	2												
Q Cycle Activity:	<table><tr><th>Q1</th><th>Q2</th><th>Q3</th><th>Q4</th></tr><tr><td>Decode</td><td>Read literal 'k' MSB</td><td>Process Data</td><td>Write literal 'k' MSB to FSRfH</td></tr><tr><td>Decode</td><td>Read literal 'k' LSB</td><td>Process Data</td><td>Write literal 'k' to FSRfL</td></tr></table>	Q1	Q2	Q3	Q4	Decode	Read literal 'k' MSB	Process Data	Write literal 'k' MSB to FSRfH	Decode	Read literal 'k' LSB	Process Data	Write literal 'k' to FSRfL
Q1	Q2	Q3	Q4										
Decode	Read literal 'k' MSB	Process Data	Write literal 'k' MSB to FSRfH										
Decode	Read literal 'k' LSB	Process Data	Write literal 'k' to FSRfL										

Example: LFSR 2, 0x3AB

After Instruction

FSR2H = 0x03
FSR2L = 0xAB

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