# RLNCF

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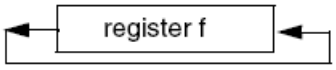
| RLNCF | Rotate Left f (no carry) |
|---|---|
| Syntax: | [ *label* ] RLNCF f [,d [,a] |
| Operands: | 0 ≤ f ≤ 255<br>d ∈ [0,1]<br>a ∈ [0,1] |
| Operation: | (f<n>) → dest<n+1>,<br>(f<7>) → dest<0> |
| Status Affected: | N, Z |

Encoding:

| | | | |
|---|---|---|---|
| 0100 | 01da | ffff | ffff |

Description: The contents of register 'f' are rotated one bit to the left. If 'd' is 0, the result is placed in W. If 'd' is 1, the result is stored back in register 'f' (default). If 'a' is 0, the Access Bank will be selected, overriding the BSR value. If 'a' is 1, then the bank will be selected as per the BSR value (default).




Words: 1

Cycles: 1

Q Cycle Activity:

| Q1 | Q2 | Q3 | Q4 |
|---|---|---|---|
| Decode | Read register 'f' | Process Data | Write to destination |

Example:

```
RLNCF    REG, 1, 0
```

Before Instruction

```
REG  =  1010 1011
```

After Instruction

```
REG  =  0101 0111
```

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