RETFIE

< Previous instruction: RESET | Instruction index | Next instruction: RETLW >

RETFIE		Return from Interrupt					
Synt	tax:	[label]	RETFIE	[s]			
Ope	rands:	$s \in [0,1]$					
Ope	ration:	$\begin{array}{c} 1 \rightarrow \text{GIE/C} \\ \text{if s = 1} \\ \text{(WS)} \rightarrow \text{W} \\ \text{(STATUSS} \\ \text{(BSRS)} \rightarrow \end{array}$	$ \begin{split} &(TOS) \to PC, \\ &1 \to GIE/GIEH \text{ or } PEIE/GIEL, \\ &if \; s = 1 \\ &(WS) \to W, \\ &(STATUSS) \to STATUS, \\ &(BSRS) \to BSR, \\ &PCLATU, \; PCLATH \; are \; unchanged. \end{split} $				
Statı	us Affected:	GIE/GIEH	, PEIE/G	iIEL.			
Enco	oding:	0000	0000	0001	000s		
Des	cription:	Return fro popped ar loaded into enabled by or low price enable bits the shador STATUSS into their ow, STATUSS update of (default).	nd Top-of the PC y setting ority glob . If 's' = 1 w registe and BSI correspoi	f-Stack (. Interru either thal interru , the col ers WS, RS are I nding re SR. If 's	TOS) is pts are ne high upt ntents of oaded gisters, ' = 0, no		
Words:		1					
Cycles:		2					
Q Cycle Activity:							
	Q1	Q2	Q3		Q4		
	Decode	No	No	l	PC from		
		operation	operation	on	stack		

Q1	Q2	Q3	Q4	
Decode	No	No	pop PC from	
	operation	operation	stack	
			Set GIEH or	
			GIEL	
No	No	No	No	
operation	operation	operation	operation	

Example: RETFIE 1

After Interrupt

PC = TOS W = WS BSR = BSRS STATUS = STATUSS GIE/GIEH, PEIE/GIEL = 1

< Previous instruction: <u>RESET</u> | Instruction <u>index</u> | Next instruction: <u>RETLW</u> >