< Previous instruction: <u>BNOV</u> | Instruction <u>index</u> | Next instruction: <u>BOV</u> >

BNZ Branch if Not Zero

Syntax: [label] BNZ n

Operands: $-128 \le n \le 127$

Operation: if zero bit is '0'

 $(PC) + 2 + 2n \rightarrow PC$

Status Affected: None

Encoding: 1110 0001 nnnn nnnn

Description: If the Zero bit is '0', then the pro-

gram will branch.

The 2's complement number '2n' is added to the PC. Since the PC will have incremented to fetch the next instruction, the new address will be PC+2+2n. This instruction is then

a two-cycle instruction.

Words: 1

Cycles: 1(2)

Q Cycle Activity:

If Jump:

	Q1	Q2	Q3	Q4
Ī	Decode	Read literal	Process	Write to PC
		'n'	Data	
Γ	No	No	No	No
	operation	operation	operation	operation

If No Jump:

Q1	Q2	Q3	Q4
Decode	Read literal 'n'	Process Data	No operation

Example: HERE BNZ Jump

Before Instruction

PC = address (HERE)

After Instruction

If Zero = 0;

PC = address (Jump)

If Zero = 1;

PC = address (HERE+2)

< Previous instruction: <u>BNOV</u> | Instruction <u>index</u> | Next instruction: <u>BOV</u> >