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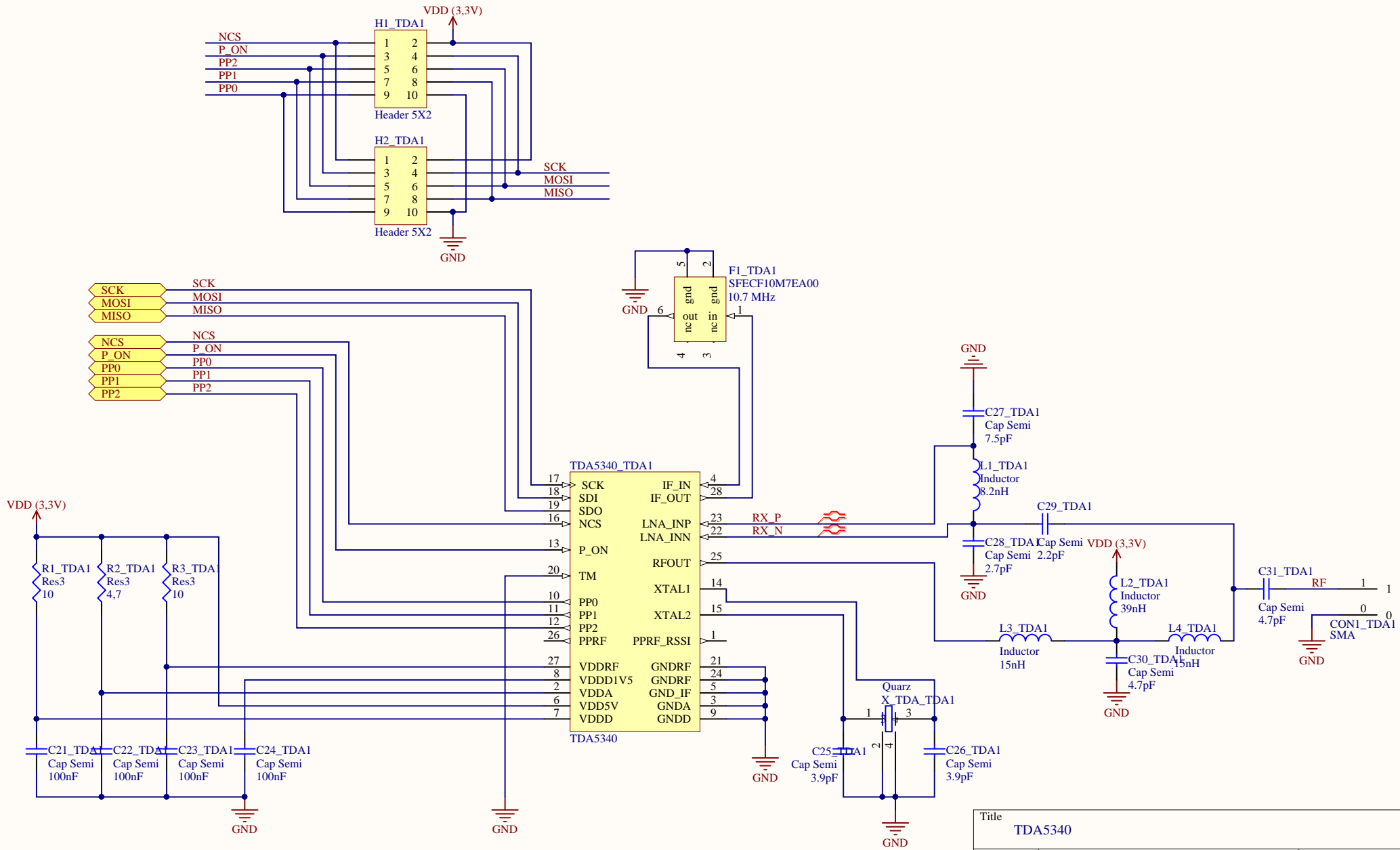
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Size	Number	Revision
A4		
Date:	05.09.2016	Sheet of
File:	D:\Bachelorarbeit\...\TDA5340.SchDoc	Drawn By: Christof Pfannenmüller

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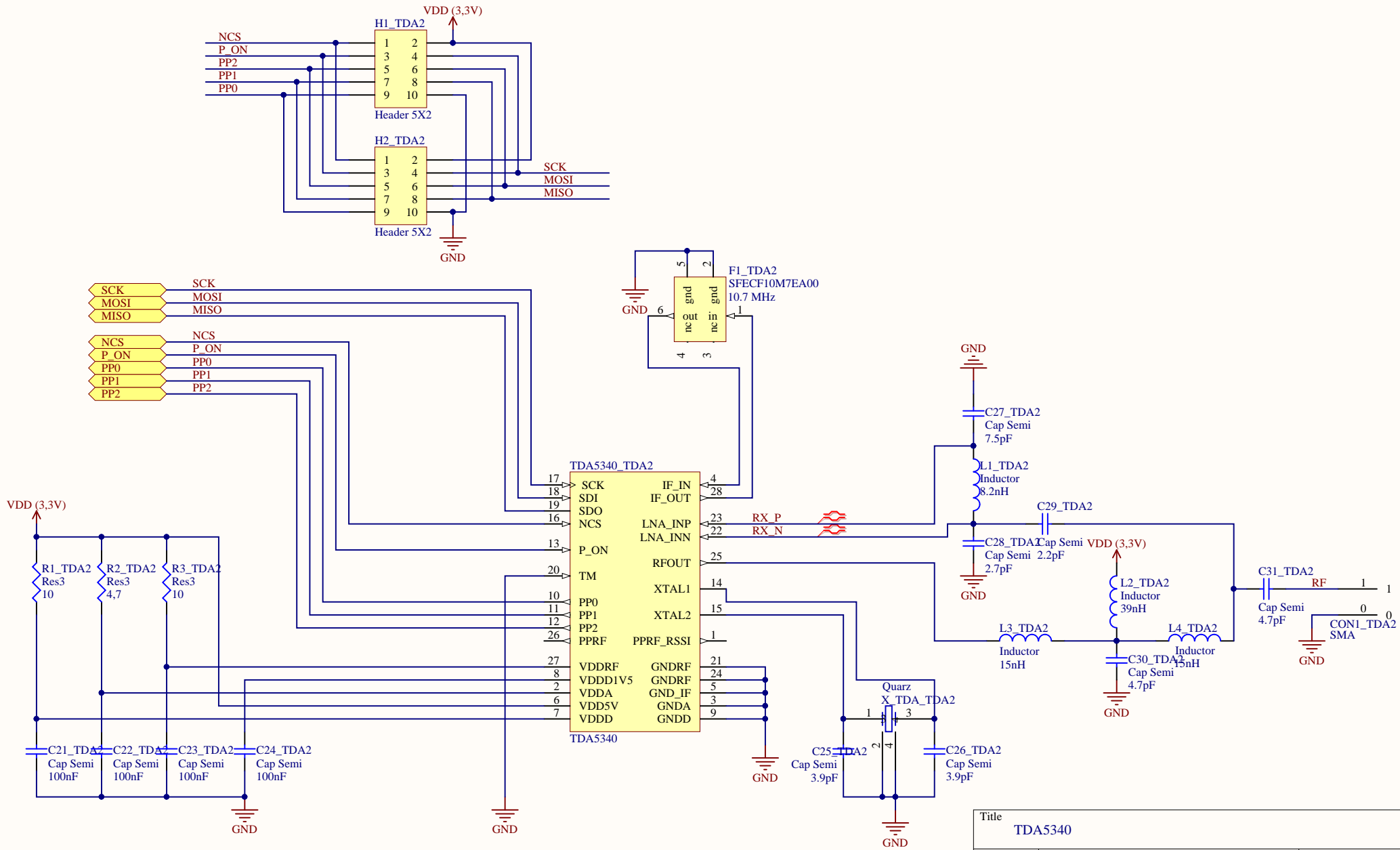
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Title		
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File:	D:\Bachelorarbeit\...\TDA5340.SchDoc	Drawn By: Christof Pfannenmüller

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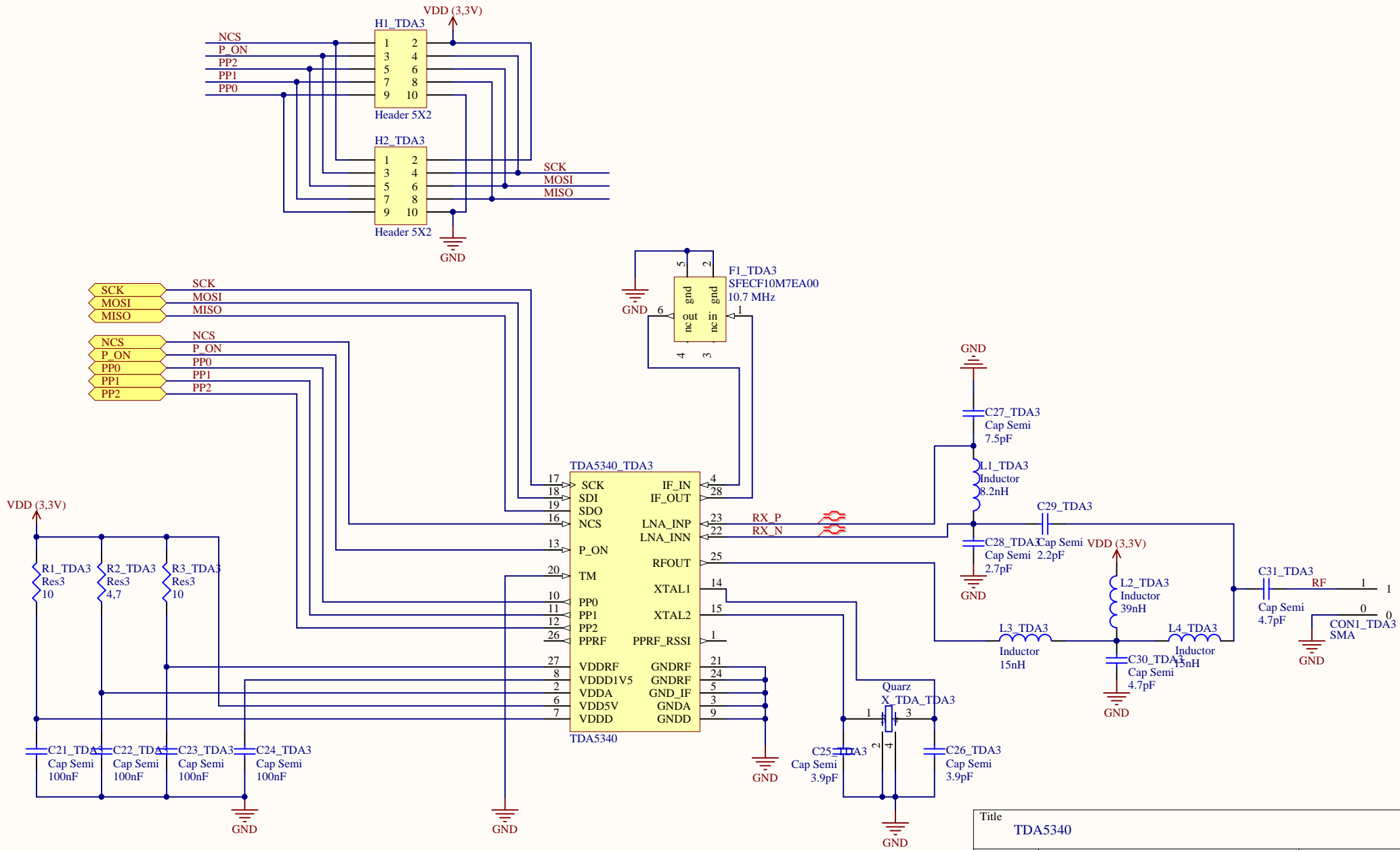
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Title		
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Size	Number	Revision
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Date:	05.09.2016	Sheet of
File:	D:\Bachelorarbeit\...\TDA5340.SchDoc	Drawn By: Christof Pfannenmüller

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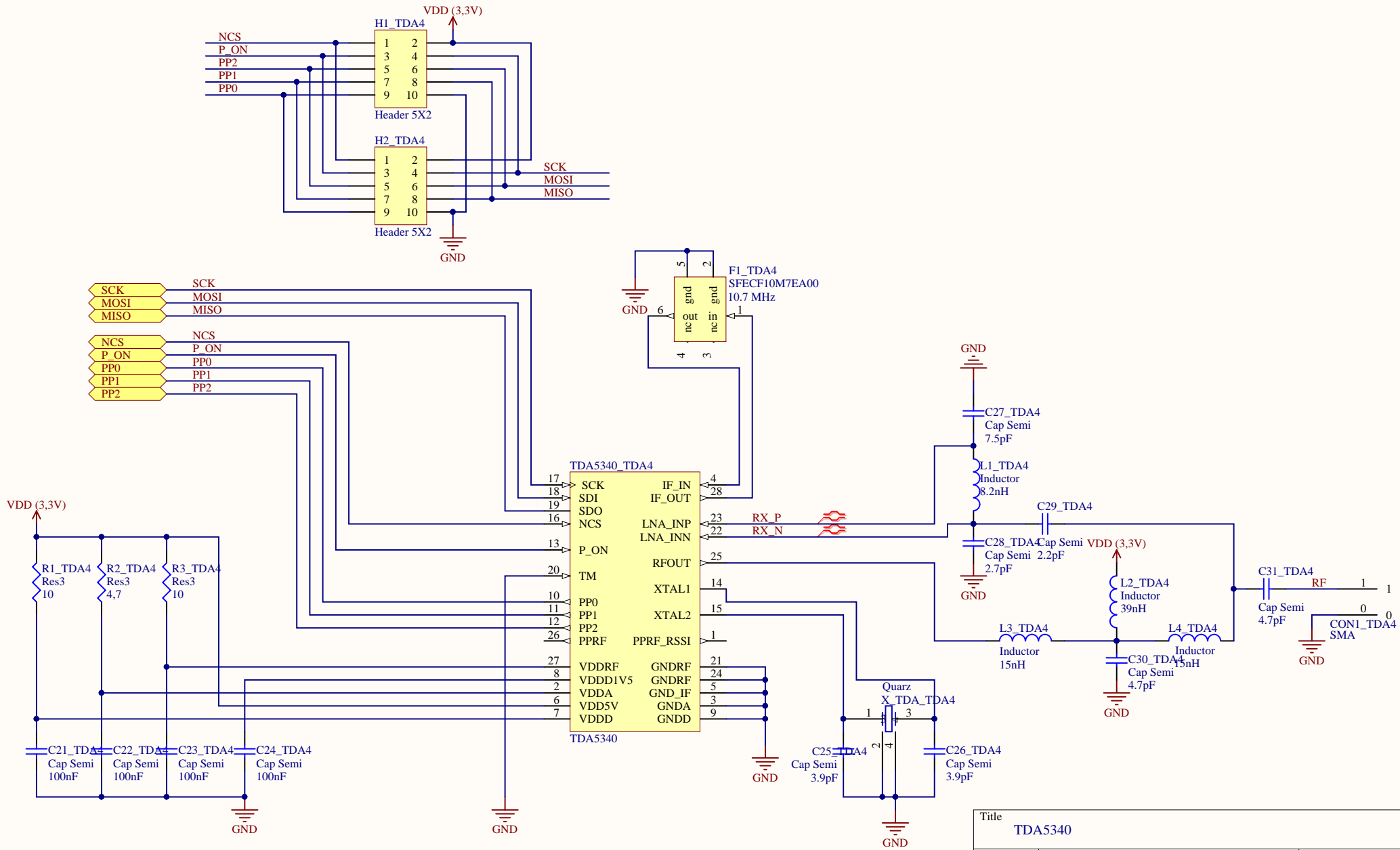
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Size	Number	Revision
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Date:	05.09.2016	Sheet of
File:	D:\Bachelorarbeit\...\TDA5340.SchDoc	Drawn By: Christof Pfannenmüller

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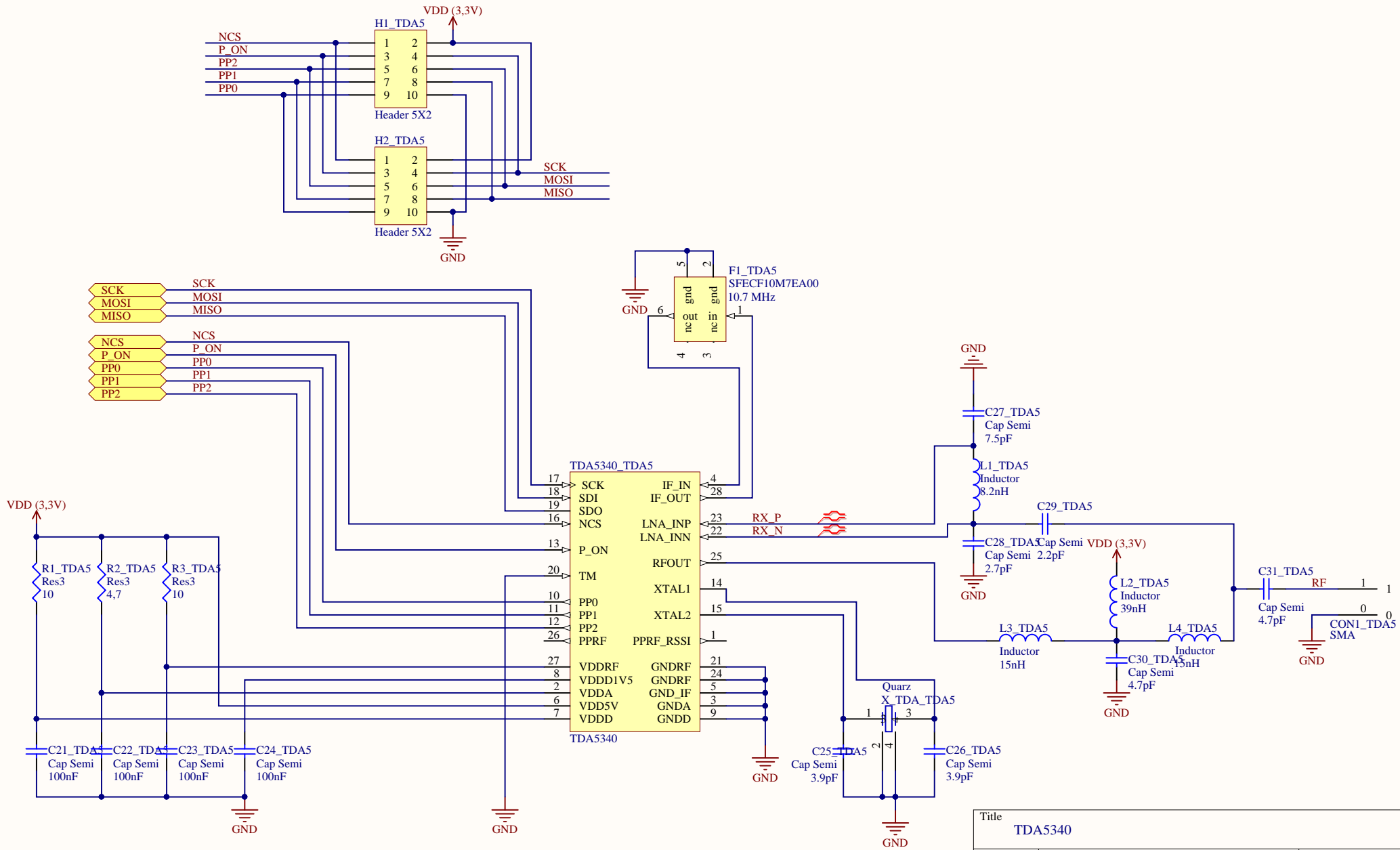
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Title		
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Size	Number	Revision
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Date:	05.09.2016	Sheet of
File:	D:\Bachelorarbeit\...\TDA5340.SchDoc	Drawn By: Christof Pfannenmüller

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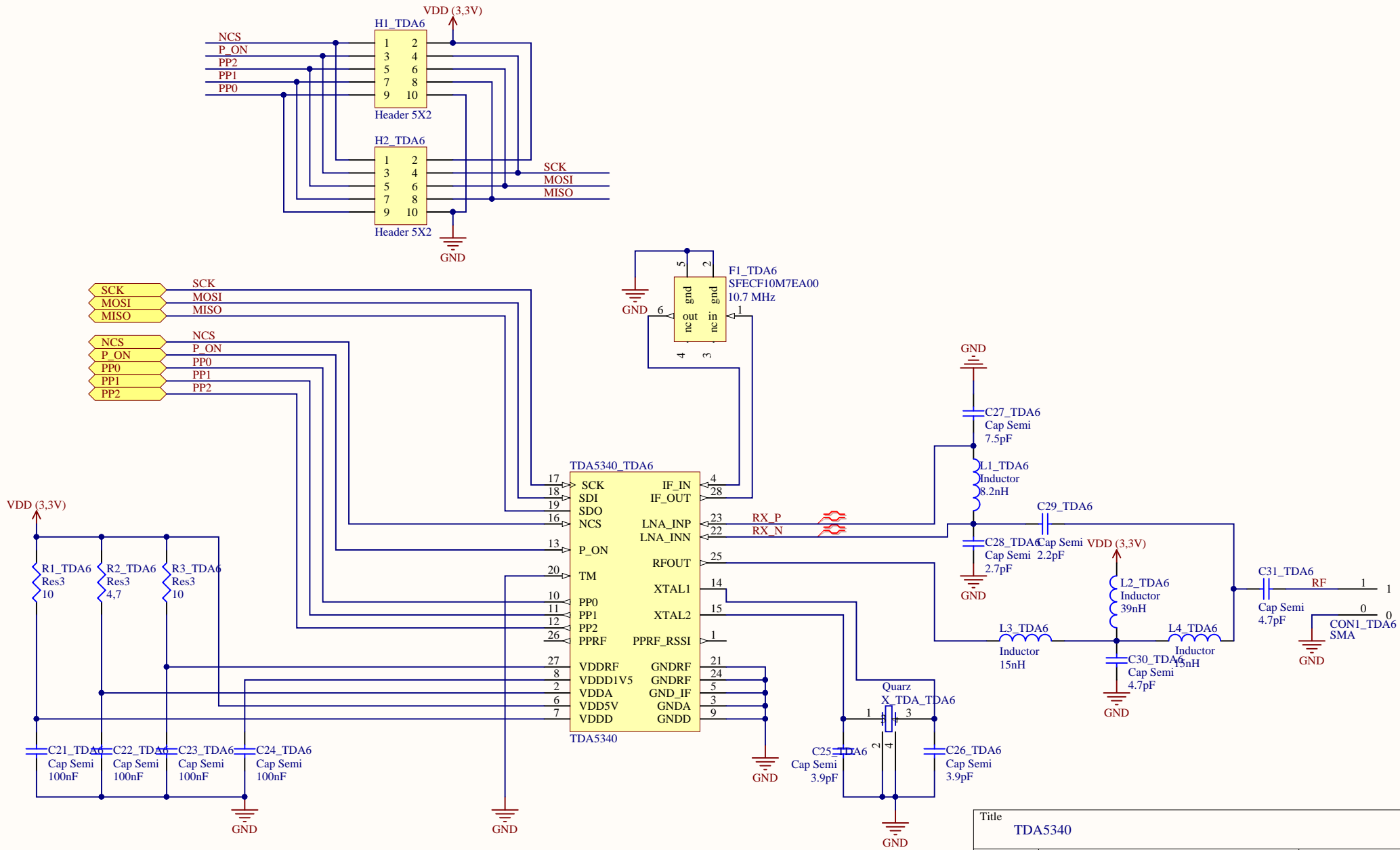
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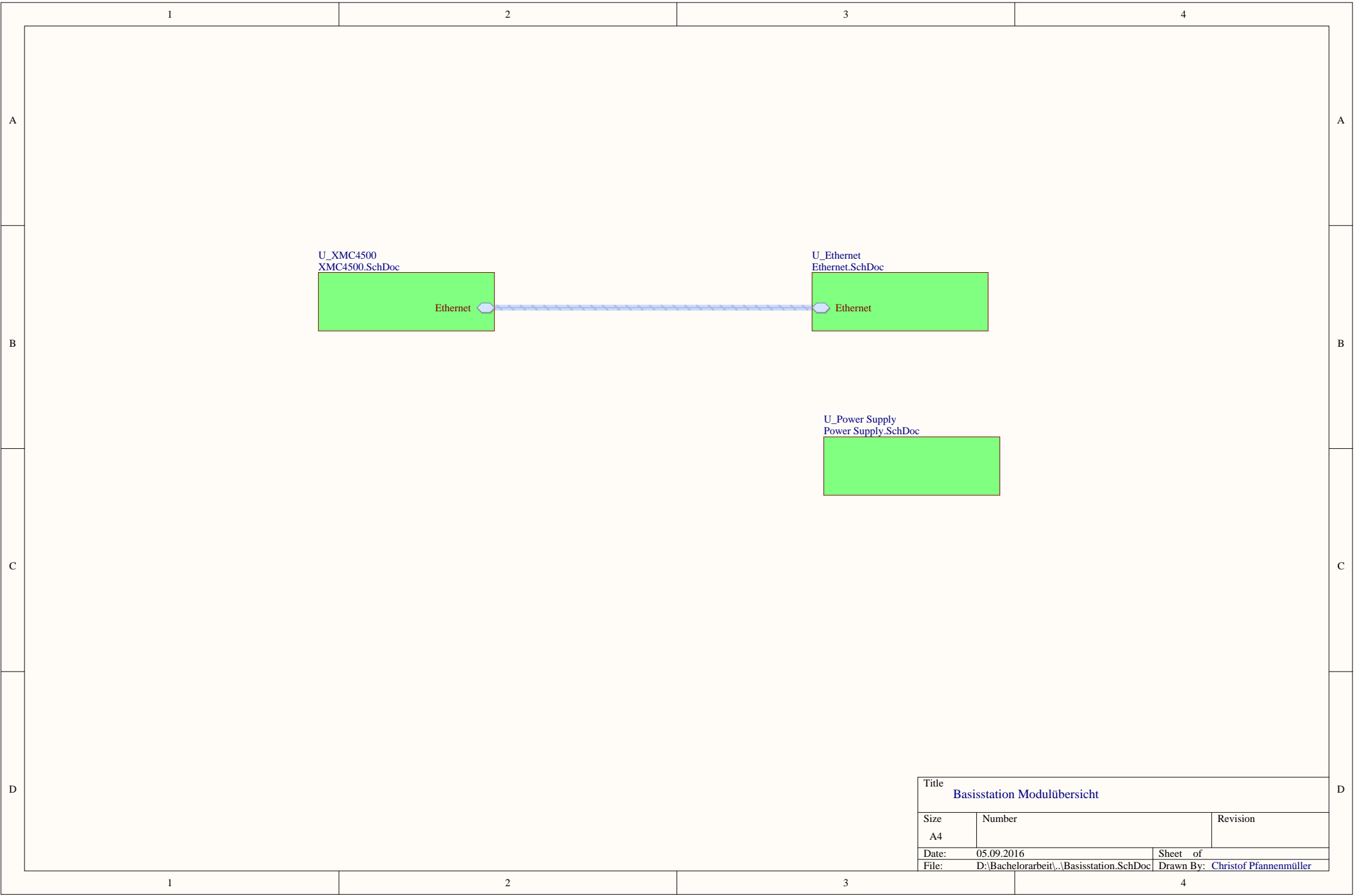
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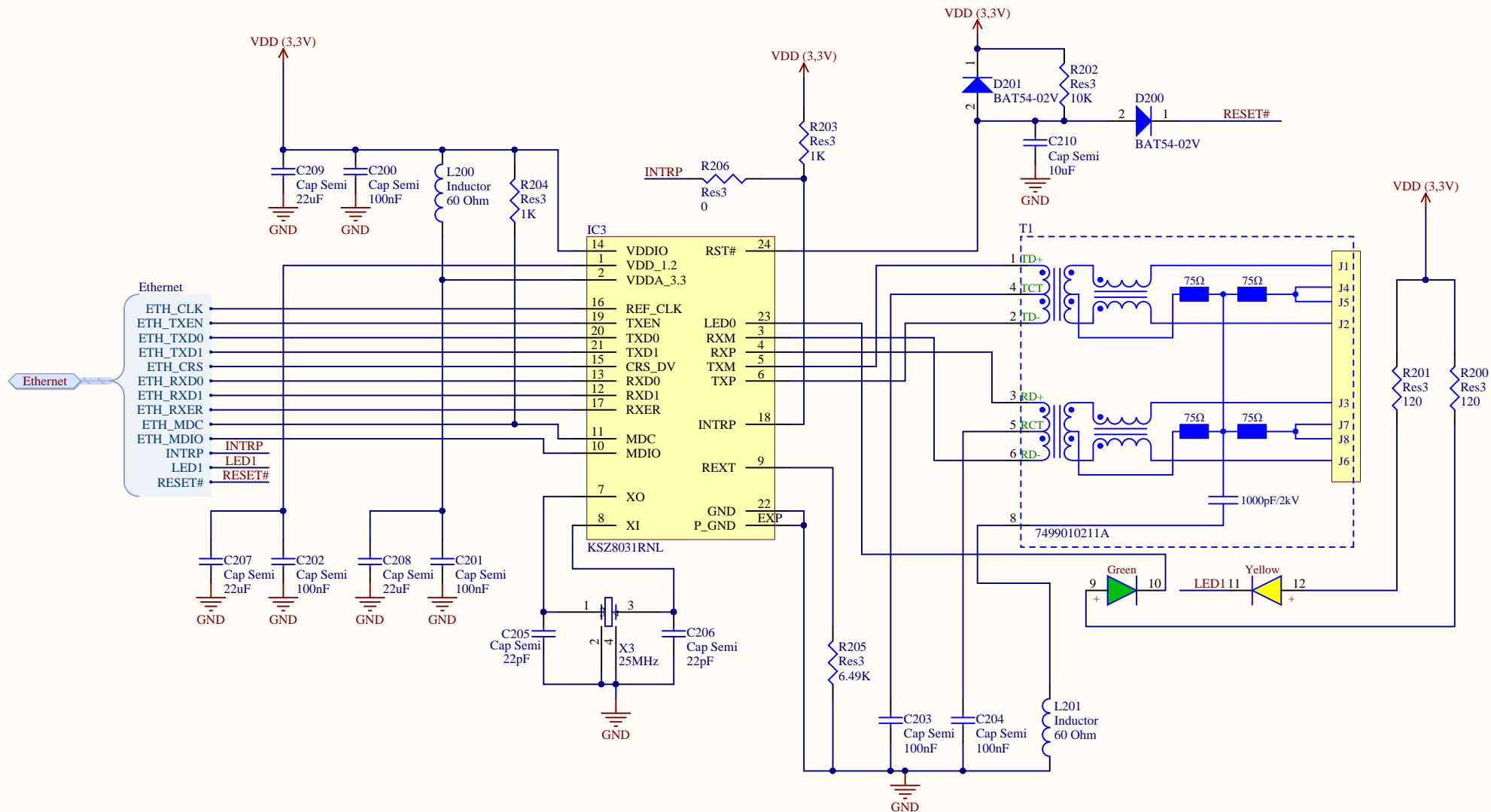
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Title		
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Size	Number	Revision
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Date:	05.09.2016	Sheet of
File:	D:\Bachelorarbeit\...\TDA5340.SchDoc	Drawn By: Christof Pfannenmüller





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Ethernet		
Size	Number	Revision
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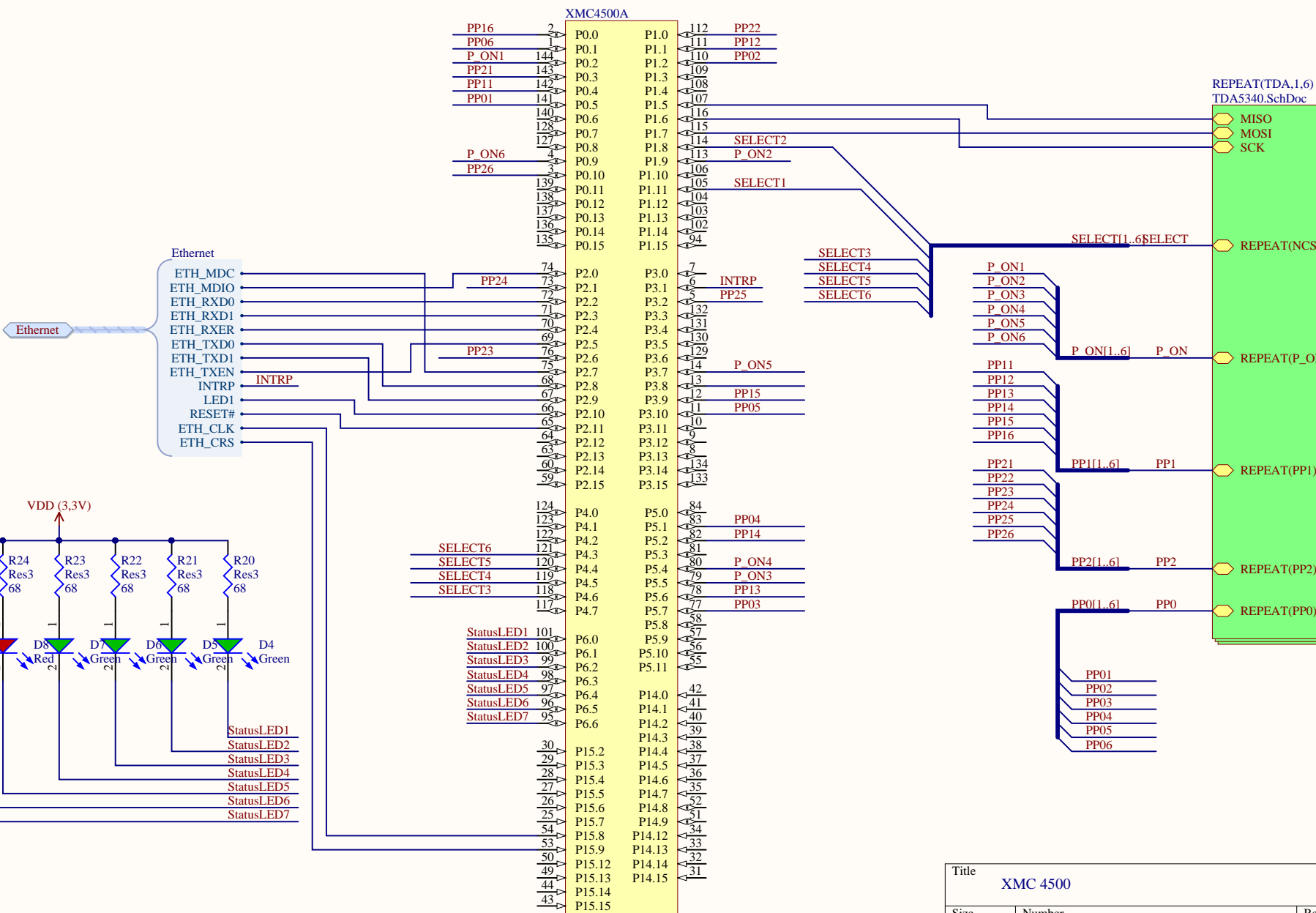
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Size	Number	Revision
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File:	D:\Bachelorarbeit\...\XMC4500.SchDoc	Drawn By: Christof Pfannenmüller

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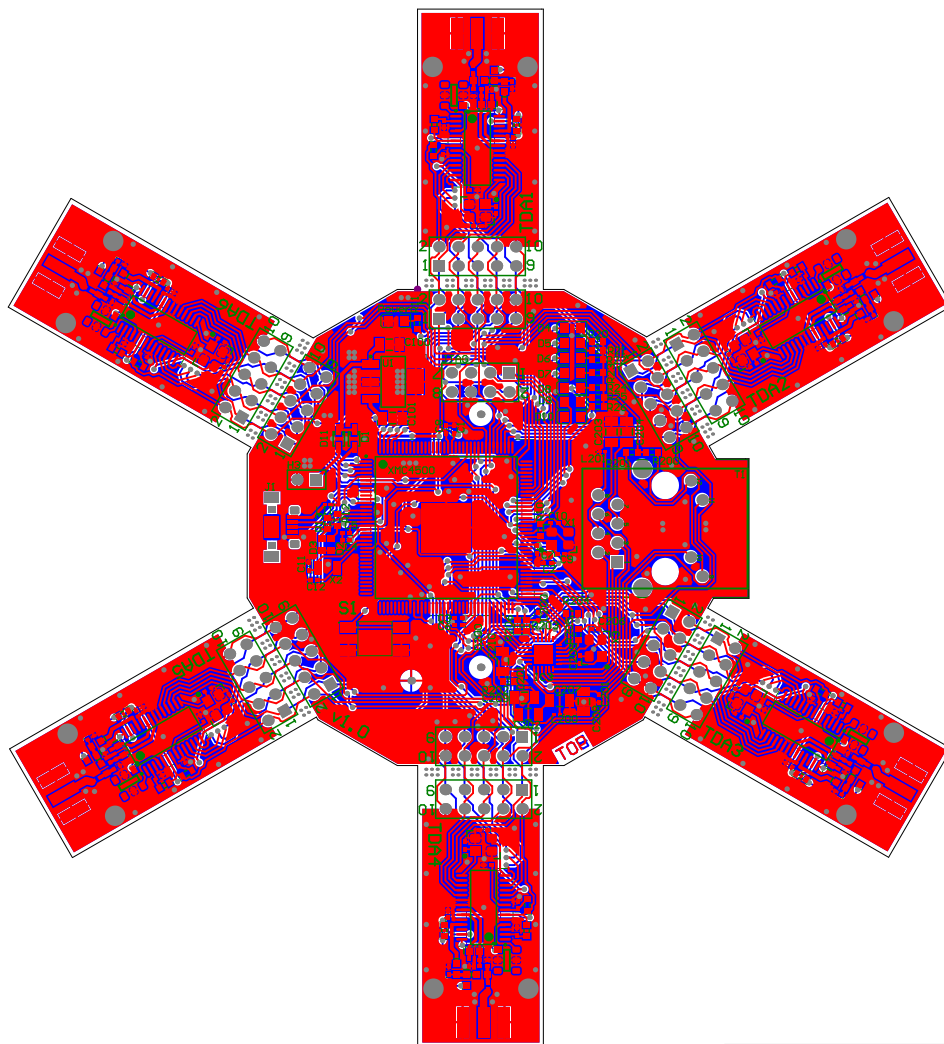
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Altium Limited
12a Rodborough Rd
Frenchs Forest
NSW 2086

ENGINEER: Christof Pfannenmueller		TITLE: Basistation PCB Layout	
PCB DESIGNER:			
DATE: 05.09.2016	PART NO.:	REV:	
FILE NAME: PCB_Testplatine.PcbDoc	DWG NO.:	SCALE:	

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