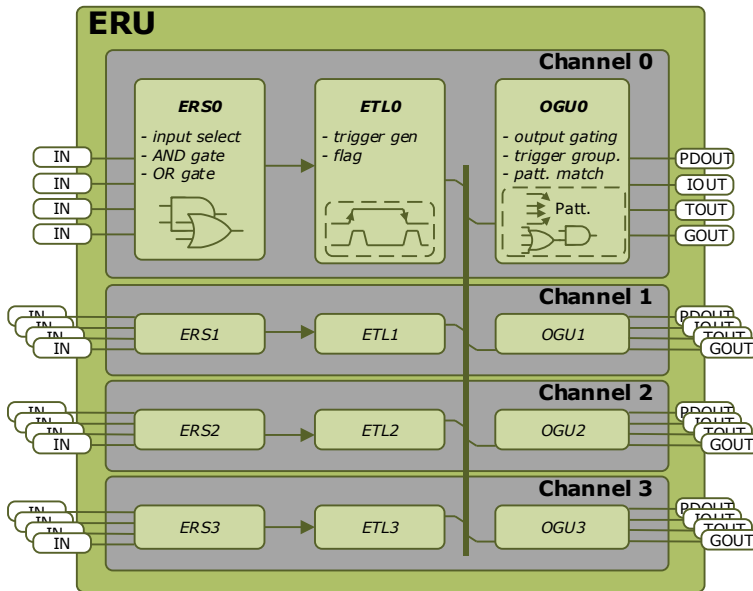# ERU

Event Request Unit

XMC™ Microcontrollers
August 2014

ERU
Event Request Unit

Highlights

The ERU module can be used to expand the P-to-P connections of the device: ports-to-peripherals, peripherals-to-peripherals and ports-to-ports. It also offers configurable logic, that allows the generation of triggers, pattern detection and real-time signal monitoring.

Key Feature

› Connection flexibility
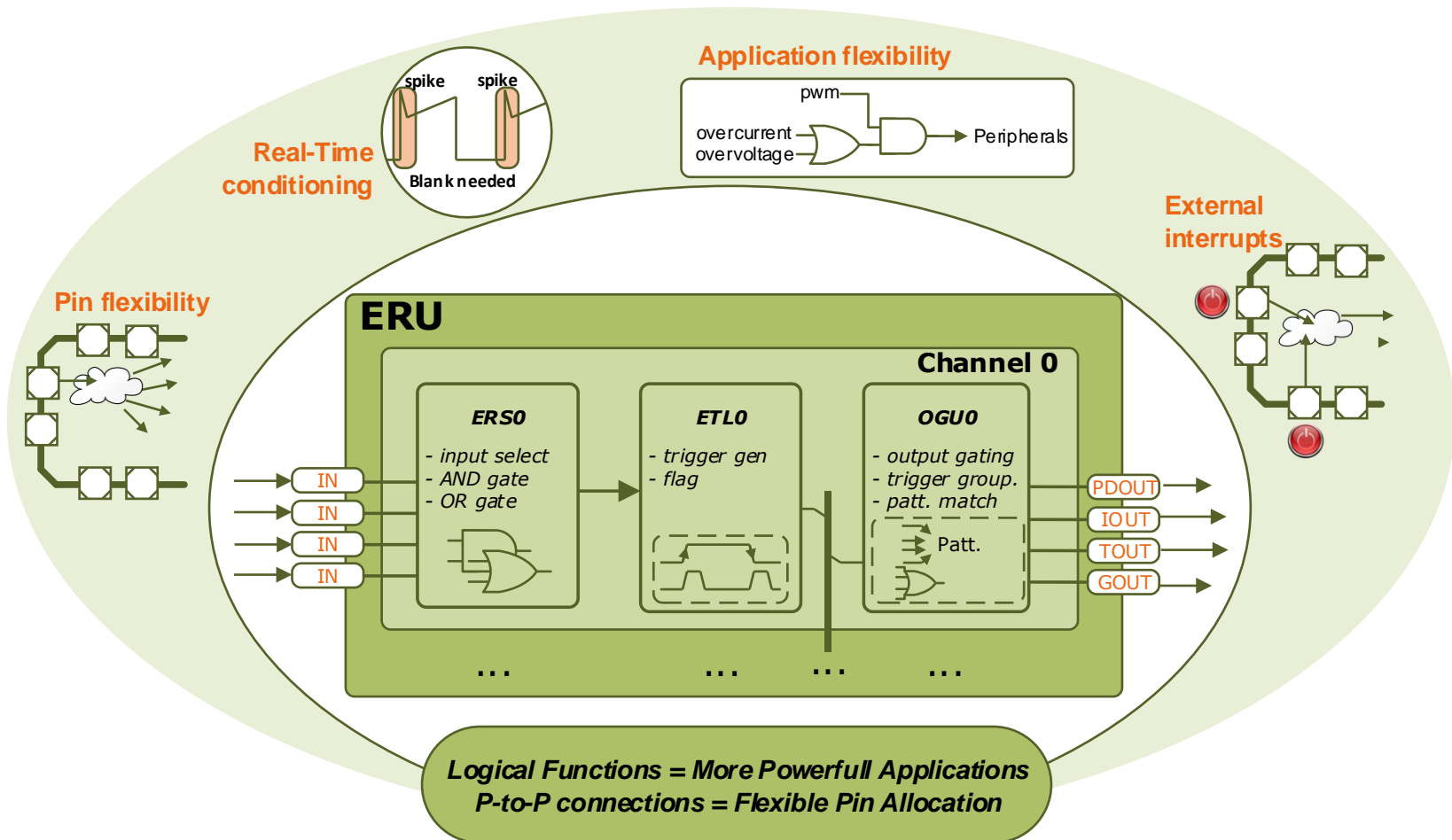
› External Interrupt Generation

› Configurable logic

Customer Benefits

› Connection expansion for small packages; Increased application case coverage for motor control, power conversion, etc; Peripheral/Port-to-Peripheral/Port;
› Multiple and parallel interrupt generation from port pins; Conditioning of interrupts with internal signals;
› Real-time conditioning of external signals; Logical functions between peripherals, e.g AND, OR;
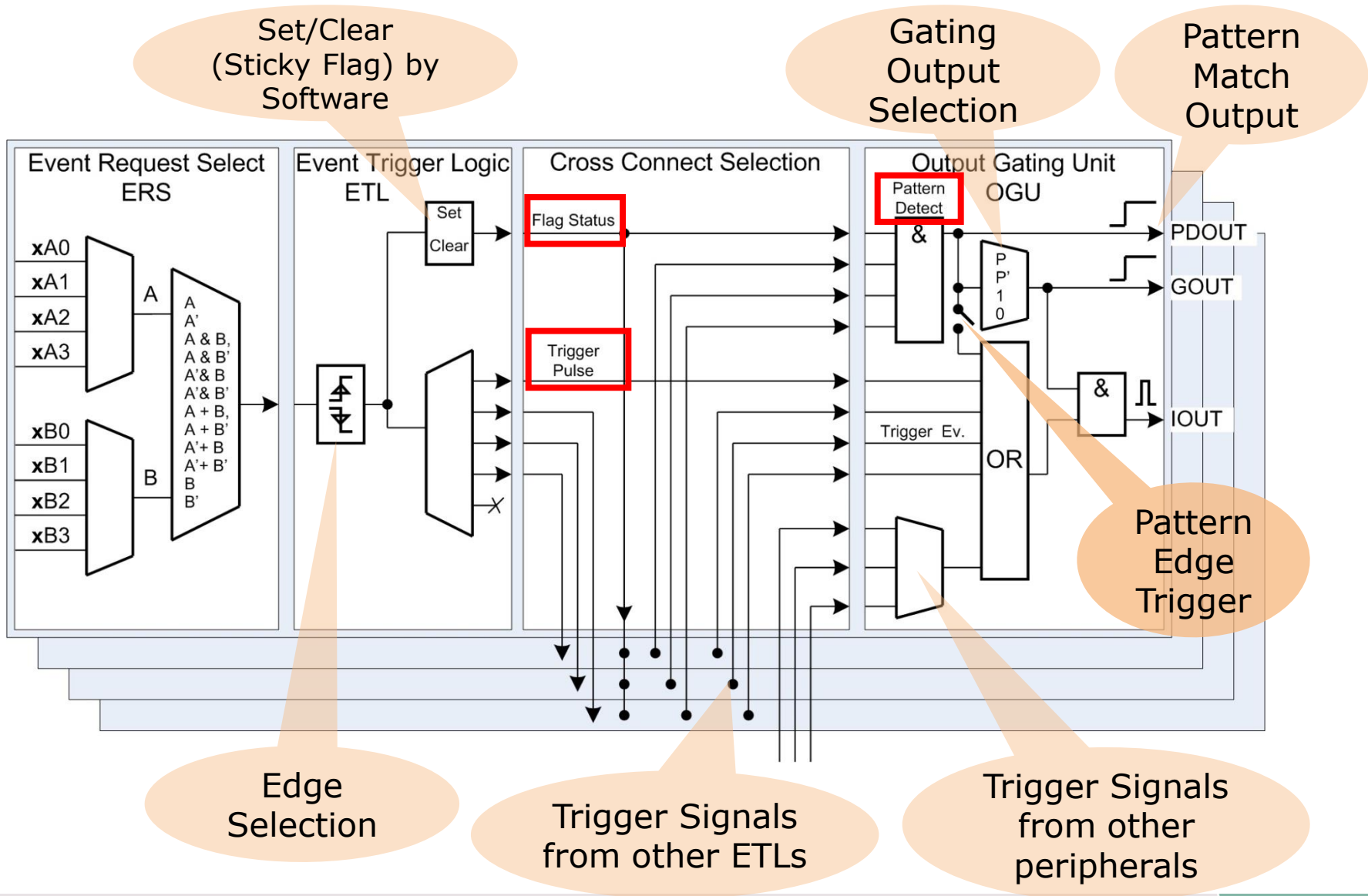
ERU
Connection flexibility (1/2)

› **P-to-P connections**: Port-to-Peripheral, Peripheral-to-Peripheral and Port-to-Port

› **Configurable logic** for application flexibility and real-time conditioning

ERU
Connection flexibility (2/2)

Set/Clear (Sticky Flag) by Software

Gating Output Selection

Pattern Match Output

Pattern Edge Trigger

Edge Selection

Trigger Signals from other ETLs

Trigger Signals from other peripherals

Event Request Select ERS

xA0
xA1
xA2
xA3
A

A
A'
A & B,
A & B'
A'& B
A'& B'
A + B,
A + B'
A'+ B
A'+ B'
B
B'

xB0
xB1
xB2
xB3
B

Event Trigger Logic ETL

Set
Clear

Cross Connect Selection

Flag Status

Trigger Pulse

Output Gating Unit OGU

Pattern Detect

&

P P' 1 0

Trigger Ev.

OR

&

PDOUT
GOUT
IOUT

ERU
External Interrupt Generation

› Edge detection function – **trigger generation**

› Especially tailored for interrupt generation

› Software flag for **monitoring/signal reconstruction**
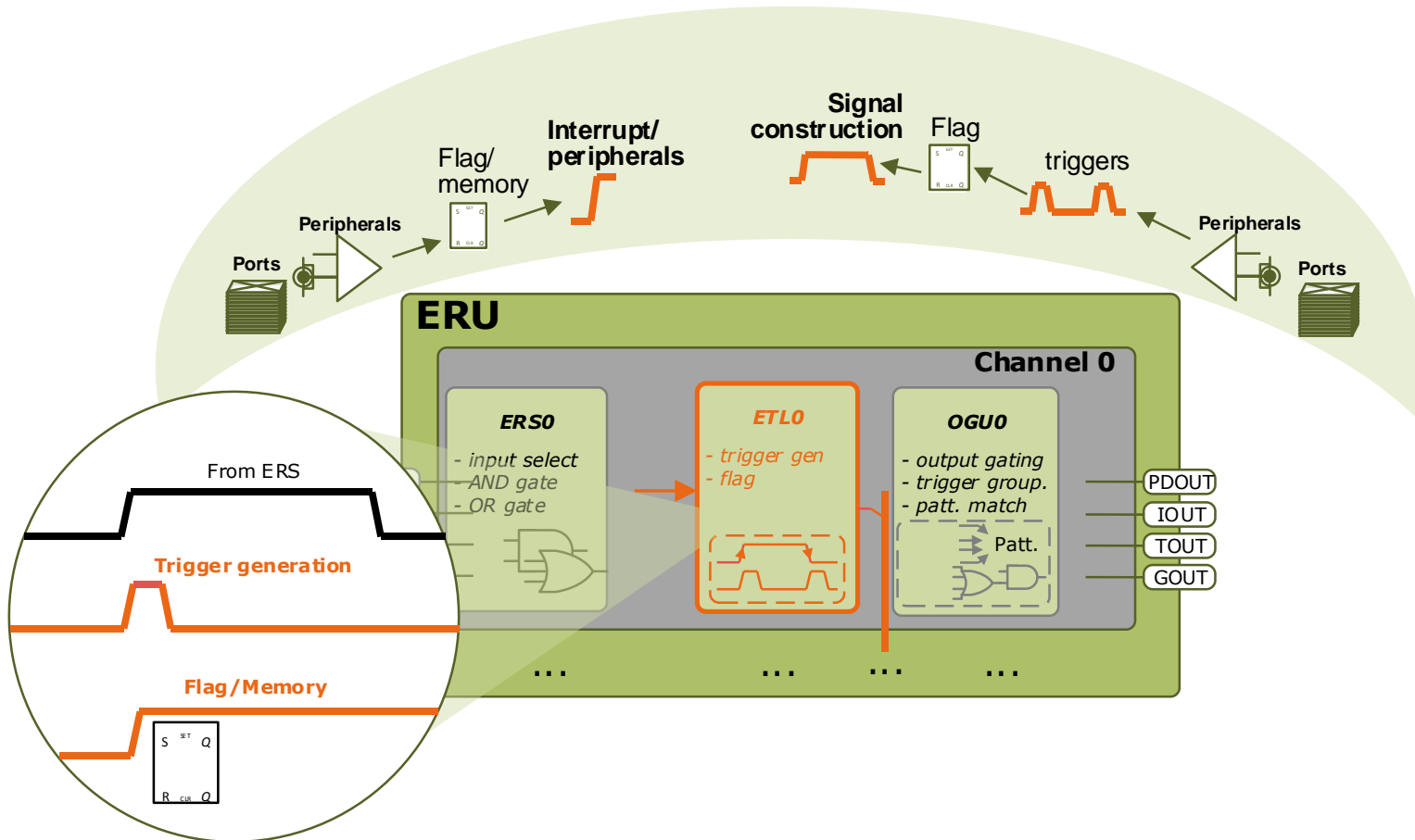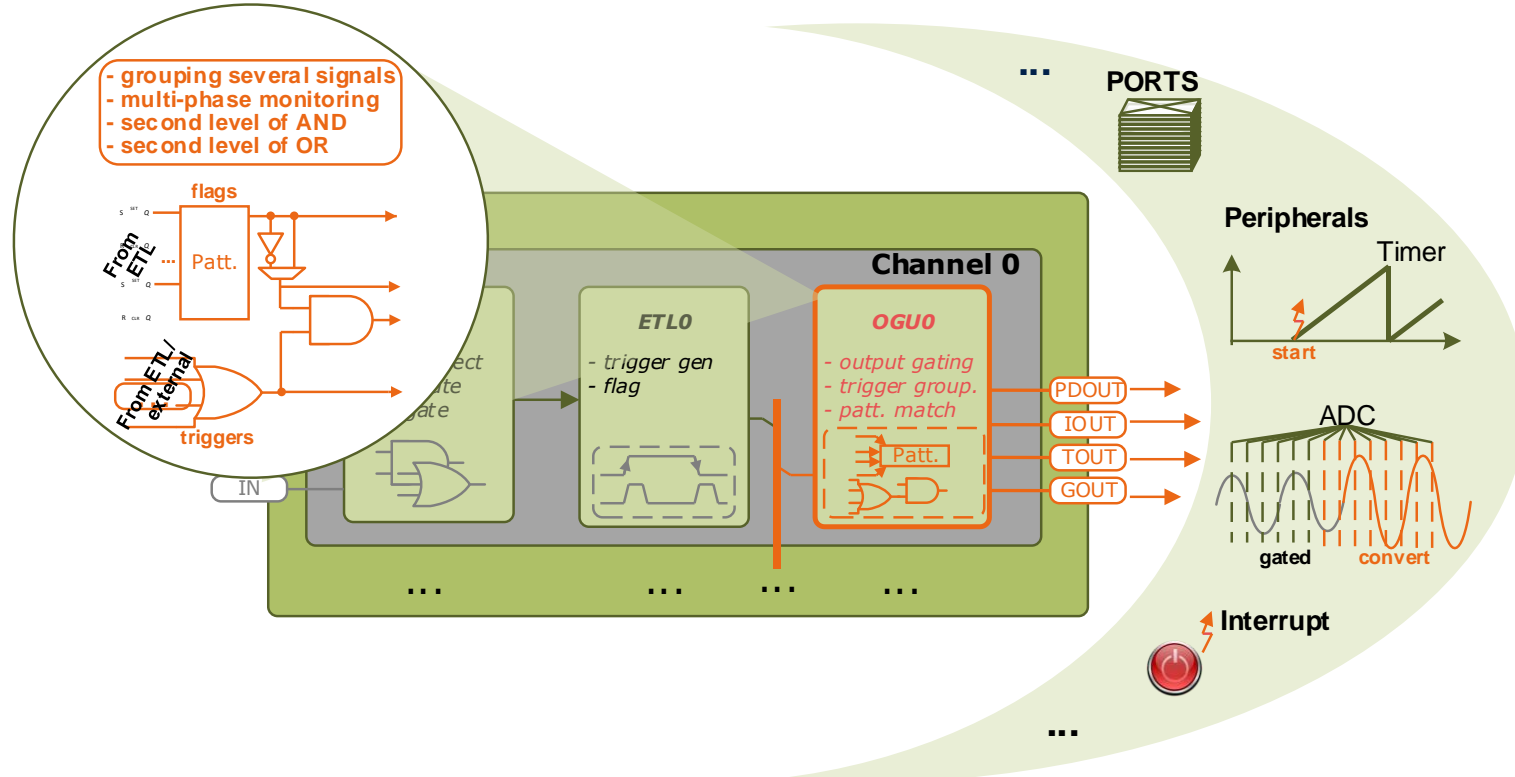
ERU
Configurable logic (1/2)

› Logical combination between two signals – **signal conditioning**

› Connection of Ports to peripherals – **flexibility even in smaller packages, interrupt generation**

› Connections of Peripherals to Peripherals – **real-time conditioning** for demanding applications
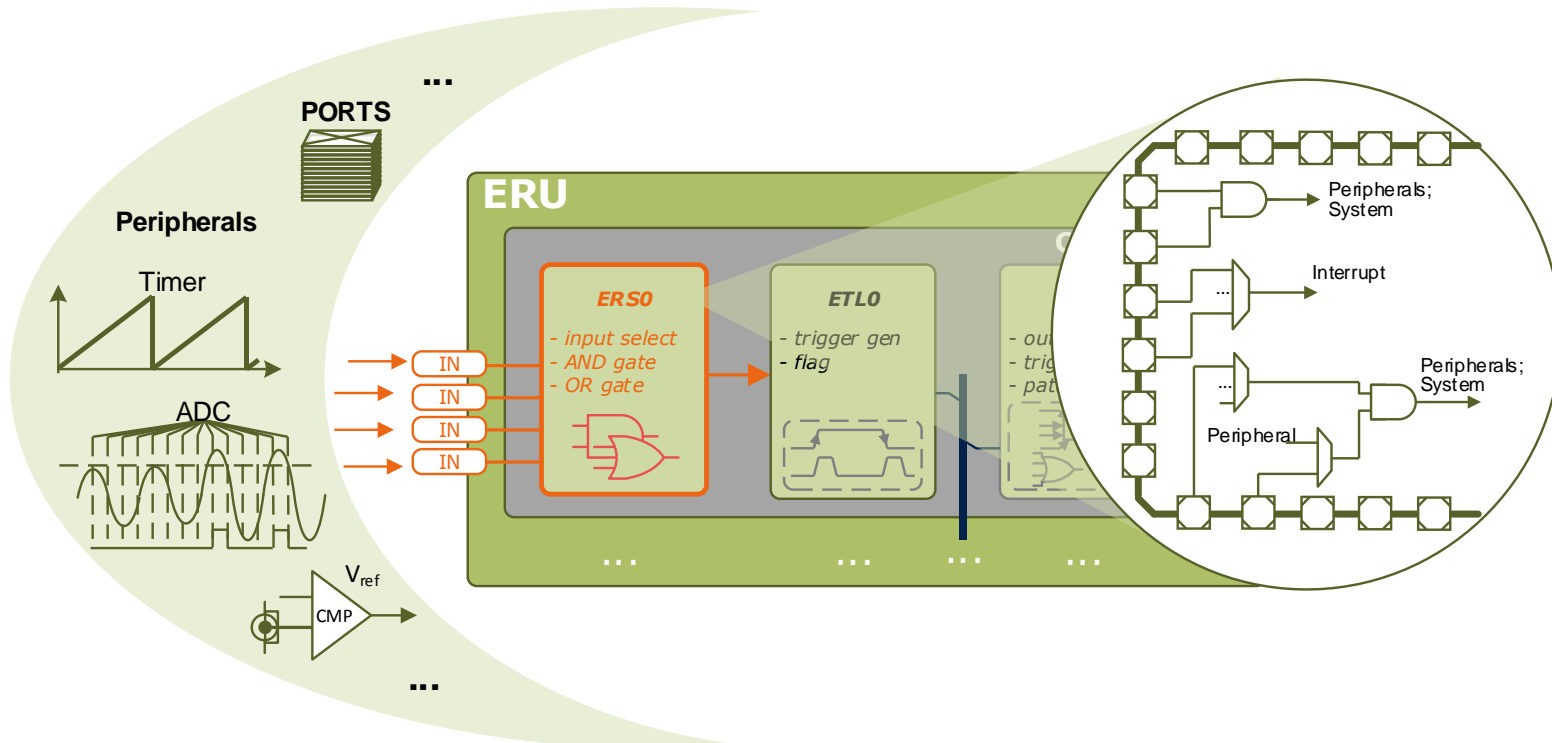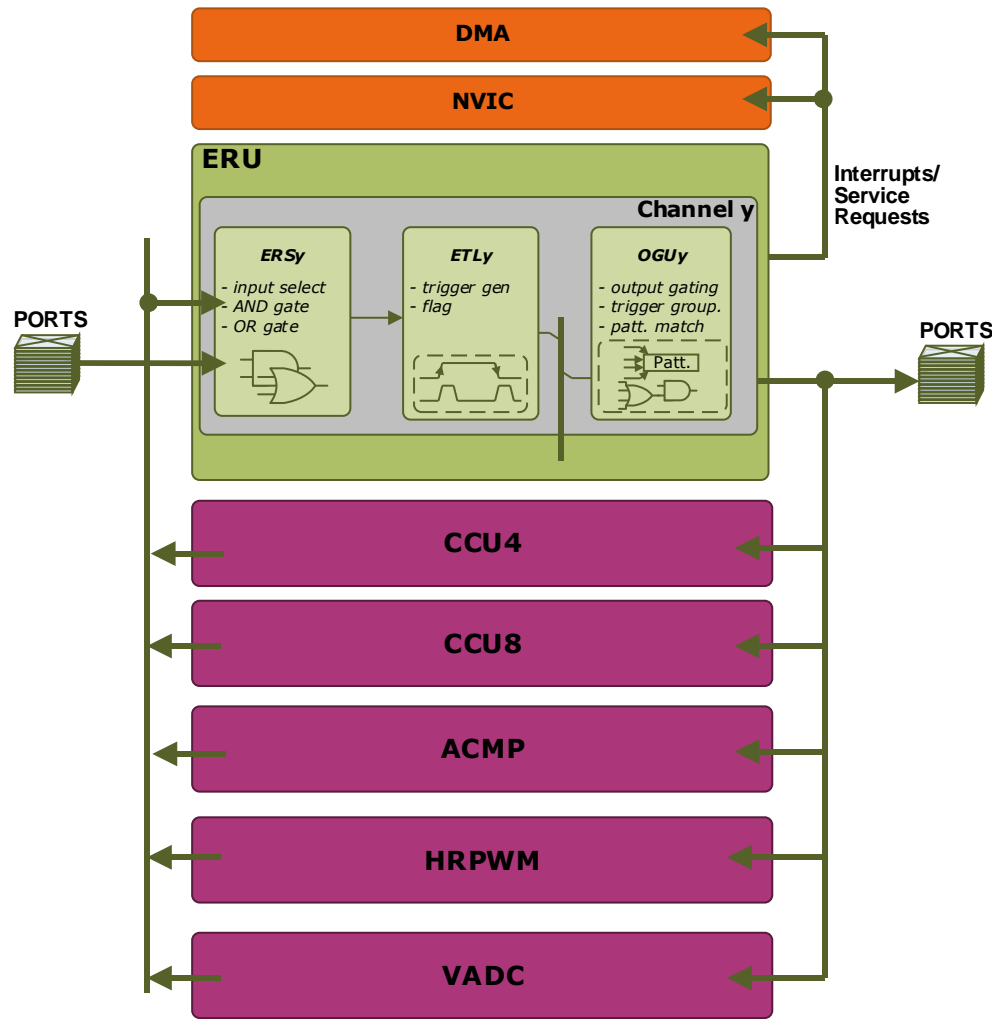
ERU
Configurable logic (2/2)

› Signal Grouping

› **Output AND and OR Gates**

› **Real-Time conditioning** for multi-phase systems

› Connections to peripherals/system and ports – **package flexibility**

ERU
System Integration

XMC4100	XMC4200	XMC4400	XMC4500	XMC4700
•	•	•	•	•

XMC1100	XMC1200	XMC1300
•	•	•

The ERU system integration offers several advantages:

› Integration within Timers and mixed-signal peripherals, e.g ADCs and Comparators. Especially suitable for real-time control.

› Multiple port connections for pin allocation flexibility

› Multiple interrupt generation from port pins

› Target applications

 – Power Conversion

 – Motor Control

 – General Purpose

Several components may be present or not depending on the device

Application Example
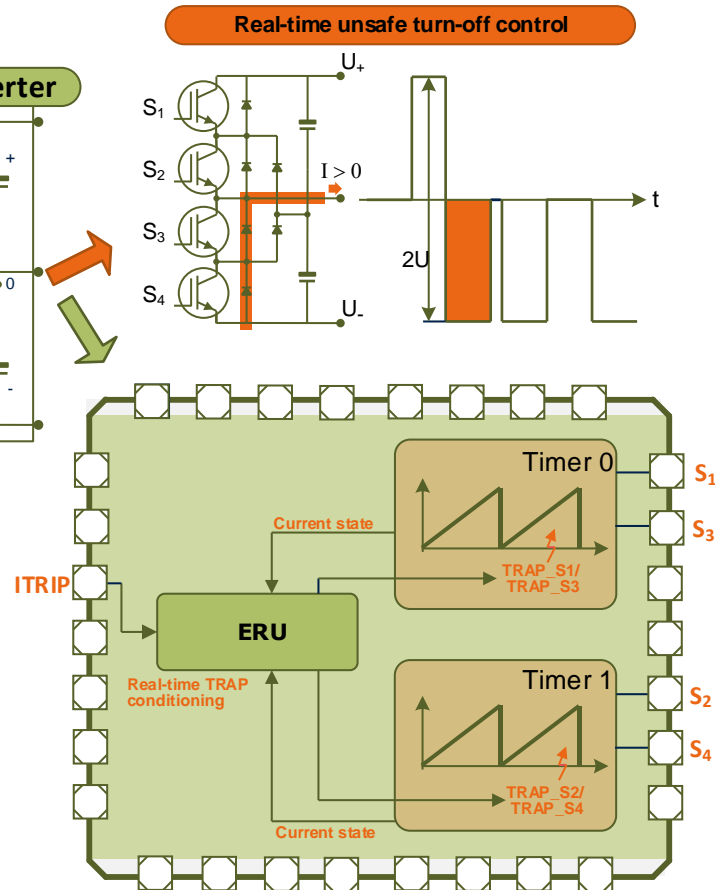# Pin flexibility

Overview

In resource optimized applications, it is necessary to have a good pin-to-peripheral flexibility to be able to map the core of an application in different devices.

The additional flexibility offered by the ERU allows the user to increase the pin routing paths, which helps to compensate for a resource rearrangement.

The same flexibility can also be used to reach a maximum resource utilization due to the expansion of peripheral-to-peripheral connections.

In Brief

ERU gives pin and resource allocation flexibility

Application Example
3-Level inverter real-time conditioning

3-Level Inverter

Real-time unsafe turn-off control

Overview

In a 3-Level inverter, it may be necessary to have a real-time monitoring and conditioning of a TRAP/ITRIP signal.

This signal indicates that the PWMs controlling the inverter stage should be shut-down, due to an overcurrent event.

Nevertheless in specific time-window of the switching activity this ITRIP signal needs to be delayed/conditioned to avoid a major overshoot in the voltage output.

This can be done using the combinational logic of the ERU - by monitoring the current PWM states and generate the proper TRAP sequence for the Timers.

In Brief

ERU combinational logic allows real-time conditioning of a ITRIP signal

Support material:

Collaterals and Brochures	› Product Briefs › Selection Guides › Application Brochures › Presentations › Press Releases, Ads	› www.infineon.com/XMC
Technical Material	› Application Notes › Technical Articles › Simulation Models › Datasheets, MCDS Files › PCB Design Data	› www.infineon.com/XMC › [Kits and Boards](#) › [DAVE^{TM}](#) › [Software and Tool Ecosystem](#)
Videos	› Technical Videos › Product Information Videos	› [Infineon Media Center](#) › [XMC Mediathek](#)
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