

1

2

3

4

A

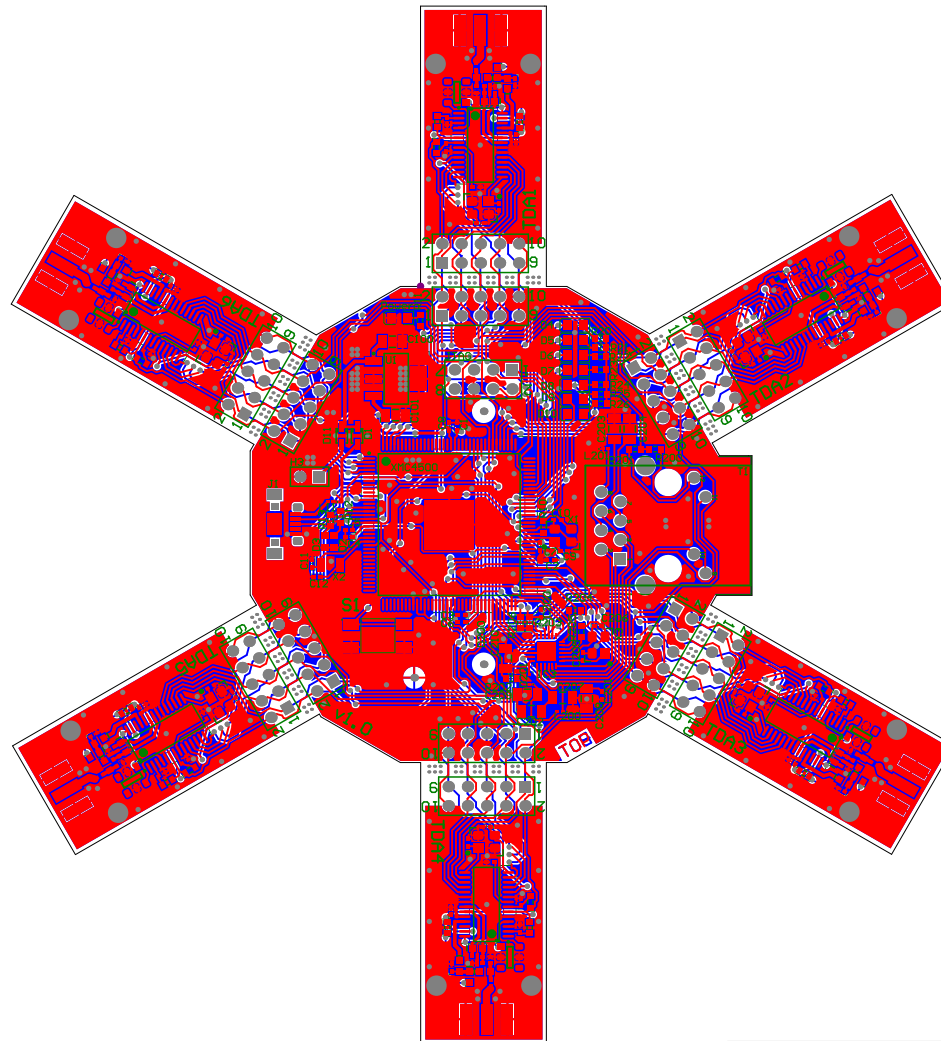
B

C

D

A

D



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ENGINEER: Christof Pfannenmueller	TITLE:		
PCB DESIGNER:			
DATE: 23.08.2016	PART NO.:	REV:	
FILE NAME: PCB_Testplatine.PcbDoc	DWG NO.:	SCALE:	

1

2

3

4