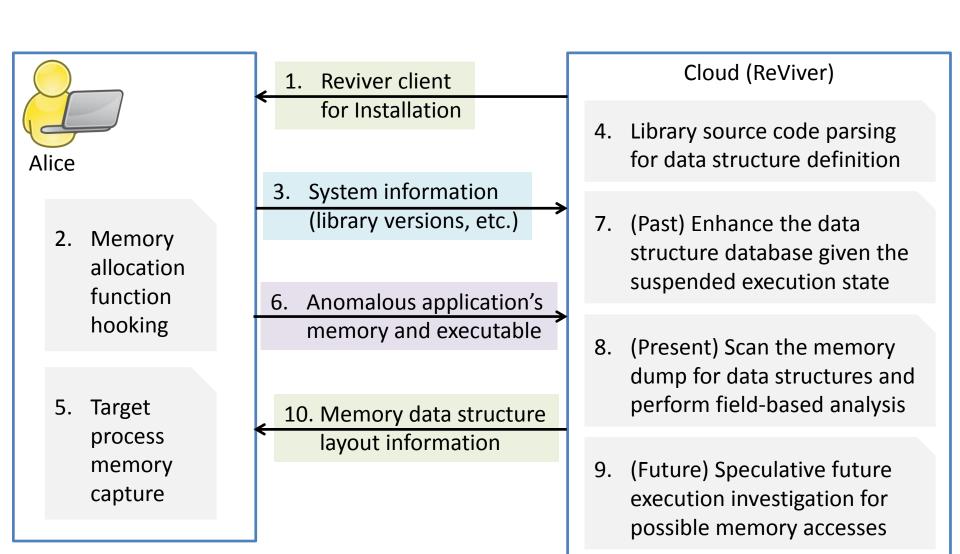
ReViver: Trace-Free Memory Data Forensics Through Speculative Symbolic Execution

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ReViver's high level architecture



Past: Statistical Information

Goal:

 Collecting the statistical information about the executable's use of data structures

How:

- Best-Effort Partial Symbolic Execution
- Prior Knowledge Collection
- Library Parsing for Structure Definitions

Present: Static Heap Analysis

Goal:

Reversing data type fine-grained for captured memdump

How:

- Trace-free heap reverse engineering
- Pruning candidate library structures
- Dump-library structure matching

Future: Speculative Forensics

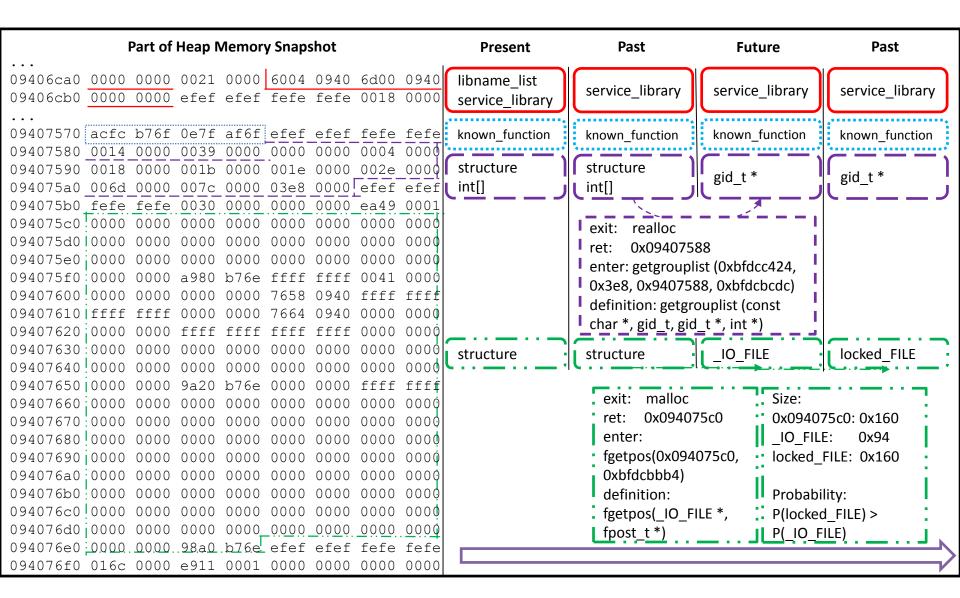
Goal:

Confirm or correct data structure by type sinks

How:

- Revived speculative symbolic execution
- Type propagation for type forensics

CoreUtils Groups Case Study



Memory Data Forensics on Smart Grid SCADA System

- Learn about the memory layout and what data are available in memory
- Improve incident response
- Detect malicious modifications
- Figure out the root-cause of misbehaving

