Project 12 Grade Sheet	Group#:			1
1 Ojest 12 Stade Stiest	Grader:			1
Point breakdowns:	Stadet.			-
B (binary): Full points awarded if criteria met, no points otherwise.				
C (criteria): Grade according to the stated criteria for the requirements for each object				
S (split): points are split evenly across all artifacts				
5 (Spire). Politics are spire everify across all artifacts	Possible	Point	Points	
	Points	Breakdown	Awarded	
High Speed 5.0 m/s	Foints	Breakdown	Awarueu	
Is there an issue log entry for the high speed design change?	10	В		
Check the stats logs for the acceptance tests and verify that the "-fs 5.0" commandline argument was used in	10	, В		
all tests	10	В		
Testing	10	, <u> </u>		
AUTOMATED: To check testing, copy the group's code from their portfolio into a clean copy of the simulator				
framework and make sure the code will compile.		N/A	C	No points allocated for a compiling simulator.
Is the Unit Test Log complete and up to date (all controller modules listed, all tests passing, input and output		IN/PA		points anocated for a complining simulator.
files properly linked.	10	В		
піво ргоропу інікой.	10	В		
AUTOMATED: Execute the unit tests using the simulator assembled in the design portfolio grading. (Note that				
this step requires a valid unit_tests.txt summary file). All tests must pass (0 failed assertions), and all tests				
listed in the unit test log must be listed in the unit_tests.txt file. If the simulator will not compile, award no credit.	10	В		
ilisted in the unit test by must be listed in the unit_tests.txt lile. In the simulator will not compile, award no credit.	10	Б		
le the Integration Test Log complete and up to date? "Complete" means all sequence diagrams are tested (up				
Is the Integration Test Log complete and up to date? "Complete" means all sequence diagrams are tested (up				
to a total of 20) and include all the original sequence diagrams (1A, 1B, 1C, 2A, 2B, 3A, 4A, 5A, 5B, 6, 7A, 7B,	4.0			
7C, 8A, 9A). "Up to date" means all tests passing, input and output files properly linked.	10	В		
AUTOMATED: Execute the integration tests using the simulator assembled in the design portfolio grading.				
(Note that this step requires a valid integration_tests.txt summary file). All tests must pass (0 failed assertions).				
All tests listed in the integration test log must be listed in the integration_tests.txt file. If the simulator will not				
compile, award no credit.	10	В		
Is the Acceptance Test Log complete and up to date? All the tests listed in the project writeup must be listed.				
Each entry must be complete (all fields filled out and input and output files properly linked). Any test that does				
not pass must be documented to describe the problem that causes the test to fail. Do not attempt to run the		_		
acceptance tests.	10	В		
AUTOMATED: Execute all acceptance tests listed in Proj 12 with an arbitrary random seed using the simulator				
assembled in the design portfolio grading. Any test that does not deliver all passengers must have a note in the		_		
test log explaining why.	10	В		
Improvements Log		_		
Is there an entry for project 12 in the improvements log? Is the individual contributions sheet complete?	5	В		
Peer Review				
Are there 4 new peer review entries in the peer review log of updated modules? If there are fewer than 4				
updated modules, are there peer review entries for any artifacts not previously reviewed?				
(Must be at least 4 new peer reviews)	20			
		Point	Deduction	
Deductions	Points lost	Breakdown	Awarded	
Is the issue log reasonably up to date?	-8	В		
Do the files that have been modified in the portfolio have the proper headers?	-16			
Do the submitted files conform to the portfolio layout guidelines?	-16			
Feedback from previous projects addressed?	-25	В		
	Possible	Points		
	Points	awarded		
Totals	105			
Late Penalty	Percentage			
	100	0		
Enter the percentage of total score (per late policy) Final Score	Percentage	Points		