

63		Sum $X_i^1$ LSB channel	6	FPGA	
64		Sum $X_i^1$ MSB channel	6	FPGA	
65		Sum $X_i^2$ LSB channel	7	FPGA	
66		Sum $X_i^2$ MSB channel	7	FPGA	
67		Sum $X_i$ channel	8	FPGA	
68		Sum $X_i$ channel	9	FPGA	PhotoDiode K7 No 5
69		Sum $X_i^2$ LSB channel	8	FPGA	
70		Sum $X_i^2$ MSB channel	8	FPGA	
71		Sum $X_i^2$ LSB channel	9	FPGA	
72		Sum $X_i^2$ MSB channel	9	FPGA	
73		Sum $X_i$ channel	10	FPGA	PhotoDiode K7 No 6
74		Sum $X_i$ channel	11	FPGA	
75		Sum $X_i^2$ LSB channel	10	FPGA	
76		Sum $X_i^2$ MSB channel	10	FPGA	
77		Sum $X_i^2$ LSB channel	11	FPGA	
78		Sum $X_i^2$ MSB channel	11	FPGA	PhotoDiode K7 No 7
79		Sum $X_i$ channel	12	FPGA	
80		Sum $X_i$ channel	13	FPGA	
81		Sum $X_i^2$ LSB channel	12	FPGA	
82		Sum $X_i^2$ MSB channel	12	FPGA	
83		Sum $X_i^2$ LSB channel	13	FPGA	PhotoDiode K7 No 8
84		Sum $X_i^2$ MSB channel	13	FPGA	
85		Sum $X_i$ channel	14	FPGA	
86		Sum $X_i$ channel	15	FPGA	
87		Sum $X_i^2$ LSB channel	14	FPGA	
88		Sum $X_i^2$ MSB channel	14	FPGA	PhotoDiode K7 No 9
89		Sum $X_i^2$ LSB channel	15	FPGA	
90		Sum $X_i^2$ MSB channel	15	FPGA	
91		Sum $X_i$ channel	16	FPGA	
92		Sum $X_i$ channel	17	FPGA	
93		Sum $X_i^2$ LSB channel	16	FPGA	PhotoDiode K7 No 10
94		Sum $X_i^2$ MSB channel	16	FPGA	
95		Sum $X_i^2$ LSB channel	17	FPGA	
96		Sum $X_i^2$ MSB channel	17	FPGA	
97		Sum $X_i$ channel	18	FPGA	
98		Sum $X_i$ channel	19	FPGA	PhotoMultiplier N° 0
99		Sum $X_i^2$ LSB channel	18	FPGA	
100		Sum $X_i^2$ MSB channel	18	FPGA	
101		Sum $X_i^2$ LSB channel	19	FPGA	
102		Sum $X_i^2$ MSB channel	19	FPGA	
103		Sum $X_i$ channel	20	FPGA	External CIS N° 0
104		Sum $X_i$ channel	21	FPGA	
105		Sum $X_i^2$ LSB channel	20	FPGA	
106		Sum $X_i^2$ MSB channel	20	FPGA	
107		Sum $X_i^2$ LSB channel	21	FPGA	
108		Sum $X_i^2$ MSB channel	21	FPGA	Internal CIS
109		Sum $X_i$ channel	22	FPGA	
110		Sum $X_i$ channel	23	FPGA	
111		Sum $X_i^2$ LSB channel	22	FPGA	
112		Sum $X_i^2$ MSB channel	22	FPGA	
113		Sum $X_i^2$ LSB channel	23	FPGA	PhotoDiode Phocal
114		Sum $X_i^2$ MSB channel	23	FPGA	
115		Sum $X_i$ channel	24	FPGA	
116		Sum $X_i$ channel	25	FPGA	
117		Sum $X_i^2$ LSB channel	24	FPGA	
118		Sum $X_i^2$ MSB channel	24	FPGA	
119		Sum $X_i^2$ LSB channel	25	FPGA	
120		Sum $X_i^2$ MSB channel	25	FPGA	
121		Sum $X_i$ channel	26	FPGA	
122		Sum $X_i$ channel	27	FPGA	