

123		Sum $X_i^2$ LSB channel	26	FPGA	PhotoMultiplier N° 1
124		Sum $X_i^2$ MSB channel	26	FPGA	
125		Sum $X_i^2$ LSB channel	27	FPGA	
126		Sum $X_i^2$ MSB channel	27	FPGA	
127		Sum $X_i$ channel	28	FPGA	
128		Sum $X_i$ channel	29	FPGA	
129		Sum $X_i^2$ LSB channel	28	FPGA	
130		Sum $X_i^2$ MSB channel	28	FPGA	
131		Sum $X_i^2$ LSB channel	29	FPGA	
132		Sum $X_i^2$ MSB channel	29	FPGA	
133		Sum $X_i$ channel	30	FPGA	External CIS N° 1
134		Sum $X_i$ channel	31	FPGA	
135		Sum $X_i^2$ LSB channel	30	FPGA	
136		Sum $X_i^2$ MSB channel	30	FPGA	
137		Sum $X_i^2$ LSB channel	31	FPGA	
138		Sum $X_i^2$ MSB channel	31	FPGA	
139		Nb event		FPGA	
140		TimeStamp		FPGA	
141	00 00 00 00	Status Element		VME	0 = Ok
142	00 00 00 01	Number of status Element		VME	
143	00 00 00 nn	Number of data Element		VME	
144	00 00 00 01	Status Block Position		VME	
145	E0 F0 00 00	End of Frame		VME	Control Word