

COMPUTER SYSTEM & ARCHITECTURE 2021

Time :45min

1st internal

Fm: 20

1. A choose the correct from given : 5*1

- i) MAR stands for(a). Memory access register, b) Memory address register, c) Memory address reference, d)) Memory access reference
- ii) Exponent in floating point number can be represented by (a) Sign magnitude form b) 1's complement form ,c) 2's complement form ,d) biased form
- iii) In this mode of address of the operand is not specified explicitly- (a)indexed, b) indirect , c)relative , d) stack
- iv) Address bus is a) bidirectional , b) unidirectional c) parallel line
- v) PC a) hold next instruction to be executed , b) fetch data from memory, c) point
- vi) to instruction ,d) hold an instruction being executed

Answer in brief

(7*2)

- 2. what are the different instruction set explain with example
- 3. What are the different addressing modes? Explain in brief