Checkers Design Document

me 3

Contents

1	Intr	roduction	1
2	Modules .		1
	2.1	Hardware Hiding Module	2
		2.1.1 Wireless Communications Hardware Hiding Module	2
		2.1.2 Clock Hardware Hiding Module	2
	2.2	Behaviour Hiding Module	2
	2.3	Software Hiding Module	2
		2.3.1 TEMPLATE XXX Hiding Module	2

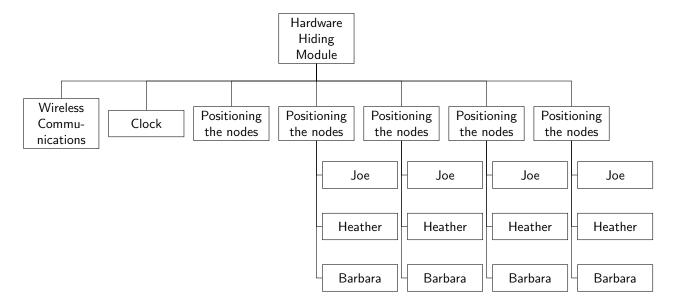
1 Introduction

This document contains decomposition, uses relationship, and traceability.

2 Modules

Modules are stuff.

2.1 Hardware Hiding Module



2.1.1 Wireless Communications Hardware Hiding Module

Host/Onboard Module

Secret This module serves to hide the secret of what wireless communications hard-

ware is being used

Responsibilites This module is responsible for handling transmission using the wireless com-

munications hardware.

 \mathbf{Uses} None Design None

2.1.2 Clock Hardware Hiding Module

Type Onboard Module

This module serves to hide the secret of what clock hardware is being used. Secret Responsibilites

This module is responsible for direct monitoring and controlling of the clock

hardware.

Uses 2.1.1 Design None

2.2 Behaviour Hiding Module

2.3 Software Hiding Module

2.3.1 TEMPLATE XXX Hiding Module

Type

Secret

Responsibilites

Uses

reference to implementation Design