



SLA 1500 CAM REV 4.0

March 23, 2019

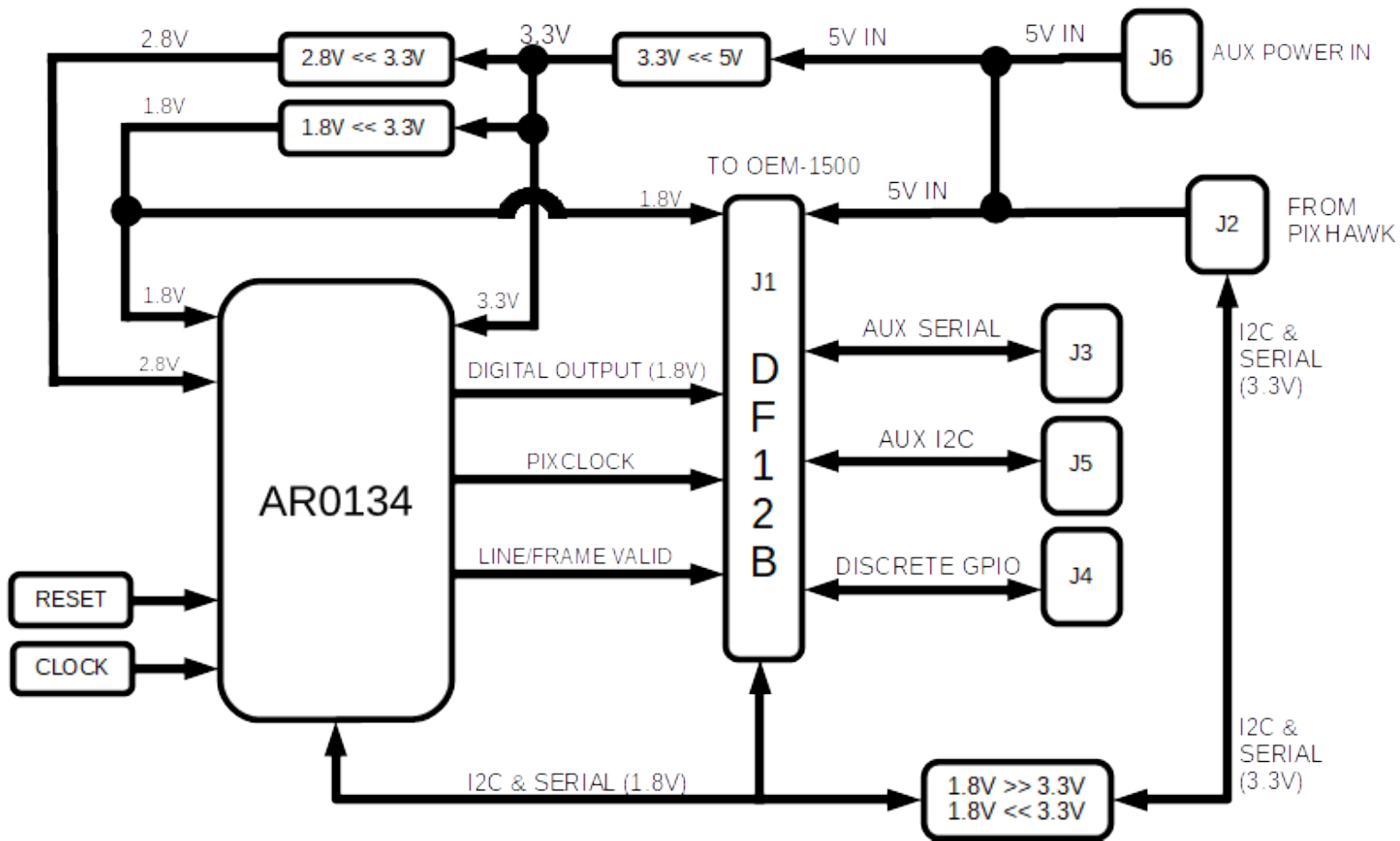


Figure 1: Level 1 Block Diagram of SLA 1500 CAM REV 4.0

| LABEL | MFG PART NUMBER | FUNCTION | MATES WITH |
|-------|----------------------------|--|---|
| J1 | HIROSE DF12B-50DS-0.5V(86) | 50-Pin Digital Connector, Connects Directly to OEM-1500 J4 | HIROSE 1500-OEM J4 [DF12B(5.0)-50DP-0.5V(86)] |
| J2 | JST SM06B-GH | 5VDC Power, Serial and I2C Communication With Pixhawk | JST GHR-06V-S |
| J3 | MOLEX 53261-0371 | Auxillary Serial Communications for Sightline Debugging | MOLEX 51021-0300 / CAB-0302 |
| J4 | MOLEX 53261-0471 | Discrete GPIO Lines from 1500- | MOLEX 51021-0400 / CAB-0401 |
| J5 | MOLEX 53261-0471 | Auxillary I2C Bus to Sightline | MOLEX 51021-0400 / CAB-0401 |
| J6 | MOLEX 53261-0471 | Auxillary 5VDC Power In | MOLEX 51021-0400 / CAB-0401 |

Figure 2: Summary of Connectors

