

## **SLA 1500 CAM REV 7.0**

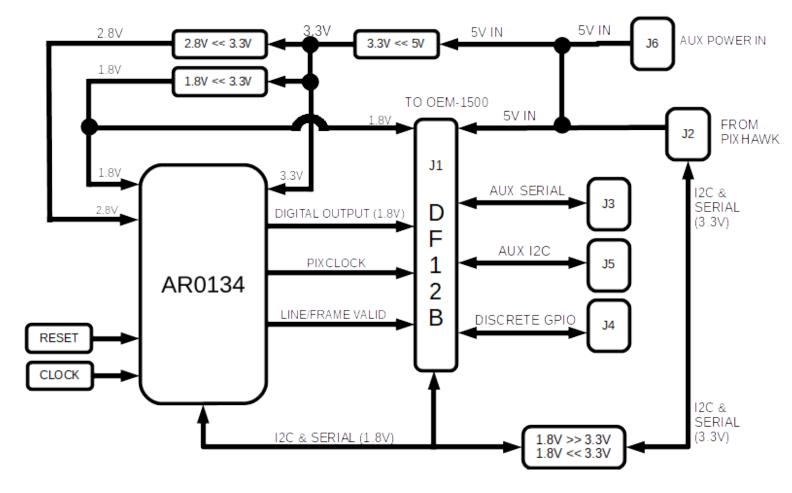


Figure 1: Level 1 Block Diagram of SLA 1500 CAM REV 7.0

LABEL	MFG PART NUMBER	FUNCTION	MATES WITH
		50-Pin Digital Connector, Connects	HIROSE 1500-OEM J4 [DF12B(5.0)-
J1	HIROSE DF12B-50DS-0.5V(86)	Directly to OEM-1500 J4	50DP-0.5V(86)]
		5VDC Power, Serial and I2C	
J2	JST SM06B-GH	Communication With Pixhawk	JST GHR-06V-S
		Auxillary Serial Communications	
J3	MOLEX 53261-0371	for Sightline Debugging	MOLEX 51021-0300 / CAB-0302
J4	MOLEX 53261-0471	Discrete GPIO Lines from 1500-	MOLEX 51021-0400 / CAB-0401
J5	MOLEX 53261-0471	Auxillary I2C Bus to Sightline	MOLEX 51021-0400 / CAB-0401
J6	MOLEX 53261-0471	Auxillary 5VDC Power In	MOLEX 51021-0400 / CAB-0401

Figure 2: *Summary of Connectors* 

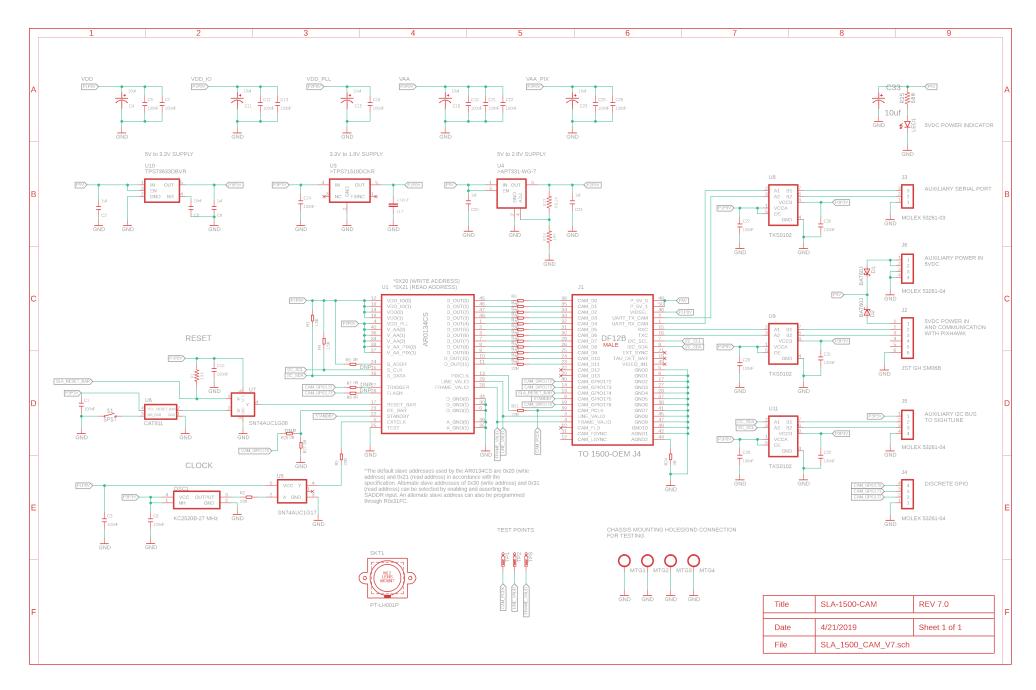


Figure 3: SLA 1500 CAM REV 7.0 Schematic