

# **dsPIC30F/dsPIC33F/dsPIC33E/PIC24H/PIC24E I<sup>2</sup>C™ Peripheral Module Library Help**

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## 1 Library Features

This peripheral library module provides:

- Module initialization functions for both master and slave modes
- Simple functions to read/write one or multiple bytes
- Functions to read registers and configure interrupts
- Simple interface macros to enable/disable interrupts

## 2 Using the Library Functions in Your Code

Library routine parameters can be constructed using an AND-based mask. For more information on this mask, see [16-bit Peripheral Libraries](#). An example of use is shown below.

### dsPIC30F Example of Use ( AND mask )

```
#include<i2c.h>

void main(void )
{
    unsigned int config2, config1;
    unsigned char *wrptr;
    unsigned char tx_data[] = {'M','I','C','R','O','C','H','I','P','\0'};
    wrptr = tx_data;
    /* Baud rate is set for 100 kHz */
    config2 = 0x11;
    /* Configure I2C for 7 bit address mode */
    config1 = (I2C_ON & I2C_IDLE_CON & I2C_CLK_HLD &
               I2C_IPMI_DIS & I2C_7BIT_ADD &
               I2C_SLW_DIS & I2C_SM_DIS &
               I2C_GCALL_DIS & I2C_STR_DIS &
               I2C_NACK & I2C_ACK_DIS & I2C_RCV_DIS &
               I2C_STOP_DIS & I2C_RESTART_DIS &
               I2C_START_DIS);
    OpenI2C(config1,config2);
    IdleI2C();
    StartI2C();
    /* Wait till Start sequence is completed */
    while(I2CCONbits.SEN);
    /* Clear interrupt flag */
    IFS0bits.MI2CIF = 0;
    /* Write Slave address and set master for transmission */
    MasterWriteI2C(0xE);
    /* Wait till address is transmitted */
    while(I2CSTATbits.TBF); // 8 clock cycles
    while(!IFS0bits.MI2CIF); // Wait for 9th clock cycle
    IFS0bits.MI2CIF = 0; // Clear interrupt flag
    while(I2CSTATbits.ACKSTAT);
    /* Transmit string of data */
    MasterputsI2C(wrptr);
    StopI2C();
}
```

```

/* Wait till stop sequence is completed */
while(I2CCONbits.PEN);
CloseI2C();
}

```

### 3 Functions

#### 3.1 AckI2C

|                           |  |
|---------------------------|--|
| <b>Function Prototype</b> | <code>void AckI2C(void);</code>  |
| <b>Include</b>            | <code>i2c.h</code>   |
| <b>Description</b>        | Generates I <sup>2</sup> C bus Acknowledge condition.                  |
| <b>Arguments</b>          | None   |
| <b>Return Value</b>       | None   |
| <b>Remarks</b>            | This function generates an I <sup>2</sup> C bus Acknowledge condition. |
| <b>Source File</b>        | <code>AckI2C.c</code>  |
| <b>Code Example</b>       | <code>AckI2C();</code>   |

#### 3.2 CloseI2C

|                           |  |
|---------------------------|--|
| <b>Function Prototype</b> | <code>void CloseI2C(void);</code>  |
| <b>Include</b>            | <code>i2c.h</code>   |
| <b>Description</b>        | This function turns off the I <sup>2</sup> C module.   |
| <b>Arguments</b>          | None   |
| <b>Return Value</b>       | None   |
| <b>Remarks</b>            | This function disables the I <sup>2</sup> C module and clears the Master and Slave Interrupt Enable and Flag bits. |
| <b>Source File</b>        | <code>CloseI2C.c</code>  |
| <b>Code Example</b>       | <code>CloseI2C();</code>   |

#### 3.3 ConfigIntI2C

|                           |  |
|---------------------------|--|
| <b>Function Prototype</b> | <code>void ConfigIntI2C(unsigned int config);</code>   |
| <b>Include</b>            | <code>i2c.h</code>   |
| <b>Description</b>        | This function configures the I <sup>2</sup> C interrupt.   |
| <b>Arguments</b>          | <p><i>config</i> - I<sup>2</sup>C interrupt priority and enable/disable information as defined below:</p> <p>I<sup>2</sup>C master Interrupt enable/disable</p> <p style="margin-left: 40px;"><code>MI2C_INT_ON</code></p> <p style="margin-left: 40px;"><code>MI2C_INT_OFF</code></p> <p>I<sup>2</sup>C slave Interrupt enable/disable</p> <p style="margin-left: 40px;"><code>SI2C_INT_ON</code></p> <p style="margin-left: 40px;"><code>SI2C_INT_OFF</code></p> <p>I<sup>2</sup>C master Interrupt priority</p> <p style="margin-left: 40px;"><code>MI2C_INT_PRI_7</code></p> <p style="margin-left: 40px;"><code>MI2C_INT_PRI_6</code></p> <p style="margin-left: 40px;"><code>MI2C_INT_PRI_5</code></p> <p style="margin-left: 40px;"><code>MI2C_INT_PRI_4</code></p> <p style="margin-left: 40px;"><code>MI2C_INT_PRI_3</code></p> <p style="margin-left: 40px;"><code>MI2C_INT_PRI_2</code></p> |

```
MI2C_INT_PRI_1
MI2C_INT_PRI_0
I2C slave Interrupt priority
SI2C_INT_PRI_7
SI2C_INT_PRI_6
SI2C_INT_PRI_5
SI2C_INT_PRI_4
SI2C_INT_PRI_3
SI2C_INT_PRI_2
SI2C_INT_PRI_1
SI2C_INT_PRI_0
```

|              |   |
|--------------|---|
| Return Value | None.   |
| Remarks      | This function clears the Interrupt Flag bits, sets the interrupt priorities of master and slave and enables/disables the interrupt. |
| Source File  | ConfigIntI2C.c  |
| Code Example | ConfigIntI2C(MI2C_INT_ON & MI2C_INT_PRI_3<br>& SI2C_INT_ON & SI2C_INT_PRI_5);   |

3.4 DataRdyI2C

|              |  |
|--------------|--|
| Function     | unsigned char DataRdyI2C(void);  |
| Prototype    |  |
| Include      | i2c.h  |
| Description  | This function provides status back to the user if the I2CRCV register contains data.                                       |
| Arguments    | None   |
| Return Value | This function returns '1' if there is data in I2CRCV register; else return '0' which indicates no data in I2CRCV register. |
| Remarks      | This function determines if there are any bytes to read from the I2CRCV register.  |
| Source File  | DataRdyI2C.c   |
| Code Example | if(DataRdyI2C());  |

3.5 IdleI2C

|              |   |
|--------------|---|
| Function     | void IdleI2C(void);   |
| Prototype    |   |
| Include      | i2c.h   |
| Description  | This function generates Wait condition until I2C bus is Idle.   |
| Arguments    | None  |
| Return Value | None  |
| Remarks      | This function will be in a wait state until Start Condition Enable bit, Stop Condition Enable bit, Receive Enable bit, Acknowledge Sequence Enable bit of I2C Control register and Transmit Status bit I2C Status register are clear. The IdleI2C function is required since the hardware I2C peripheral does not allow for spooling of bus sequence. The I2C peripheral must be in Idle state before an I2C operation can be initiated or write collision will be generated. |
| Source File  | IdleI2C.c   |
| Code Example | IdleI2C();  |

3.6 MastergetsI2C

|          |   |
|----------|---|
| Function | unsigned int MastergetsI2C(unsigned int length, |
|----------|---|

|                     |   |
|---------------------|---|
| <b>Prototype</b>    | <code>unsigned char *rdpctr, unsigned int i2c_data_wait);</code>  |
| <b>Include</b>      | <code>i2c.h</code>  |
| <b>Description</b>  | This function reads predetermined data string length from the I <sup>2</sup> C bus.   |
| <b>Arguments</b>    | <p><i>length</i> - Number of bytes to read from I<sup>2</sup>C device.</p> <p><i>rdpctr</i> -Character type pointer to RAM for storage of data read from I<sup>2</sup>C device</p> <p><i>i2c_data_wait</i> - This is the time-out count for which the module has to wait before return. If the time-out count is 'N', the actual time out would be about (20 * N – 1) instruction cycles.</p> |
| <b>Return Value</b> | This function returns '0' if all bytes have been sent or number of bytes read from I <sup>2</sup> C bus if its not able to read the data within the specified <i>i2c_data_wait</i> time out value   |
| <b>Remarks</b>      | This routine reads a predefined data string from the I <sup>2</sup> C bus.  |
| <b>Source File</b>  | <code>MastergetsI2C.c</code>  |
| <b>Code Example</b> | <pre> unsigned char string[10]; unsigned char *rdpctr; unsigned int length, i2c_data_wait; length = 9; rdpctr = string; i2c_data_wait = 152; MastergetsI2C(length, rdpctr, i2c_data_wait); </pre>   |

### 3.7 MasterputsI2C

|                     |   |
|---------------------|---|
| <b>Function</b>     | <code>unsigned int MasterputsI2C(unsigned char *wrpctr);</code>   |
| <b>Prototype</b>    |   |
| <b>Include</b>      | <code>i2c.h</code>  |
| <b>Description</b>  | This function is used to write out a data string to the I <sup>2</sup> C bus. .   |
| <b>Arguments</b>    | <i>wrpctr</i> - Character type pointer to data objects in RAM. The data objects are written to the I <sup>2</sup> C device.   |
| <b>Return Value</b> | This function returns '-3' if a write collision occurred. This function returns '0' if the null character was reached in data string.   |
| <b>Remarks</b>      | This function writes a string to the I <sup>2</sup> C bus until a null character is reached. Each byte is written via a call to the <code>MasterputcI2C</code> function. The actual called function body is termed <code>MasterWriteI2C</code> . <code>MasterWriteI2C</code> and <code>MasterputcI2C</code> refer to the same function via a <code>#define</code> statement in the <code>i2c.h</code> |
| <b>Source File</b>  | <code>MasterputsI2C.c</code>  |
| <b>Code Example</b> | <pre> unsigned char string[] = " MICROCHIP "; unsigned char *wrpctr; wrpctr = string; MasterputsI2C( wrpctr); </pre>  |

### 3.8 MasterReadI2C

|                     |  |
|---------------------|--|
| <b>Function</b>     | <code>unsigned char MasterReadI2C(void);</code>  |
| <b>Prototype</b>    |  |
| <b>Include</b>      | <code>i2c.h</code>   |
| <b>Description</b>  | This function is used to read a single byte from I <sup>2</sup> C bus.   |
| <b>Arguments</b>    | None   |
| <b>Return Value</b> | The return value is the data byte read from the I <sup>2</sup> C bus.  |
| <b>Remarks</b>      | This function reads in a single byte from the I <sup>2</sup> C bus. This function performs the same function as <code>MastergetcI2C</code> . |
| <b>Source File</b>  | <code>MasterReadI2C.c</code>   |

**Code Example**      `unsigned char value;  
value = MasterReadI2C();`

### 3.9 MasterWriteI2C

**Function Prototype**      `unsigned char MasterWriteI2C(unsigned char data_out);`

**Include**      `i2c.h`

**Description**      This function is used to write out a single data byte to the I<sup>2</sup>C device.

**Arguments**      `data_out`  
A single data byte to be written to the I<sup>2</sup>C bus device.

**Return Value**      This function returns '-1' if there was a write collision else it returns a '0'.

**Remarks**      This function writes out a single data byte to the I<sup>2</sup>C bus device. This function performs the same function as MasterputcI2C.

**Source File**      `MasterWriteI2C.c`

**Code Example**      `MasterWriteI2C('a');`

### 3.10 NotAckI2C

**Function Prototype**      `void NotAckI2C(void);`

**Include**      `i2c.h`

**Description**      Generates I<sup>2</sup>C bus Not Acknowledge condition.

**Arguments**      None

**Return Value**      None

**Remarks**      This function generates an I<sup>2</sup>C bus *Not Acknowledge* condition.

**Source File**      `NotAckI2C.c`

**Code Example**      `NotAckI2C();`

### 3.11 OpenI2C

**Function Prototype**      `void OpenI2C(unsigned int config1,  
                  unsigned int config2);`

**Include**      `i2c.h`

**Description**      Configures the I<sup>2</sup>C module.

**Arguments**      `config1` - This contains the parameter to configure the I2CCON register.  
I<sup>2</sup>C Enable bit  
          `I2C_ON`  
          `I2C_OFF`  
I<sup>2</sup>C Stop in Idle Mode bit  
          `I2C_IDLE_STOP`  
          `I2C_IDLE_CON`  
SCL Release Control bit  
          `I2C_CLK_REL`  
          `I2C_CLK_HLD`  
Intelligent Peripheral Management Interface Enable bit  
          `I2C_IPMI_EN`  
          `I2C_IPMI_DIS`  
10-bit Slave Address bit  
          `I2C_10BIT_ADD`  
          `I2C_7BIT_ADD`  
Disable Slew Rate Control bit

I2C\_SLW\_DIS  
I2C\_SLW\_EN

SMBus Input Level bits

I2C\_SM\_EN  
I2C\_SM\_DIS

General Call Enable bit

I2C\_GCALL\_EN  
I2C\_GCALL\_DIS

SCL Clock Stretch Enable bit

I2C\_STR\_EN  
I2C\_STR\_DIS

Acknowledge Data bit

I2C\_ACK  
I2C\_NACK

Acknowledge Sequence Enable bit

I2C\_ACK\_EN  
I2C\_ACK\_DIS

Receive Enable bit

I2C\_RCV\_EN  
I2C\_RCV\_DIS

Stop Condition Enable bit

I2C\_STOP\_EN  
I2C\_STOP\_DIS

Repeated Start Condition Enable bit

I2C\_RESTART\_EN  
I2C\_RESTART\_DIS

Start Condition Enable bit

I2C\_START\_EN  
I2C\_START\_DIS

*config2* - computed value for the baud rate generator

Return Value  
None

Remarks  
This function configures the I<sup>2</sup>C Control register and I<sup>2</sup>C Baud Rate Generator register.

Source File  
OpenI2C.c

Code Example  
OpenI2C(I2C\_ON & I2C\_IDLE\_IDLE\_CON);

3.12 RestartI2C

Function Prototype  
void RestartI2C(void);

Include  
i2c.h

Description  
Generates I<sup>2</sup>C bus Not Acknowledge condition.

Arguments  
None

Return Value  
None

Remarks  
This function generates an I<sup>2</sup>C Bus Restart condition.

Source File  
RestartI2C.c

Code Example  
RestartI2C();

3.13 SlavegetsI2C

Function  
Prototype

unsigned int SlavegetsI2C(unsigned char \*rdptr,  
unsigned int i2c\_data\_wait);



|                     |  |
|---------------------|--|
| <b>Include</b>      | <code>i2c.h</code>   |
| <b>Description</b>  | This function reads pre-determined data string length from the I <sup>2</sup> C bus.   |
| <b>Arguments</b>    | <i>rdptr</i> - Character type pointer to RAM for storage of data read from the I <sup>2</sup> C device.<br><i>i2c_data_wait</i> - This is the time-out count for which the module has to wait before return. If the time-out count is 'N', the actual time out would be about (20*N - 1) instruction cycles. |
| <b>Return Value</b> | Returns the number of bytes received from the I <sup>2</sup> C bus.  |
| <b>Remarks</b>      | This routine reads a predefined data string from the I <sup>2</sup> C bus.   |
| <b>Source File</b>  | <code>SlavegetsI2C.c</code>  |
| <b>Code Example</b> | <pre>unsigned char string[12]; unsigned char *rdptr; rdptr = string; i2c_data_out = 0x11; SlavegetsI2C(rdptr, i2c_data_wait);</pre>  |

### 3.14 SlaveputsI2C

|                           |  |
|---------------------------|--|
| <b>Function Prototype</b> | <code>unsigned int SlaveputsI2C(unsigned char *wrptr);</code>  |
| <b>Include</b>            | <code>i2c.h</code>   |
| <b>Description</b>        | This function is used to write out a data string to the I <sup>2</sup> C bus.  |
| <b>Arguments</b>          | <i>wrptr</i> - Character type pointer to data objects in RAM. The data objects are written to the I <sup>2</sup> C device. |
| <b>Return Value</b>       | This function returns '0' if the null character was reached in the data string.  |
| <b>Remarks</b>            | This routine reads a predefined data string from the I <sup>2</sup> C bus.   |
| <b>Source File</b>        | <code>SlavegetsI2C.c</code>  |
| <b>Code Example</b>       | <pre>unsigned char string[] = "MICROCHIP"; unsigned char *rdptr; rdptr = string; SlaveputsI2C(rdptr);</pre>                |

### 3.15 SlaveReadI2C

|                           |   |
|---------------------------|---|
| <b>Function Prototype</b> | <code>unsigned char SlaveReadI2C(void);</code>  |
| <b>Include</b>            | <code>i2c.h</code>  |
| <b>Description</b>        | This function is used to read a single byte from the I <sup>2</sup> C bus.  |
| <b>Arguments</b>          | None  |
| <b>Return Value</b>       | The return value is the data byte read from the I <sup>2</sup> C bus.   |
| <b>Remarks</b>            | This function reads in a single byte from the I <sup>2</sup> C bus. This function performs the same function as <code>SlavegetcI2C</code> . |
| <b>Source File</b>        | <code>SlaveReadI2C.c</code>   |
| <b>Code Example</b>       | <pre>unsigned char value; value = SlaveReadI2C();</pre>   |

### 3.16 SlaveWriteI2C

|                           |   |
|---------------------------|---|
| <b>Function Prototype</b> | <code>void SlaveWriteI2C(unsigned char data_out);</code>                      |
| <b>Include</b>            | <code>i2c.h</code>  |
| <b>Description</b>        | This function is used to write out a single byte to the I <sup>2</sup> C bus. |

|                     |   |
|---------------------|---|
| <b>Arguments</b>    | <i>data_out</i> - A single data byte to be written to the I <sup>2</sup> C bus device.  |
| <b>Return Value</b> | None  |
| <b>Remarks</b>      | This function writes out a single data byte to the I <sup>2</sup> C bus device. This function performs the same function as <code>SlaveputcI2C</code> . |
| <b>Source File</b>  | <code>SlaveWriteI2C.c</code>  |
| <b>Code Example</b> | <code>SlaveWriteI2C('a');</code>  |

### 3.17 StartI2C

|                           |   |
|---------------------------|---|
| <b>Function Prototype</b> | <code>void StartI2C(void);</code>                               |
| <b>Include</b>            | <code>i2c.h</code>  |
| <b>Description</b>        | Generates I <sup>2</sup> C Bus Start condition.                 |
| <b>Arguments</b>          | None  |
| <b>Return Value</b>       | None  |
| <b>Remarks</b>            | This function generates a I <sup>2</sup> C Bus Start condition. |
| <b>Source File</b>        | <code>StartI2C.c</code>   |
| <b>Code Example</b>       | <code>StartI2C();</code>  |

### 3.18 StopI2C

|                           |  |
|---------------------------|--|
| <b>Function Prototype</b> | <code>void StopI2C(void);</code>                               |
| <b>Include</b>            | <code>i2c.h</code>   |
| <b>Description</b>        | Generates I <sup>2</sup> C Bus Stop condition.                 |
| <b>Arguments</b>          | None   |
| <b>Return Value</b>       | None   |
| <b>Remarks</b>            | This function generates a I <sup>2</sup> C Bus Stop condition. |
| <b>Source File</b>        | <code>StopI2C.c</code>   |
| <b>Code Example</b>       | <code>StopI2C();</code>  |

## 4 Macros

### 4.1 EnableIntMI2C

|                     |  |
|---------------------|--|
| <b>Macro</b>        | <code>EnableIntMI2C</code>   |
| <b>Overview</b>     | This macro sets Master I <sup>2</sup> C Enable bit of Interrupt Enable Control register. |
| <b>Input</b>        | None   |
| <b>Output</b>       | None   |
| <b>Remarks</b>      | None   |
| <b>Code Example</b> | <code>EnableIntMI2C;</code>  |

### 4.2 DisableIntMI2C

|                     |  |
|---------------------|--|
| <b>Macro</b>        | <code>DisableIntMI2C</code>  |
| <b>Overview</b>     | This macro clears Master I <sup>2</sup> C Interrupt Enable bit of Interrupt Enable Control register. |
| <b>Input</b>        | None   |
| <b>Output</b>       | None   |
| <b>Remarks</b>      | None   |
| <b>Code Example</b> | <code>DisableIntMI2C;</code>   |

### 4.3 SetPriorityIntMI2C

|                     |   |
|---------------------|---|
| <b>Macro</b>        | <code>SetPriorityIntMI2C</code>   |
| <b>Overview</b>     | This macro sets Master I <sup>2</sup> C Interrupt Priority bits of Interrupt Priority Control register. |
| <b>Input</b>        | Interrupt Priority Level.   |
| <b>Output</b>       | None  |
| <b>Remarks</b>      | None  |
| <b>Code Example</b> | <code>SetPriorityIntMI2C(7);</code>   |

#### 4.4 *EnableIntSI2C*

|                     |   |
|---------------------|---|
| <b>Macro</b>        | <code>EnableIntSI2C</code>  |
| <b>Overview</b>     | This macro sets Slave I <sup>2</sup> C Enable bit of Interrupt Enable Control register. |
| <b>Input</b>        | None  |
| <b>Output</b>       | None  |
| <b>Remarks</b>      | None  |
| <b>Code Example</b> | <code>EnableIntSI2C;</code>   |

#### 4.5 *DisableIntSI2C*

|                     |   |
|---------------------|---|
| <b>Macro</b>        | <code>DisableIntSI2C</code>   |
| <b>Overview</b>     | This macro clears Slave I <sup>2</sup> C Enable bit of Interrupt Enable Control register. |
| <b>Input</b>        | None  |
| <b>Output</b>       | None  |
| <b>Remarks</b>      | None  |
| <b>Code Example</b> | <code>DisableIntSI2C;</code>  |

#### 4.6 *SetPriorityIntSI2C*

|                     |  |
|---------------------|--|
| <b>Macro</b>        | <code>SetPriorityIntSI2C</code>  |
| <b>Overview</b>     | This macro sets Slave I <sup>2</sup> C Interrupt Priority bits of Interrupt Priority Control register. |
| <b>Input</b>        | Interrupt Priority Level.  |
| <b>Output</b>       | None   |
| <b>Remarks</b>      | None   |
| <b>Code Example</b> | <code>SetPriorityIntSI2C(7);</code>  |