



**Notes:**

- Physical power inputs: 3V3, 5V, 12V, -12V
- All power inputs include polarity protection
- Will be using 12.288 MHz to cover all possible conversion rates of CS5381
- VA Analog MIN is 4.25V to 5.25V (Should be 5V)
- VD Digital MIN is 2.1V to 5.25V
- VL Logic MIN is 2.37V to 5.25V
- VD pin powers the chip's digital core logic - the internal digital processing circuitry that handles things like: (Delta-sigma modulator output, Digital filters (decimation, etc.), Sample rate control logic, Configuration control logic (I2C/SPI core))
- VL (Logic 1 = VS) controls MCLK, SCLK, LRCK
- Power from USB: pins D+/D- left optionally floating or shorted

