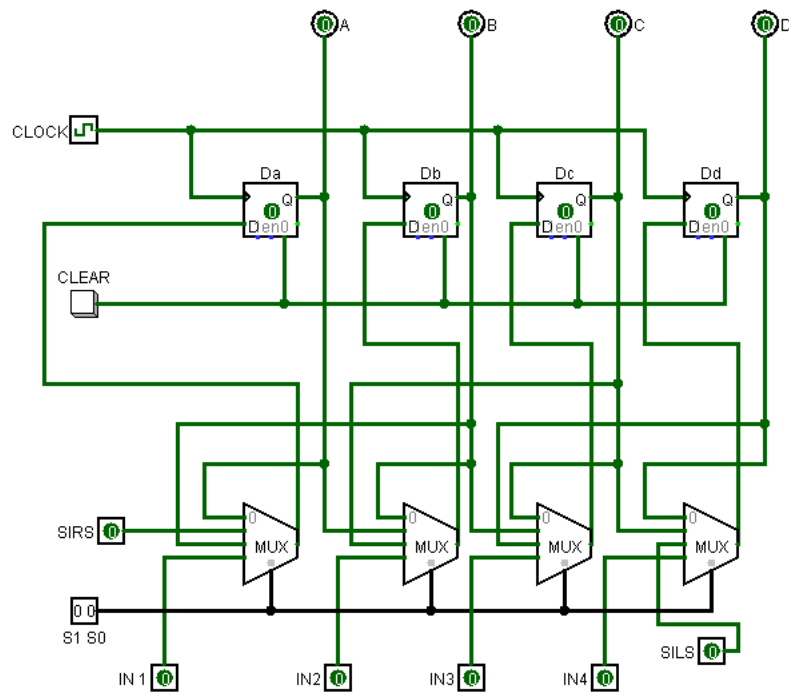


# DD Lab 9 Assignment

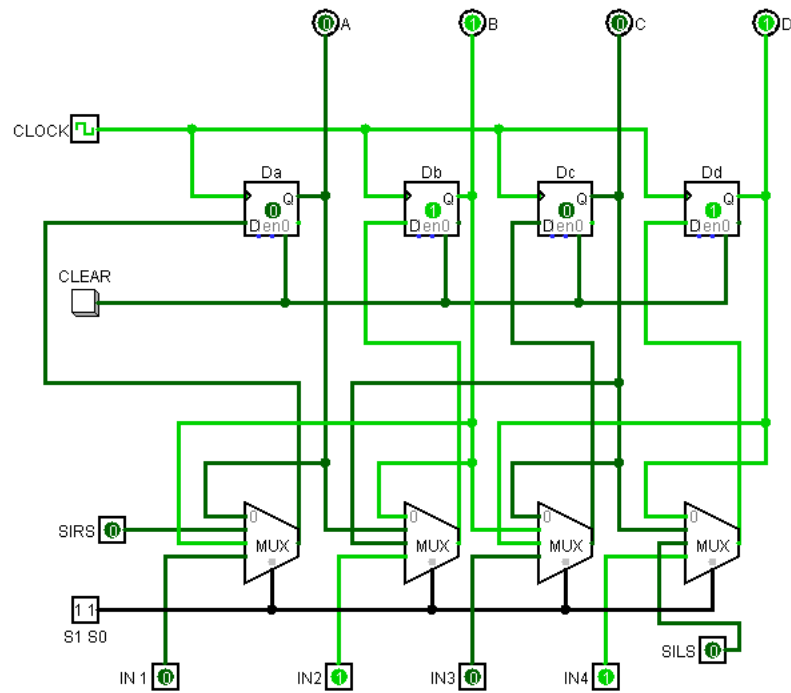
Sai Kartik  
2020A3PS0435P

November 20, 2021

Circuit diagram in the no change state



# 1 Circuit diagram in the load state



## 2 Left shifting the loaded register

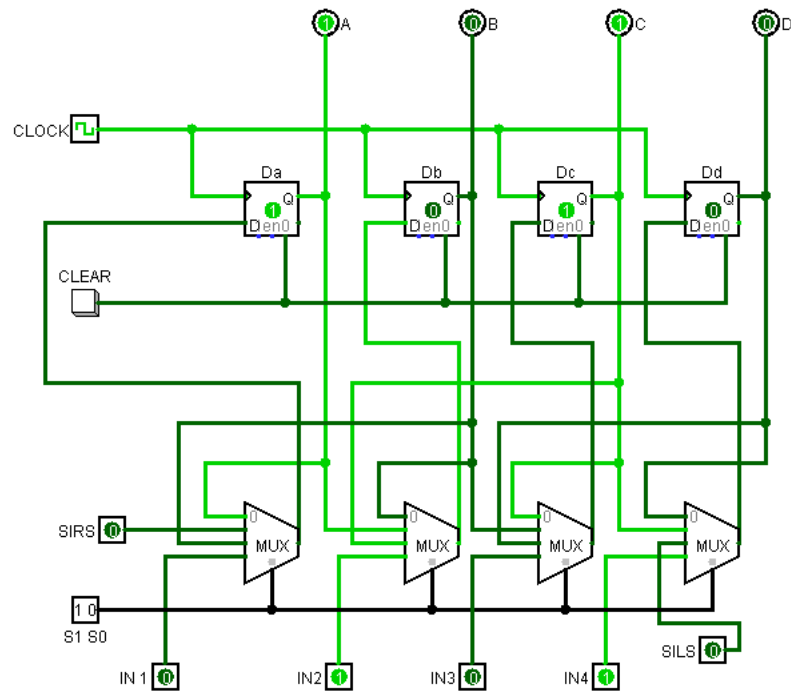


Figure 1: Left shifting once

### 3 Right shifting the initial loaded register twice

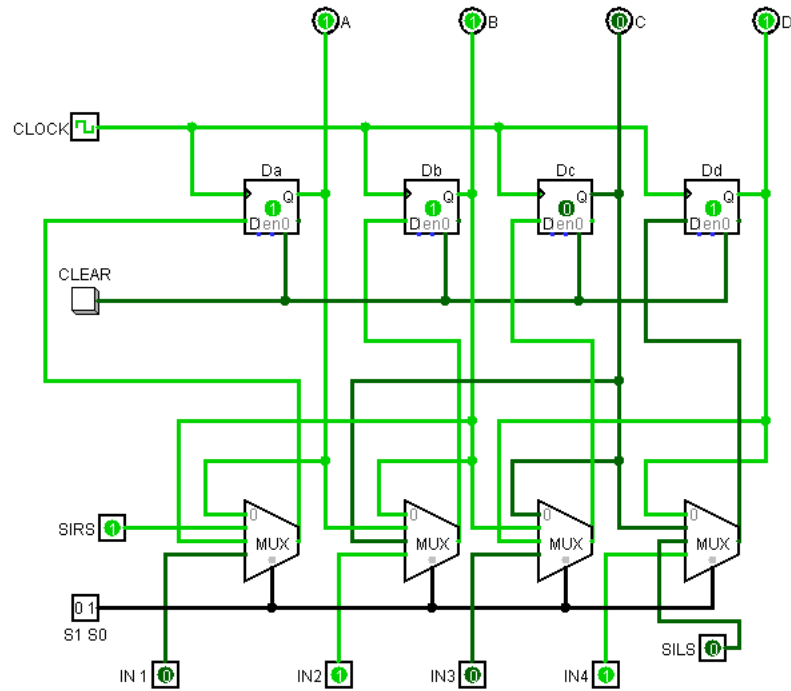


Figure 2: Right shifting twice