DA1469x DevKit Pro Mainboard

Design Name	2522-mb-pro
Ref. Number	331-07-C
Version	C1.0
Date	Nov 13, 2018
Designer	PR

History Table

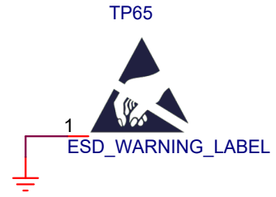
Version	Date	Comments
A0.0	04 / Oct / 2017	Draft for internal review
	26 / Oct / 2017	Release for internal review
	16 / Nov / 2017	Release for final review
A1.0	22 / Nov / 2017	Release to manufacturing
B0.0	05 / Jan / 2018	Set R4, D9 as not populated
	30 / Jan / 2018	Changed TP3216 footprint to single pad (TP28, TP29)
	05 / Feb / 2018	Changed C88 to non-populated
	24 / Apr / 2018	Changed Q1,Q2,Q3 type to BSS138L Changed U21 OE pin connection - R4 populated, connected to VDDIO - R149 set as not populated Added options to drive BRSTn (MikroBUS/Arduino reset) - R309 -> always high (default) - R308 -> controlled by P0_12 Set D3 as not populated (disable reset from J-Link) Added options for current measurement circuit - R310, R311 (N.P.) - R312, R313 (O ohm) - C119, C120 (N.P.) - C121, C122 (1uF) - R314 (N.P.)
B1.0	15 / May / 2018	Released to manufacturing
C0.0	19 / Sep / 2018	Changed R308 to 1k, R309 to N.P. (BRSTn controlled by P0_12) Changed Q1,Q2,Q3 type to FDV301N Added bottom testpoints for T_SWDIO, T_SWCLK Changed Arduino/MikroBUS D0..D5 to 3V-capable GPIOs Removed R86..R91 Changed D3, D8 type to RB531SM-30 Set D3 as populated -> allow J-Link hardware reset Rearranged the signals on J10 (haptic motor) and added a jumper shunt to enable/disable JTAG reset Changed R148 to N.P. Updated several parts as obsoleted/EOL and some with environmental compliance problems
C1.0	21 / Nov / 2018	Released to manufacturing

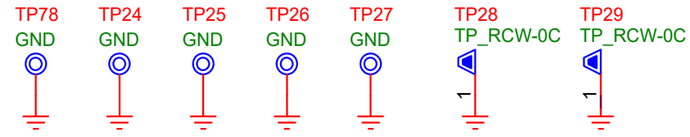
Configuration note

NOTE box

Changes in this Version.

TP71 FIDUCIAL
TP72 FIDUCIAL
TP73 FIDUCIAL
TP45 FIDUCIAL
TP46 FIDUCIAL
TP47 FIDUCIAL





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Dialog Semiconductor
N.E.O. Patron-Athinon 15
26441, Patras, Greece
tel. (+30) 2610390940
fax. (+30) 2610390941

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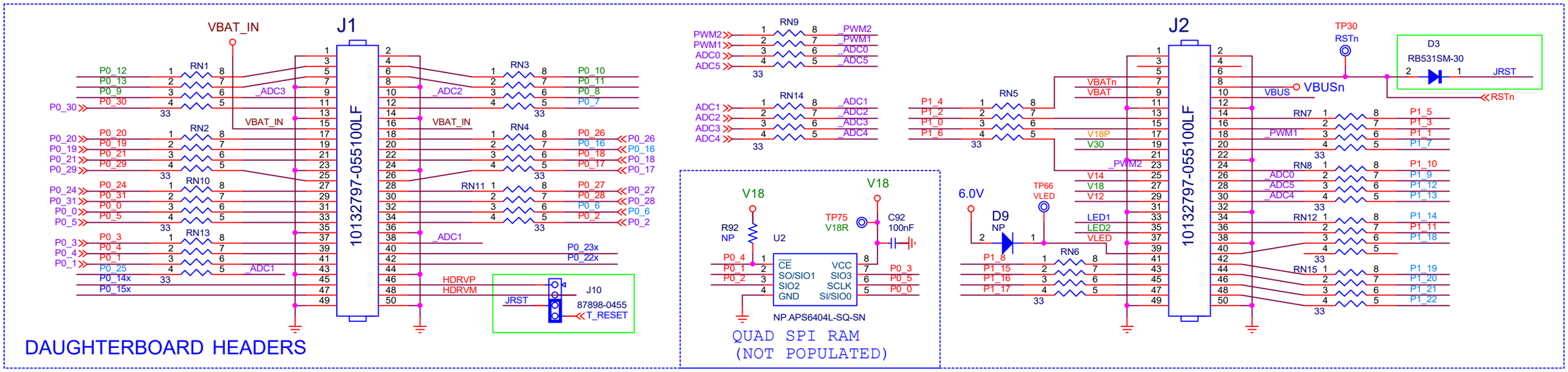

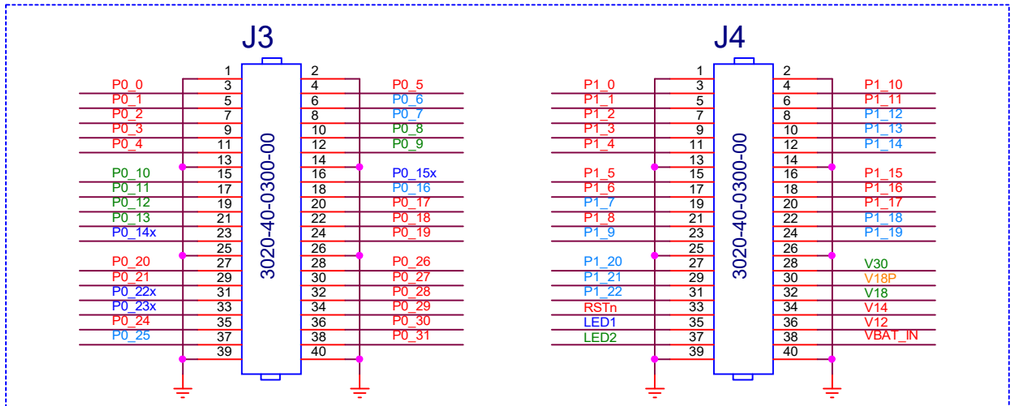

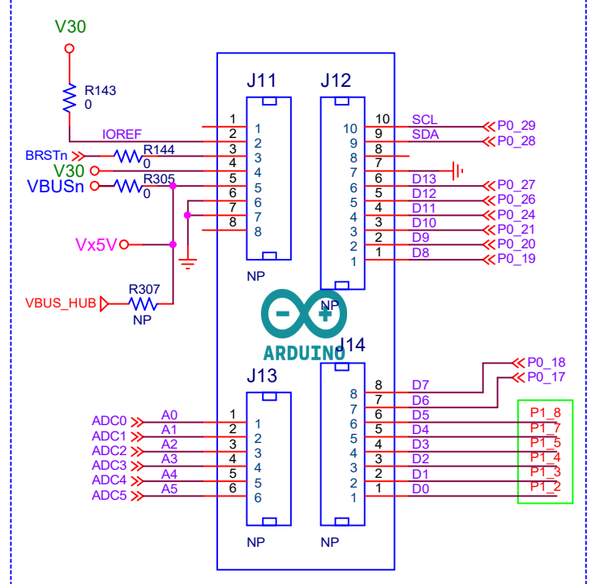

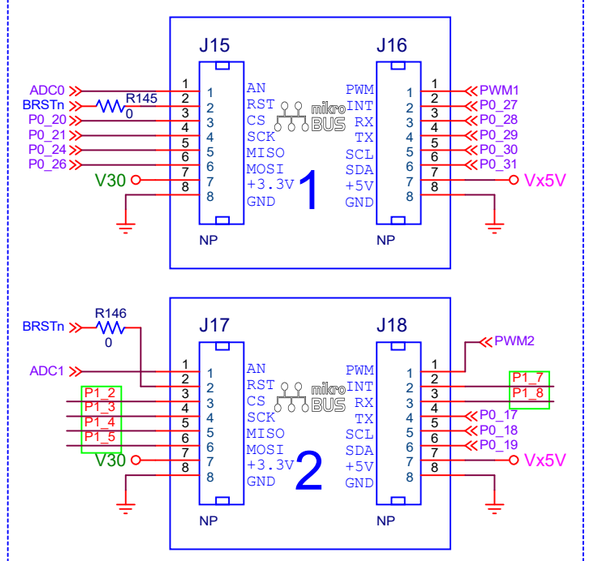

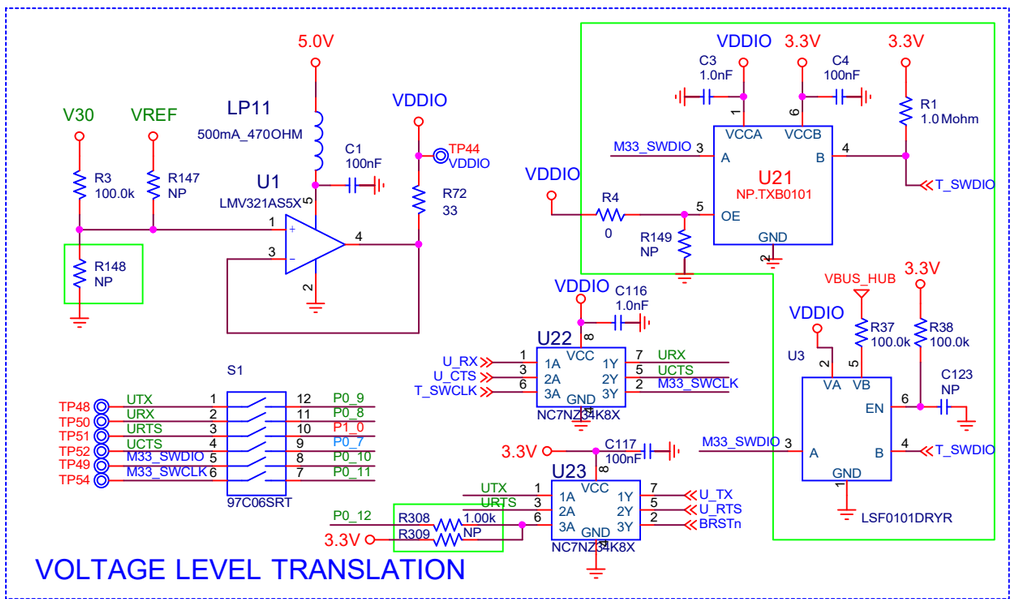

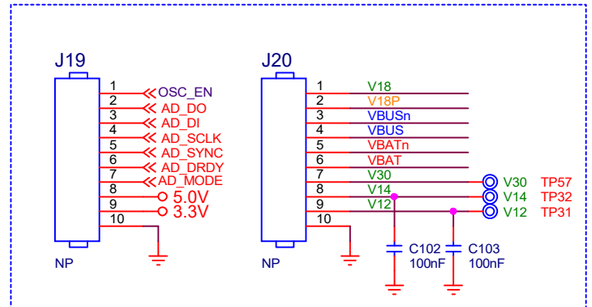

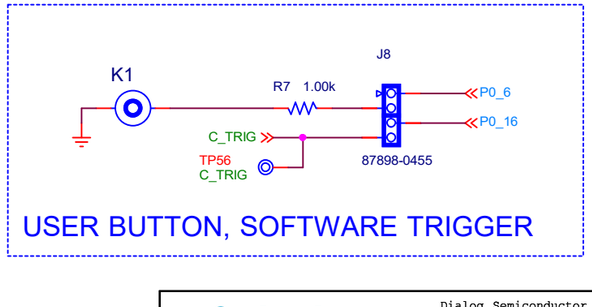

Dialog Semiconductor
N.E.O. Patron-Athinon 15
26441, Patras, Greece
tel. (+30) 2610390940
fax. (+30) 2610390941

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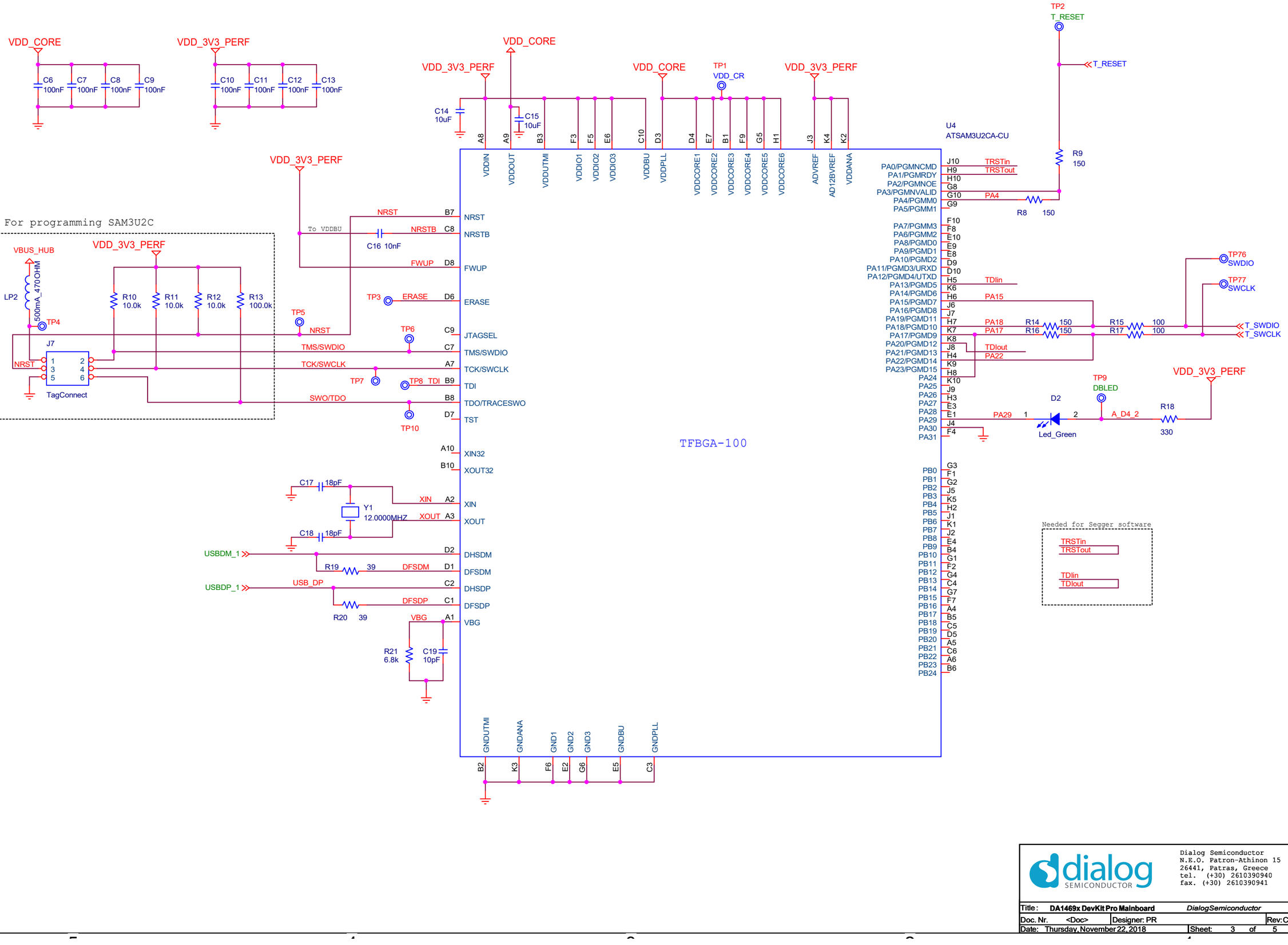

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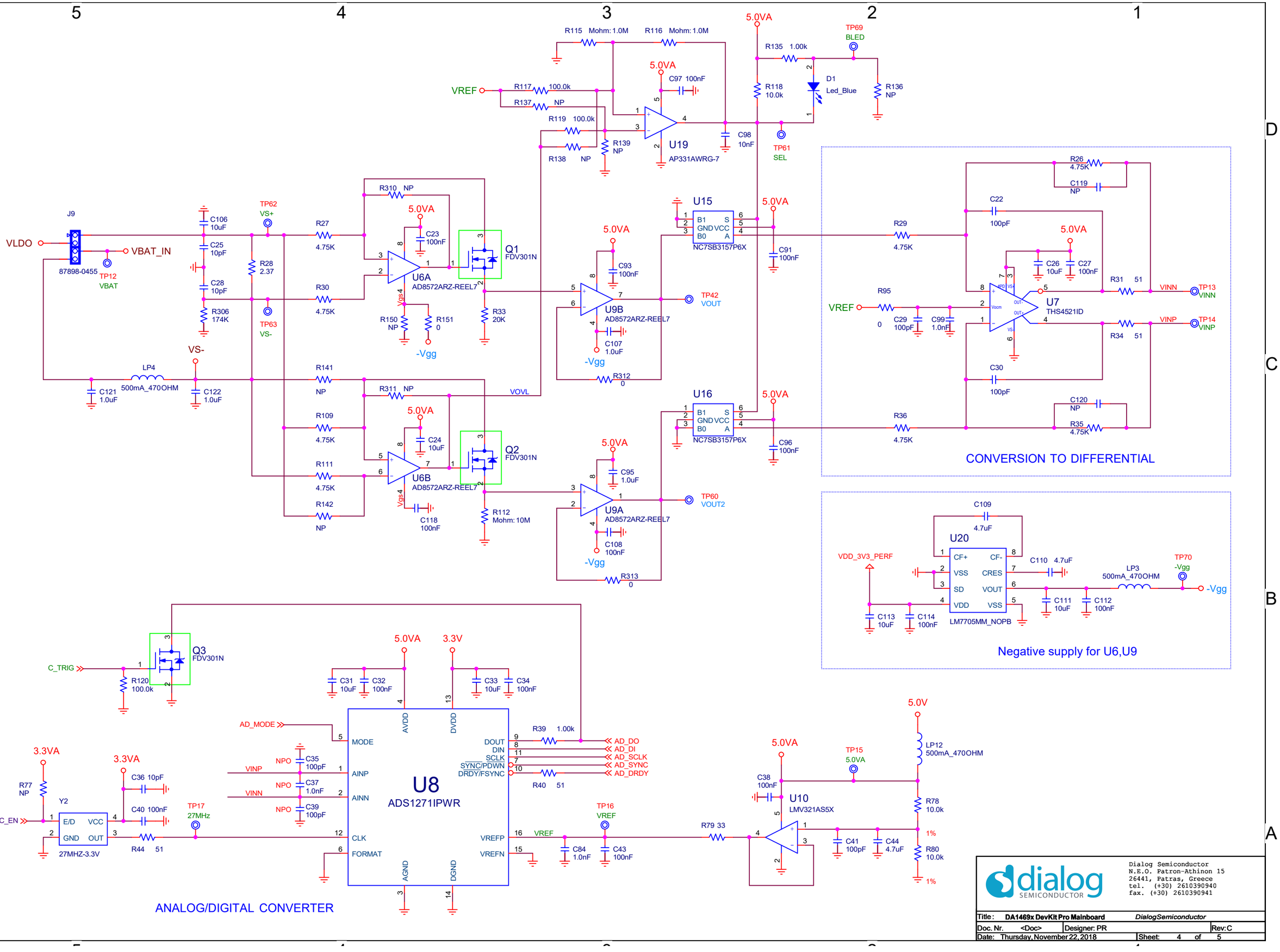
R115 Mohm: 1.0M
R116 Mohm: 1.0M
5.0VA
TP69
BLED
R135 1.00k
VREF
R117 100.0k
R137 NP
R119 100.0k
R138 NP
R139 NP
C97 100nF
U19
AP331AWRG-7
R118 10.0k
D1
Led_Blue
R136 NP
C98 10nF
TP61
SEL
J9
VLDO
VBAT_IN
87898-0455
TP12
VBAT
C106 10uF
C25 10pF
C28 10pF
R306 174K
TP62
VS+
TP63
VS-
R28 2.37
R27 4.75K
R30 4.75K
R310 NP
C23 100nF
U6A
AD8572ARZ-REEL7
R150 NP
R151 0
R33 20K
-Vgg
Q1
FDV301N
U15
NC7SB3157P6X
C91 100nF
C93 100nF
U9B
AD8572ARZ-REEL7
C107 1.0uF
R312 0
TP42
VOUT
LP4
500mA_470OHM
C121 1.0uF
C122 1.0uF
R141 NP
R311 NP
VOVL
R109 4.75K
R111 4.75K
R142 NP
C24 10uF
U6B
AD8572ARZ-REEL7
C118 100nF
Q2
FDV301N
R112 Mohm: 10M
U16
NC7SB3157P6X
C96 100nF
C95 1.0uF
U9A
AD8572ARZ-REEL7
C108 100nF
R313 0
TP60
VOUT2
R26 4.75K
C119 NP
C22 100pF
R29 4.75K
C26 10uF
C27 100nF
R95 0
C29 100pF
C99 1.0nF
U7
THS4521ID
R31 51
R34 51
VINN
VINP
TP13
VINN
TP14
VINP
C30 100pF
C120 NP
R35 4.75K
R36 4.75K
CONVERSION TO DIFFERENTIAL
C109 4.7uF
U20
VDD_3V3_PERF
C113 10uF
C114 100nF
LM7705MM_NOPB
C110 4.7uF
C111 10uF
C112 100nF
LP3
500mA_470OHM
TP70
-Vgg
Negative supply for U6,U9
C_TRIG
Q3
FDV301N
R120 100.0k
3.3VA
R77 NP
Y2
OSC_EN
E/D
VCC
GND
OUT
27MHZ-3.3V
C36 10pF
C40 100nF
R44 51
TP17
27MHz
C31 10uF
C32 100nF
3.3V
C33 10uF
C34 100nF
AD_MODE
U8
ADS1271IPWR
MODE
AINP
AINN
CLK
FORMAT
AVDD
DVDD
AGND
DGND
DOUT
DIN
SCLK
SYNC/PDWN
DRDY/FSYNC
NPO C35 100pF
NPO C37 1.0nF
NPO C39 100pF
VINP
VINN
R39 1.00k
R40 51
AD_DO
AD_DI
AD_SCLK
AD_SYNC
AD_DRDY
VREFP
VREFN
VREF
C84 1.0nF
C43 100nF
TP16
VREF
R79 33
5.0V
LP12
500mA_470OHM
C38 100nF
U10
LMV321AS5X
TP15
5.0VA
R78 10.0k
R80 10.0k
1%
C41 100pF
C44 4.7uF
ANALOG/DIGITAL CONVERTER
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Dialog Semiconductor
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26441, Patras, Greece
tel. (+30) 2610390940
fax. (+30) 2610390941
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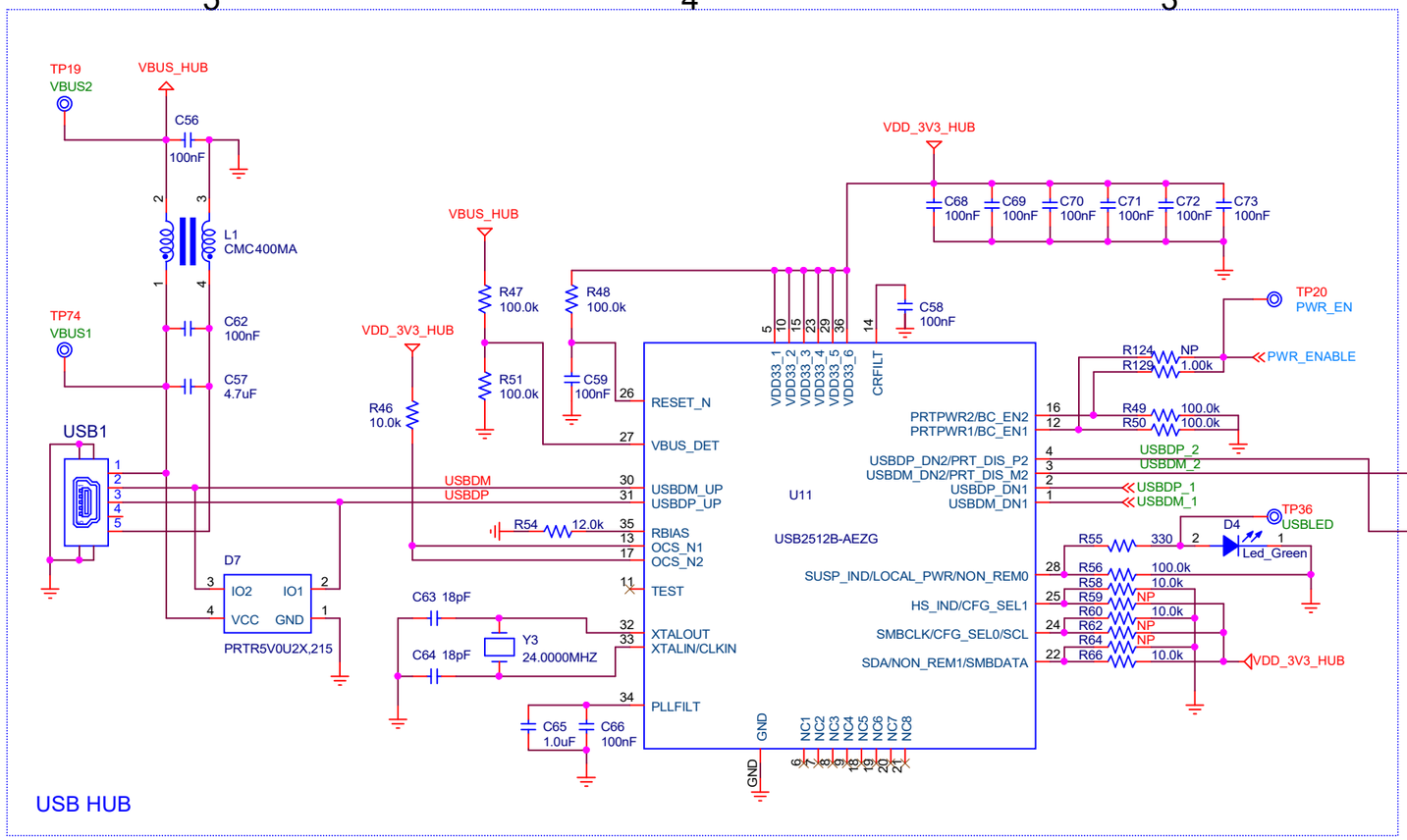

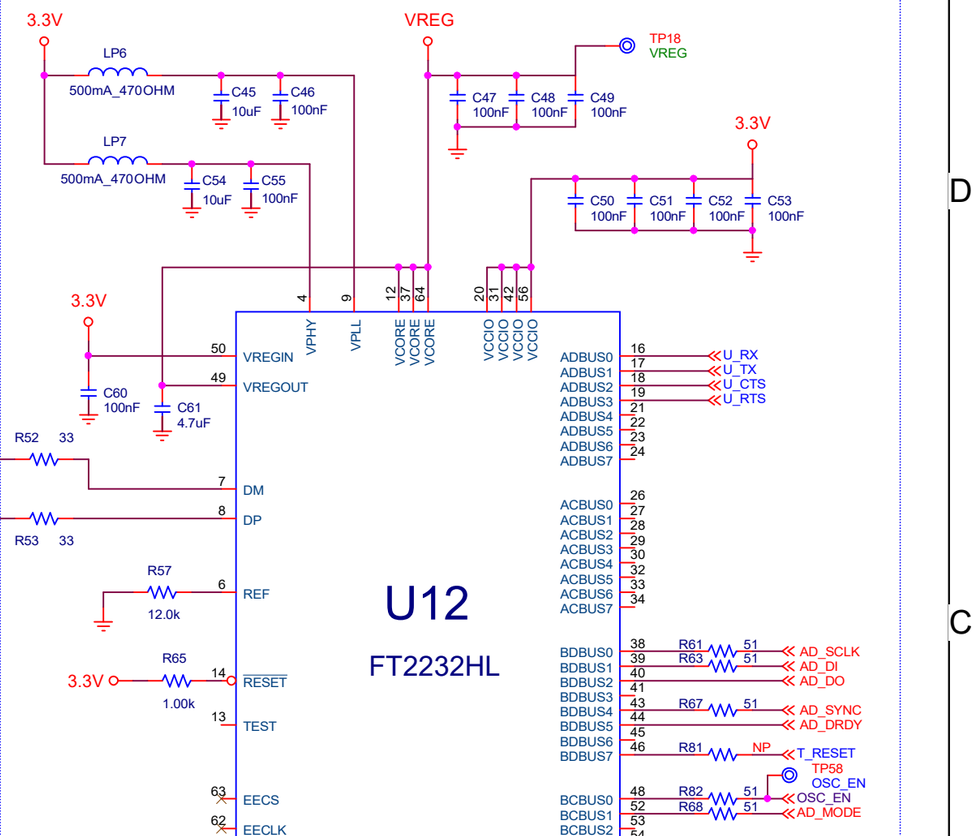

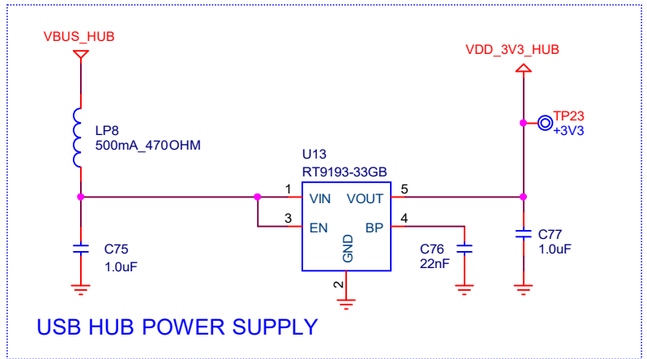

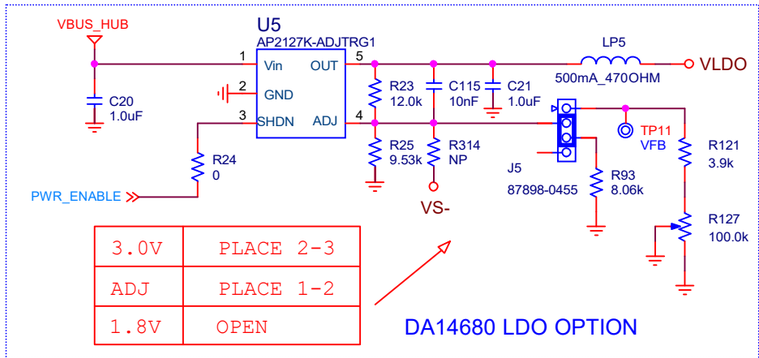

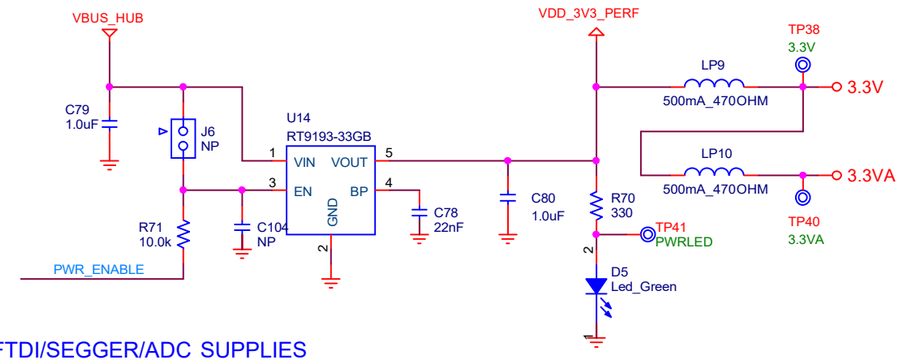

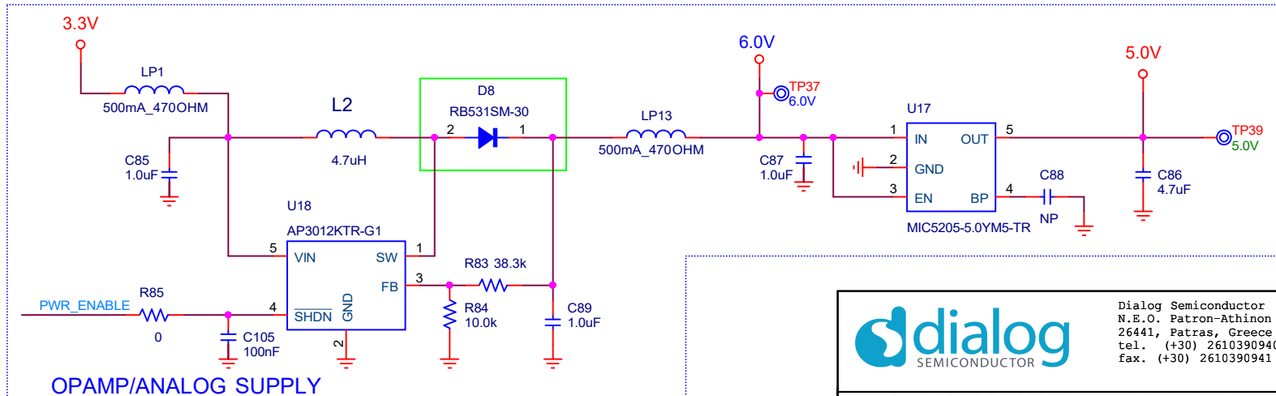

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