



DeePattern: Layout Pattern Generation with Transforming Convolutional Auto-Encoder

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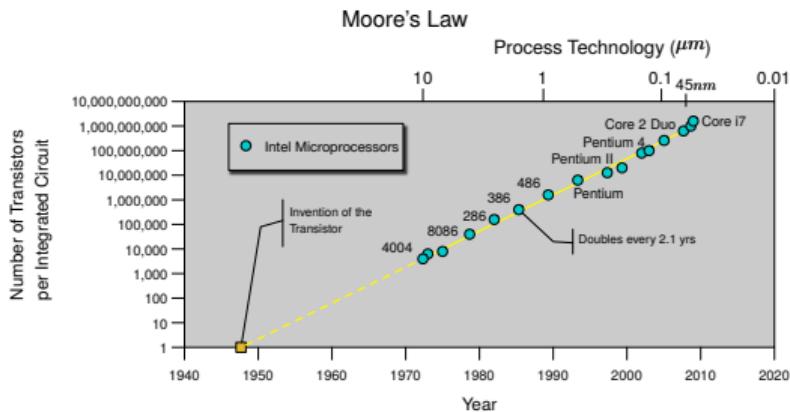
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EUV Brings Challenges in DFM

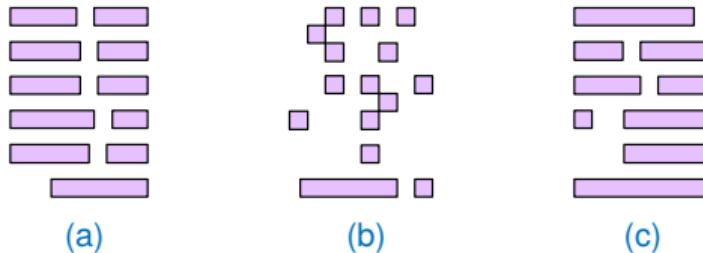
Moore's Law to Extreme Scaling



- ▶ Hotspot detection and fix
 - ▶ Hotspot pattern library covering the design space required by machine learning and pattern matching solutions.
 - ▶ Lithographic simulation challenge due to complicated computational lithography model under EUV nodes.*
- ▶ Early technology node development
 - ▶ Design rule, OPC recipe development,
 - ...

*Levinson et al. "Current challenges and opportunities for EUV lithography." International Conference on Extreme Ultraviolet Lithography 2018. Vol. 10809.

Related Works

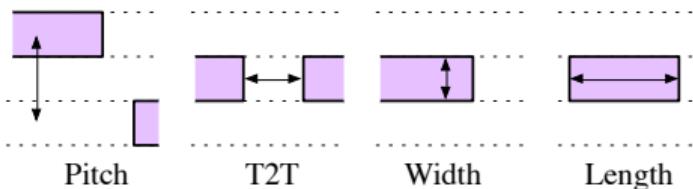


- ▶ Transferring from previous technology node (not applicable for large technology node gap) †
- ▶ Randomly placing patterns according to certain constraints (limited diversity)
- ▶ Generative machine learning models (violating design rules)

†Zhuang et al. "A novel methodology of process weak-point identification to accelerate process development and yield ramp-up", in Proc. ICSICT, 2016.

Pattern Generation Challenges

- ▶ Satisfying design rules



- ▶ Coverage of the design space

- ▶ The *complexity of a pattern* in x and y directions (denoted as c_x and c_y) are defined as the number of scan lines subtracted by one along x -axis and y -axis, respectively.
- ▶ The *diversity of a pattern library* is given by the *Shannon Entropy* of the pattern complexity sampled from the library,

$$H = - \sum_i \sum_j P(c_{xi}, c_{yj}) \log P(c_{xi}, c_{yj}),$$

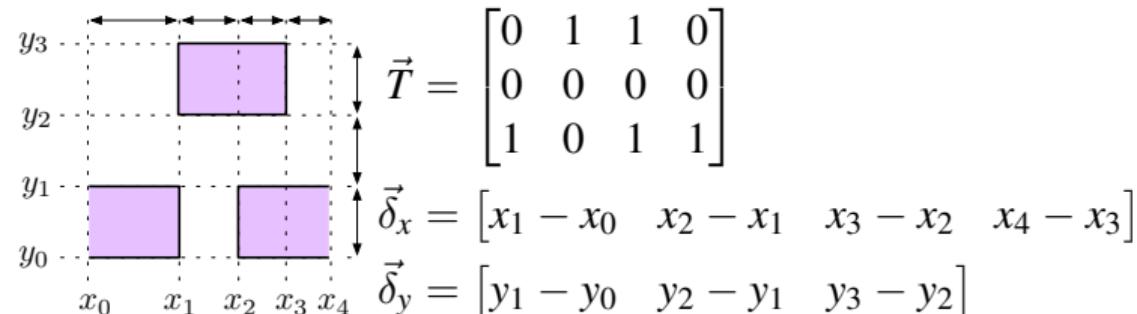
where $P(c_{xi}, c_{yj})$ is the probability of a pattern sampled from the library has complexities of c_{xi} and c_{yj} in x and y directions respectively.

Layout Pattern Generation

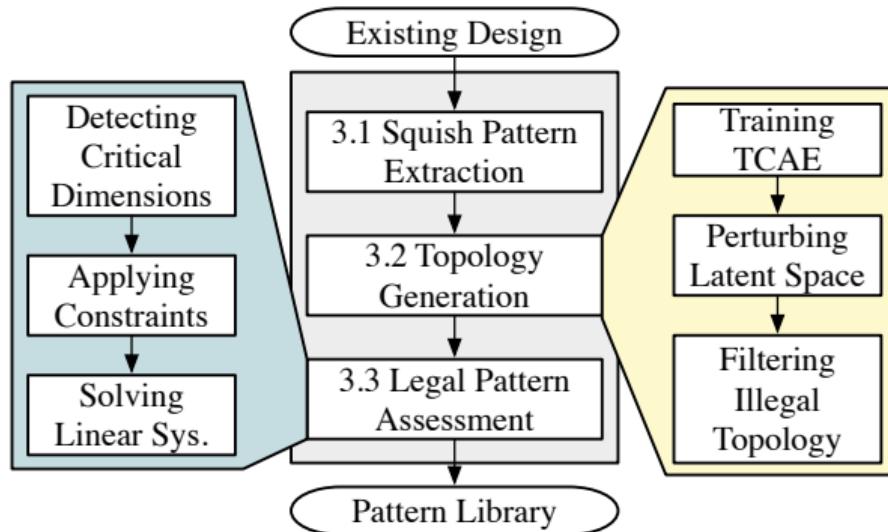
Problem (Pattern Generation)

Given a set of layout design rules, the objective of pattern generation is to generate a pattern library such that the pattern diversity and the number of unique DRC-clean patterns in the library is maximized.

Problem Simplification with Squish Patterns

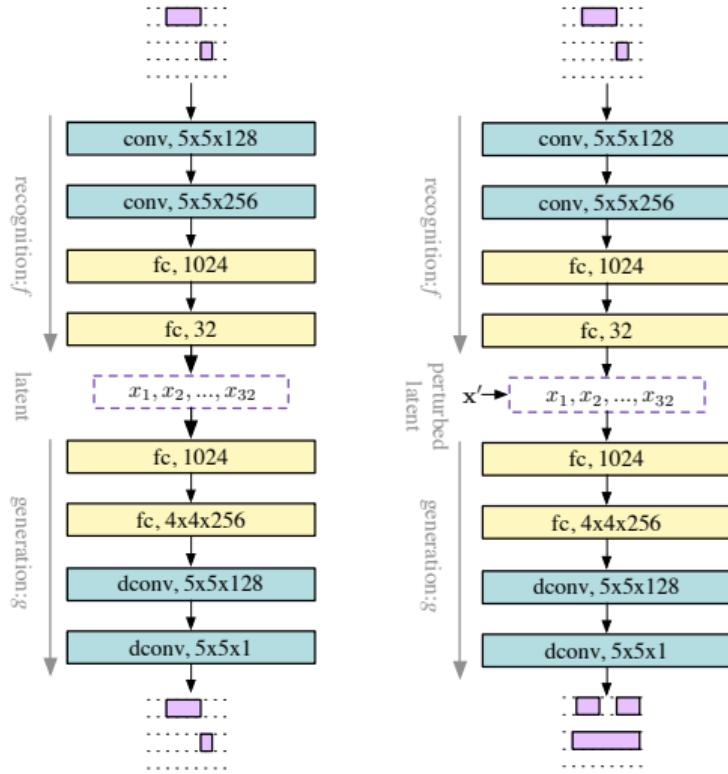


The Overall Flow



With the help of squish patterns, the problem becomes generating legal topologies and solving associated $\vec{\delta}_x$ s and $\vec{\delta}_y$ s that are much easier than directly generating DRC-clean patterns.

Topology Generation with TCAE



Input pattern to latent space,

$$\vec{l} = f(\vec{T}; \vec{W}_f)$$

Topology reconstruction,

$$\vec{T}' = g(\vec{l} + \Delta\vec{l}; \vec{W}_g)$$

Training objective,

$$\min_{\vec{W}_f, \vec{W}_g} \|\vec{T} - \vec{T}'\|_2, \text{ s.t. } \Delta\vec{l} = \vec{0}$$

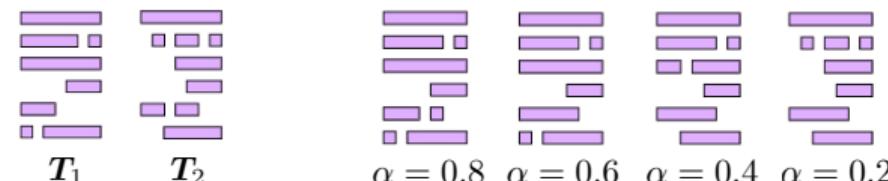
TCAE-Combine

- ▶ Generalization from existing topologies

$$\vec{T}_g = g\left(\sum_i \alpha_i f(\vec{T}_i)\right),$$

where $0 < \alpha_i < 1, \forall i$ are combination coefficients and satisfy $\sum_i \alpha_i = 1$.

- ▶ Sample results

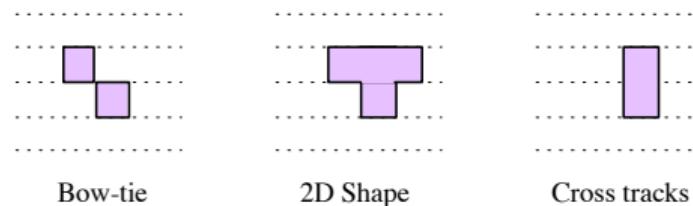


TCAE-Random

- ▶ Feature Sensitivity

Let $\vec{l} = [l_1 \ l_2 \ \dots \ l_n]^\top$ be the output of the layer associated with the latent vector space. The sensitivity s_i of a latent vector node l_i is defined as the probability of reconstructed pattern being invalid when a perturbation $\Delta l_i \in [-t, t]$ is added up on l_i with everything else unchanged.

- ▶ Filter illegal topologies



- ▶ Sample perturbation vectors from $\mathcal{N}(0, \frac{1}{s_i})$

Legal Pattern Assessment

Creating DRC constraints for legal $\vec{\delta}_x$ s and $\vec{\delta}_y$ s,

$$y_{i+1} - y_i = \frac{p}{2}, \quad \forall i,$$

$$x_i - x_j = t_{\min}, \quad \forall (i,j) \in \mathcal{C}_{T2T},$$

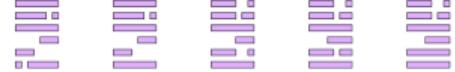
$$x_i - x_j = l_{\min}, \quad \forall (i,j) \in \mathcal{C}_W,$$

$$x_{i+1} - x_i > 0, \quad \forall i,$$

$$x_{\max} - x_0 = d_x, y_{\max} - y_0 = d_y.$$

Experiments

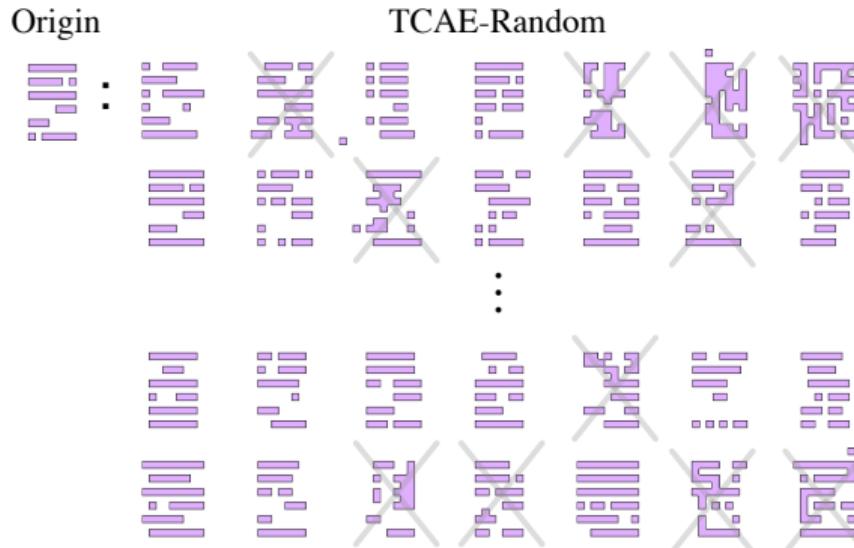
Understanding Features in TCAE

Transformations	Reconstructed Topologies				
Extend or pull back line-ends					
Create or destroy shapes					
Control shape directions					

Experiments

TCAE-Random Examples

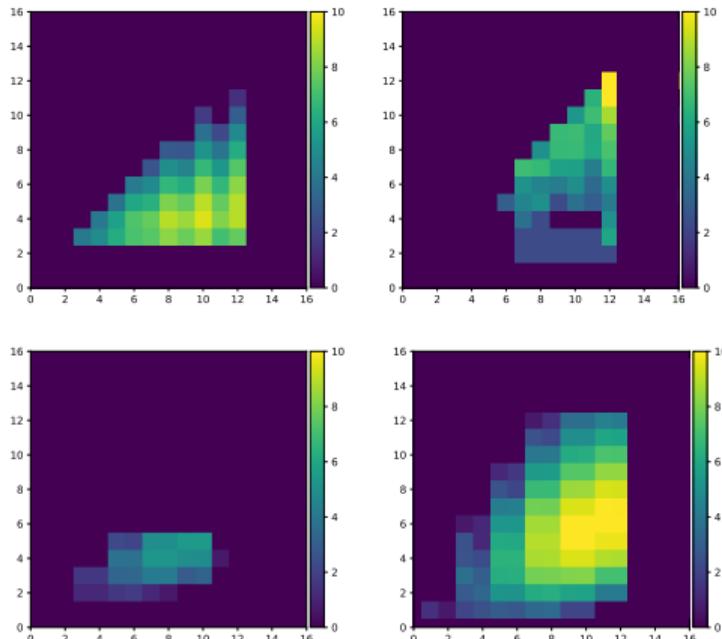
Contribution of Gaussian perturbation on topology reconstruction. 1000 topologies (~ 400 legal) are created from one topology randomly picked from the existing pattern library.



Experiments

Comparison with State-of-the-Art

Method	Pattern #	H
Existing Design	-	3.101
Industry Tool	55408	1.642
DCGAN	1	0
TCAE-Combine	1738	2.665
TCAE-Random	286898	3.337



(a) Existing layout pattern dataset. (b) Industrial layout generator; (c) TCAE-Combine; (d) TCAE-Random.

Conclusion

- ▶ Address the pattern library requirements in DFM flows/researches under advanced technology nodes.
- ▶ Propose a TCAE framework that can capture layout design rule characteristics.
- ▶ We show auto-learned features contribute to layout space locally or globally.
- ▶ The experimental results show that our framework outperforms a state-of-the-art industrial layout generation tool in terms of pattern library diversity.

Thank You