

Technische Informatik: Submission 4

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Exercise 4.1 (Decoder Fulladder)

We can construct a fulladder from a 3-8-Decoder and two OR-Gates

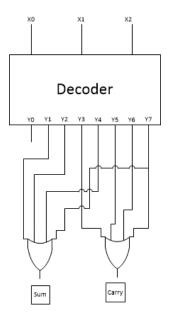


Abbildung 1: Fulladder

Exercise 2 ()

open

Exercise 3: (Racing and Beting)

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