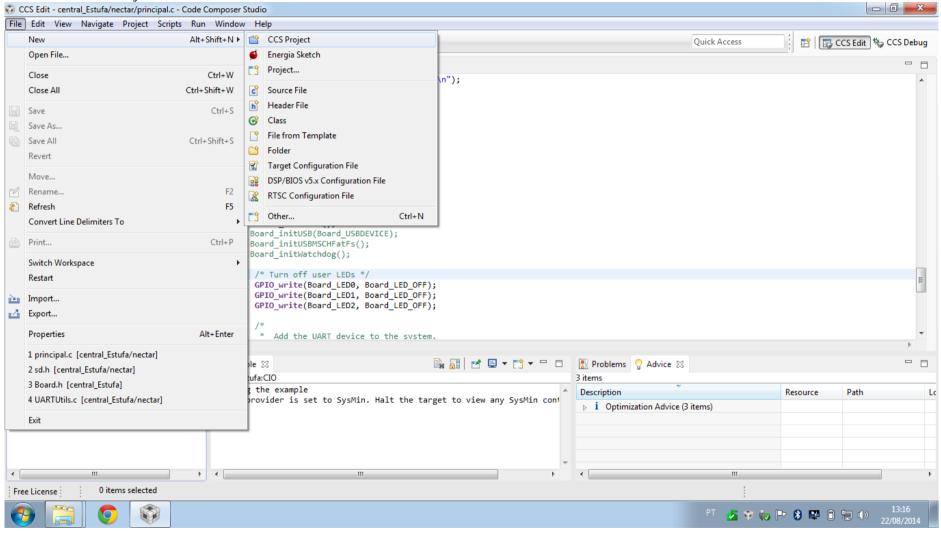
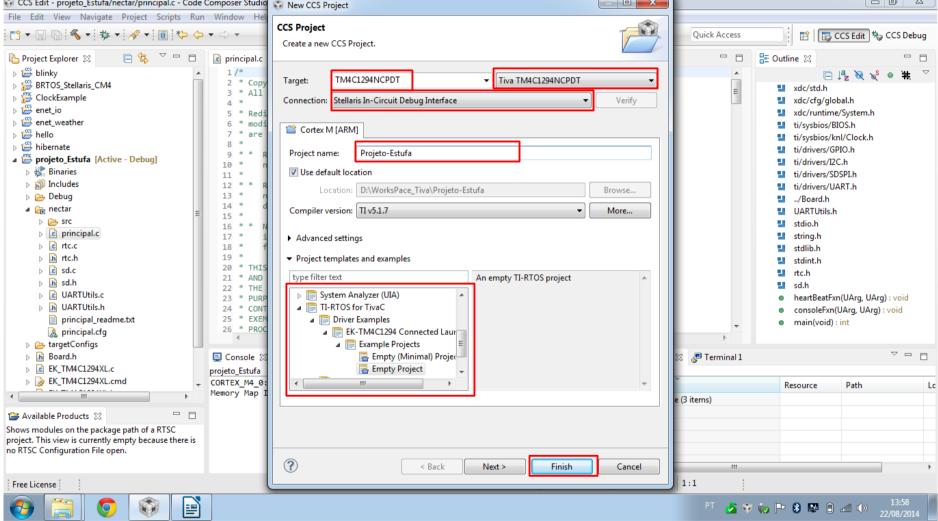
File>New>CCS Project



Target: TM4C1294NCPDT > Connection: Stellaris > Nome: "tanto Faz" > Selecionar Empty Project dentro de TI-RTOS for TivaC>Driver...>

EK-TM4C...>Example...> Empty Project e depois "Finish" CCS Edit - projeto_Estufa/nectar/principal.c - Code Composer Studio _ 0 X New CCS Project File Edit View Navigate Project Scripts Run Window He **CCS Project Ouick Access**



Quando criar o projeto, clicar com o botão direito na pasta e New > Folder _ 0 X CCS Edit - Code Composer Studio File Edit View Navigate Project Scripts Run Window Help E CCS Edit 5 CCS Debug Quick Access Project Explorer 🛭 _ 8 E Outline ⊠ - H ⊳ 🛱 enet io An outline is not available. ▶ 🛅 Project... New Add Files... CCS Project projeto_Estufa [Active - Del Energia Sketch Ctrl+C ▶ 👸 Includes Paste Ctrl+V File Debug File from Template Delete Delete targetConfigs ▶ In Board.h Folder Refactor Source œ Class Move... Header File ▶ In EK_TM4C1294XL.h Rename... Source File empty_min_readme.txt Target Configuration File Import a empty_min.cfg ∠ Export... Ctrl+N ☐

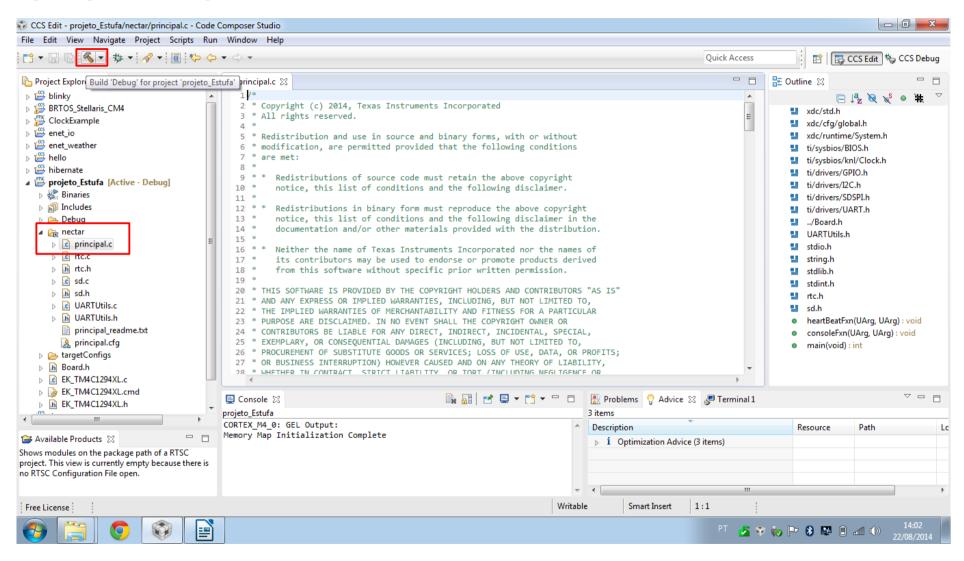
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Other... Show Build Settings... **Build Project** Clean Project Available Products ₩ Rebuild Project Shows modules on the package path Refresh F5 project. This view is currently empty b no RTSC Configuration File open. Close Project ▽ □ □ Problems O Advice X P Terminal 1 **Build Configurations** 0 items Make Targets Path Description Resource Lc ysMin. Halt the target to view any SysMin cont Index Debug As Team Compare With Restore from Local History... projeto_ Alt+Enter Free License Properties PT 💍 💝 🍖 🏲 🚷 👺 📵 📶 🕪

Maximisar a janela > "Advanced" > "Link to alternate loc..." > "Browse" > selecione a pasta nectar, onde estiver > OK > Finish New Folder Folder The 'folder' name is empty. × Procurar Pasta Select the link target. Enter or select the parent folder: projeto_Estufa ▲ I Documentos **☆** ⇔ ⇒ D Arduino blinky ▶ Marduino 328 → BRTOS_Stellaris_CM4 Bluetooth Folder > 👺 ClockExample ⊳ 👺 enet_io Delta December 2.4 D Inergia b Hibernate Garmin projeto_Estufa [Active - Debug] ■ MitHub RemoteSystemsTempFiles ■ AutomacaoEstufa D 📗 Energia nectar Folder name: << Advanced My ISO Files D III R-TT Use default location Folder is not located in the file system (Virtual Folder) 1 -+1-500 maga279n led En Link to alternate location (Linked Folder) nectar Pasta: Browse... Variables... Choose file system: default ▼ Criar Nova Pasta OK Cancelar Resource Filters... ? Finish Cancel PT 3 \$ 13:45

Selecione os três arquivos como abaixo e apague-os. _ 0 % CCS Edit - Code Composer Studio File Edit View Navigate Project Scripts Run Window Help E CCS Edit 5 CCS Debug Quick Access _ 8 E Outline ⊠ - H An outline is not available. → W BRTOS_Stellaris_CM4 Discrete ClockExample ⊳ 👺 hello - - X ■ Projeto_Estufa [Active - Debug] Delete Resources Includes Are you sure you want to delete the 3 selected resources from the file system? 📂 Debug ▶ In Board.h FK TM4C1294XI h OK Preview > Cancel empty_readme.txt empty.cfg b 📂 timers ♦ ♦ ♦ Problems Advice A Proplems Advice A Proplems _ _ ■ Console XX 0 items CDT Build Console [Projeto_Estufa] Description Resource Path Lc _ _ Shows modules on the package path of a RTSC project. This view is currently empty because there is no RTSC Configuration File open. 3 items selected Free License PT 💍 💗 🎁 🏲 🚷 👺 🗋 📶 🕪 22/08/2014

Expanda a pasta "nectar", clique no "martelinho" (Build) e cruze os dedos...



Que beleza!

Se não der, arraste o arquivo "principal.cfj" para a pasta do projeto: _ 0 X CCS Edit - Projeto_Estufa/nectar/principal.c - Code Composer Studio File Edit View Navigate Project Scripts Run Window Help
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 bl 2 * Copyright (c) 2014, Texas Instruments Incorporated → W BRTOS_Stellaris_CM4 xdc/std.h 3 * All rights reserved. xdc/cfg/global.h 4 * xdc/runtime/System.h 5 * Redistribution and use in source and binary forms, with or without 6 * modification, are permitted provided that the following conditions ti/sysbios/BIOS.h ti/svsbios/knl/Clock.h 8 * ti/drivers/GPIO.h Redistributions of source code must retain the above copyright 9 * * Projeto Estufa [Active - Debug] ti/drivers/I2C.h notice, this list of conditions and the following disclaimer. 10 * ▶ ⋒ Includes ti/drivers/SDSPI.h 11 * 👝 Debug 12 * * Redistributions in binary form must reproduce the above copyright ti/drivers/UART.h 13 * notice, this list of conditions and the following disclaimer in the ../Board.h documentation and/or other materials provided with the distribution. 14 * ▶ c principal.c UARTUtils.h 15 * stdio.h 16 * * Neither the name of Texas Instruments Incorporated nor the names of ⊳ h rtc.h its contributors may be used to endorse or promote products derived string.h 18 * from this software without specific prior written permission. stdlib.h 19 * ⊳ h sd.h stdint.h 20 * THIS SOFTWARE IS PROVIDED BY THE COPYRIGHT HOLDERS AND CONTRIBUTORS "AS IS" rtc.h 21 * AND ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED TO, b In UARTUtils.h sd.h 22 * THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR 23 * PURPOSE ARE DISCLAIMED, IN NO EVENT SHALL THE COPYRIGHT OWNER OR heartBeatFxn(UArg, UArg): void principal.cfg 24 * CONTRIBUTORS BE LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, consoleFxn(UArg, UArg): void 25 * EXEMPLARY, OR CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT LIMITED TO, main(void): int 26 * PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR PROFITS; ▶ In Board.h 27 * OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY, 28 * WHETHER TN CONTRACT STRTCT LIARTITTY OR TORT (TNCHINTNG NEGLIGENCE OR - n ■ Console \(\times \) 0 items CDT Build Console [Projeto Estufa] Description Resource Path Lc - -Available Products ⋈ Shows modules on the package path of a RTSC project. This view is currently empty because there is no RTSC Configuration File open. Projeto Estufa/nectar/principal.cfg Free License PT 💋 👽 🍖 🏲 🖇 👺 🗎 📶 🕪

Fica assim:

