

INSTRUCTION	EFFECT	DESCRIPTION
movb S,D	$D \leftarrow S$	Move byte
movw S,D		Move word
movl S,D		Move double word
movq S,D		Move quad word
movzbw S,R	$R \leftarrow \text{ZeroExt}(S)$	Move zero-extended byte to word
movzbl S,R		
movzwl S,R		
movzbq S,R		
movzwq S,R		
movsbw S,R	$R \leftarrow \text{SignExt}(S)$	Move sign-extended byte to word
movsbl S,R		
movswl S,R		
movsbq S,R		
movswq S,R		

INSTRUCTION	EFFECT	DESCRIPTION
pushq S	$R[\%rsp] \leftarrow R[\%rsp] - 8;$ $M[R[\%rsp]] \leftarrow S$	Push quad word
popq D	$D \leftarrow M[R[\%rsp]];$ $R[\%rsp] \leftarrow R[\%rsp] + 8$	Pop quad word

INSTRUCTION	EFFECT	DESCRIPTION
leaq S,D	$D \leftarrow \&S$	Load effective address
inc D	$D \leftarrow D + 1$	
dec D	$D \leftarrow D - 1$	
neg D	$D \leftarrow -D$	
not D	$D \leftarrow \sim D$	
add S,D	$D \leftarrow D + S$	
sub S,D	$D \leftarrow D - S$	
imul S,D	$D \leftarrow D * S$	
xor S,D	$D \leftarrow D \oplus S$	
sal k,D	$D \leftarrow D \ll k$	Equivalent to shl k,D
sar k,D	$D \leftarrow D \gg k$	Arithmetic right shift
shr k,D	$D \leftarrow D \gg k$	Logical right shift

INSTRUCTION	EFFECT	DESCRIPTION
imulq S	$R[\%rdx]:R[\%rax] \leftarrow S * R[\%rax]$	Signed full multiply
mulq S	$R[\%rdx]:R[\%rax] \leftarrow S * R[\%rax]$	Unsigned full multiply
cqto S	$R[\%rdx]:R[\%rax] \leftarrow \text{SignExt}(R[\%rax])$	To oct word
idivq S	$R[\%rdx] \leftarrow R[\%rdx]:R[\%rax] \bmod S$ $R[\%rax] \leftarrow R[\%rdx]:R[\%rax] \div S$	Signed divide
divq S		Unsigned divide

CF: Carry flag. Most recent operation generated carry from most significant bit. Detects overflow in unsigned operations.

ZF: Zero flag. Most recent operation yielded 0.

SF: Sign flag. Most recent operation yielded a negative value.

OF: Overflow flag. Most recent operation caused a two's complement overflow.

INSTRUCTION	EFFECT	DESCRIPTION
<code>cmp S₁, S₂</code>	$S_2 - S_1$	Set flags
<code>test S₁, S₂</code>	$S_1 \& S_2$	Set flags

INSTRUCTION	SYNONYM	CONDITION	DESCRIPTION
<code>jmp</code>		1	Direct jump
<code>je</code>	<code>jz</code>	ZF	Equal/zero
<code>jne</code>	<code>jnz</code>	\sim ZF	Not equal/not zero
<code>js</code>		SF	Negative
<code>jns</code>		\sim SF	Nonnegative
<code>jg</code>	<code>jnle</code>	$\sim(SF \wedge OF) \& \sim ZF$	Signed >
<code>jge</code>	<code>jnl</code>	$\sim(SF \wedge OF)$	Signed >=
<code>jl</code>	<code>jnge</code>	$SF \wedge OF$	Signed <
<code>jle</code>	<code>jng</code>	$(SF \wedge OF) \mid ZF$	Signed <=
<code>ja</code>	<code>jnbe</code>	$\sim CF \& \sim ZF$	Unsigned >
<code>jae</code>	<code>jnb</code>	$\sim CF$	Unsigned >=
<code>jb</code>	<code>jnae</code>	CF	Unsigned <
<code>jbe</code>	<code>jna</code>	$CF \mid ZF$	Unsigned <=

There are analogous `cmov` (conditional move) and `set` (for accessing conditional codes) instructions. Other important operations include:

call: Calls a function at specified label.

ret: Return control to address on top of stack. Synonymous with `retq`.

rep: Or `repz`, can be ignored.

nop: Effectively does nothing.

leave: Move `%rbp` to `%rsp`, then pops `%rbp`.

Equivalent floating point operations for movement, conversion, comparison, arithmetic, etc.