

Digitaler Output	Erwarteter Spannungswert	Tatsächlicher Spannungswert	Abweichung	Bitmuster
16	0,320V	0,321V	0,001	"00010000"
32	0,640V	0,639V	-0,001	"00100000"
48	0,960V	0,967V	0,007	"00110000"
64	1,280V	1,277V	-0,003	"01000000"
80	1,600V	1,601V	0,001	"01010000"
96	1,920V	1,922V	0,002	"01100000"
112	2,240V	2,240V	0,000	"01110000"
128	2,560V	2,566V	0,006	"10000000"
144	2,880V	2,887V	0,007	"10010000"
160	3,200V	3,203V	0,003	"10100000"
176	3,520V	3,534V	0,014	"10110000"
192	3,840V	3,843V	0,003	"11000000"
208	4,160V	4,158V	-0,002	"11010000"
224	4,480V	4,483V	0,003	"11100000"
240	4,800V	4,802V	0,002	"11110000"
255	5,120V	5,119V	-0,001	"11111111"