Duale Hochschule Baden-Württemberg Stuttgart

Bachelor Thesis

Development of a PoC for a PCIe based Automotive Zonal Architecture

Computer Science

IT-Automotive

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I hereby declare that I have written the present thesis titled *Development of a PoC for a PCIe based Automotive Zonal Architecture* independently and have used no sources or aids other than those stated. Furthermore, I confirm that the submitted electronic version corresponds to the printed version.

Place, Date   Philipp Gehrig

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Ort, Datum   Philipp Gehrig

Achkowledgement

This chapter contains the Achkowledgement

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Acronyms

PCIe   Peripheral Component Interconnect Express

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