

703308 VO High-Performance Computing WS2022/2023 MPI Groups, Communicators and One-Sided Communication

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#### Overview

- communicators and groups
  - more ways of limiting and controlling collective communication
- one-sided communication
  - decouples data access and synchronization
- error handling

# TOP500 November '22 update highlights

- No changes in the top 3
  - Lumi at EuroHPC/CSC in Finland doubled to 309 Pflops, would be 4th otherwise
- Frontier still the only Exascale system
- New: Leonardo at EuroHPC/CINECA in Italy is No. 4 with 174 Pflops
  - Austrian contribution!
  - the only new system in the top 10

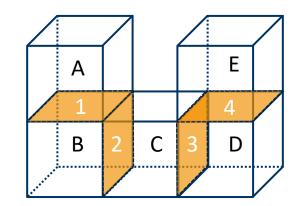
Rank	System	Cores	Rmax (PFlop/s)	Rpeak (PFlop/s)	Power (kW)
1	Frontier - HPE Cray EX235a, AMD Optimized 3rd Generation EPYC 64C 2GHz, AMD Instinct MI250X, Slingshot-11, HPE D0E/SC/Oak Ridge National Laboratory United States	8,730,112	1,102.00	1,685.65	21,100
2	Supercomputer Fugaku - Supercomputer Fugaku, A64FX 48C 2.2GHz, Tofu interconnect D, Fujitsu RIKEN Center for Computational Science Japan	7,630,848	442.01	537.21	29,899
3	<b>LUMI</b> - HPE Cray EX235a, AMD Optimized 3rd Generation EPYC 64C 2GHz, AMD Instinct MI250X, Slingshot-11, HPE EuroHPC/CSC Finland	2,220,288	309.10	428.70	6,016
4	Leonardo - BullSequana XH2000, Xeon Platinum 8358 32C 2.6GHz, NVIDIA A100 SXM4 40 GB, Quad-rail NVIDIA HDR100 Infiniband, Atos EuroHPC/CINECA Italy	1,463,616	174.70	255.75	5,610
5	Summit - IBM Power System AC922, IBM POWER9 22C 3.07GHz, NVIDIA Volta GV100, Dual-rail Mellanox EDR Infiniband, IBM DOE/SC/Oak Ridge National Laboratory United States	2,414,592	148.60	200.79	10,096

#### Motivation

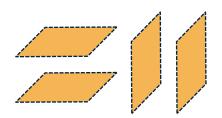
- Real-world applications are rarely a single component
  - often MPMD
  - usually combination of libraries (e.g. molecular dynamics and quantum mechanics)
- ▶ Adds several new complexities compared to single-component software
  - collective communication via MPI\_COMM\_WORLD?
  - how to identify sub-programs?
  - how to specifically communicate between and within programs?
- And what about NUMA?
  - virtual topologies do not reflect e.g. shared memory address spaces

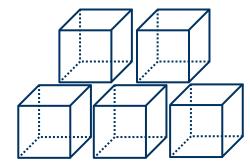
#### Motivation cont'd

- unstructured grid with cell and face element types
  - assume both require global communication among their types



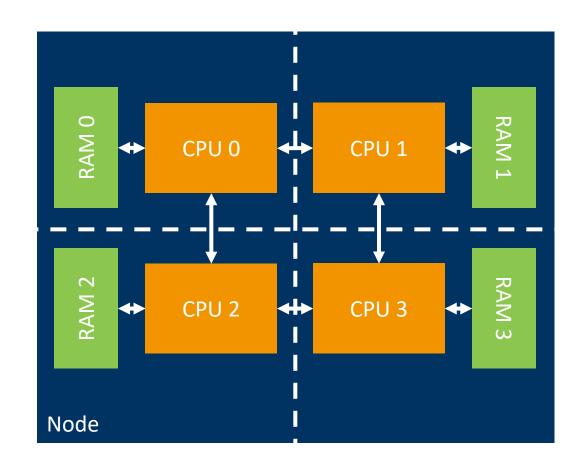
- a communicator per element type would be useful
  - but how?





#### Motivation cont'd

- consider shared memory node with 4 CPUs and many cores per CPU
- local collective communication among cores of a CPU is cheap
  - how to limit?
  - construct a communicator per CPU?
  - do this in hardware- and compilerindependent way?



### Communicators and groups

- Communicators and groups hold sets of ranks
  - directly used for e.g. collective communication
  - also required for identifying single ranks
  - remember MPI basics lecture: everything in MPI is relative to a communicator or group
- Why not stick to MPI\_COMM\_WORLD?
  - isolate application sub-programs
    - individual processing steps running in parallel (task parallelism, MPMD)
    - ▶ domain decomposition (SPMD, c.f. slicing Cartesian topologies)
    - libraries (portability)
  - usability
    - add user-defined attributes such as topologies
  - performance
    - re-numbering of ranks (virtual topology vs. hardware topology)

# Communicators and groups cont'd

#### ▶ MPI\_Group

- holds ordered set of ranks
- ordering is given by mapping process identifier (e.g. PID) to rank number
- construction of and operations on groups are always local operations

#### ▶ MPI\_Comm

- holds an MPI\_Group
  - transitively holds ordered set of ranks
- can hold attributes (e.g. topology)
- constructed from groups
- construction of communicators are global operations

### Operations on MPI\_Group

#### Constructors

- MPI\_Comm\_group(...)
- MPI\_Group\_union(...)
- MPI\_Group\_intersection(...)
- MPI\_Group\_difference(...)
- MPI\_Group\_incl(...)
- MPI\_Group\_excl(...)
- MPI\_Group\_range\_incl(...)
- MPI\_Group\_range\_excl(...)

#### Accessors

- MPI\_Group\_size(...)
- MPI\_Group\_rank(...)
- MPI\_Group\_compare(...)
  - result is MPI\_IDENT, MPI\_SIMILAR or MPI\_UNEQUAL

#### Destructor

MPI\_Group\_free(...)

# Operations on MPI\_Comm

#### Constructors

- MPI\_Comm\_dup(...)
- MPI\_Comm\_create(...)
- MPI\_Comm\_split(...)
- Convenience constructors such as MPI\_Cart\_sub(...)

#### Accessors

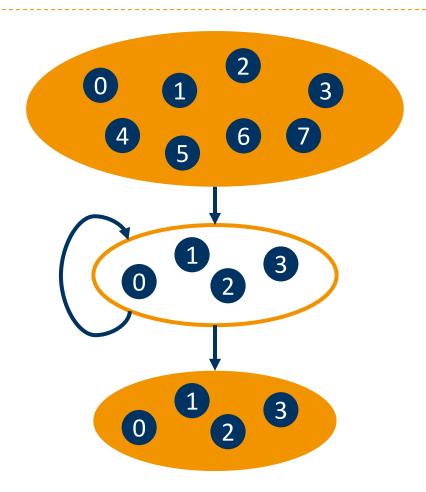
- MPI\_Comm\_size(...)
- MPI\_Comm\_rank(...)
- MPI\_Comm\_compare(...)
  - result is MPI\_IDENT, MPI\_SIMILAR, MPI\_CONGRUENT or MPI\_UNEQUAL

#### Destructor

MPI\_Comm\_free(...)

# Group and communicator workflow

- start with MPI\_COMM\_WORLD
- construct group(s) of rank subsets and modify as required
  - MPI\_Group\_union(),
    MPI\_Group\_range\_incl(),...
- create new communicator from group and use for communication

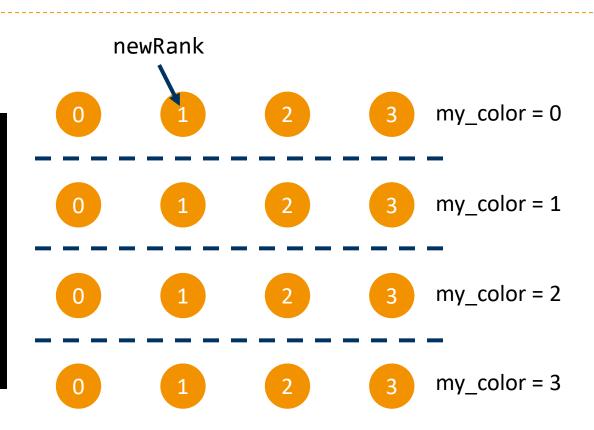


## Splitting communicators

- int MPI\_Comm\_split(MPI\_Comm comm, int color, int key, MPI\_Comm\* newcomm)
  - comm: current communicator
  - color: control of subset assignment (same color: same new communicator)
  - key: control of rank assignment (0: sorted as in comm; otherwise according to ascending key values)
  - newcomm: new communicator
- MPI\_Comm\_split\_type(...)
  - allows to split dependent on hardware properties

# MPI\_Comm\_split example

```
MPI_Comm newComm;
MPI_Comm_rank(MPI_COMM_WORLD, &myRank);
int myColor = myRank / 4;
MPI_Comm_split(MPI_COMM_WORLD, myColor,
    myRank, &newComm);
MPI_Comm_rank(newComm, &newRank);
```



# Solutions to motivation examples

```
MPI_Comm newComm;
MPI_Comm_rank(MPI_COMM_WORLD, &rank);
int color =
   (elementType == TYPE_FACES);
MPI_Comm_split(MPI_COMM_WORLD,
   color, rank, &newComm);
```

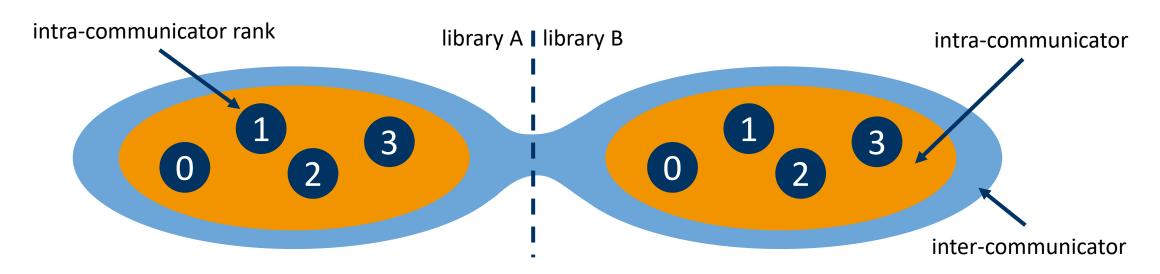
#### Intra- and inter-communicators

#### intra-communicator

- collection of ranks that can send messages to each other via point-topoint and collectives
- e.g. MPI\_COMM\_WORLD

#### inter-communicator

- collection of ranks from disjoint intracommunicators
- allows sending messages between communicators



# One-sided Communication

#### Motivation

#### message-passing paradigm

- fits distributed memory systems well
- data transfers among distinct address spaces require network communication
- requires explicit communication
- little control over message aggregation & synchronization

#### shared memory paradigm

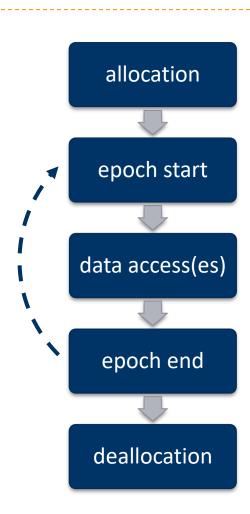
- no message passing required
- b data transfer aggregation possible write multiple bytes, elements, ... in one go
- much more convenient from a user and performance perspective
  - does not necessarily require receiving side to participate
- also, messages are needless overhead on shared-memory systems
  - sending/receiving messages just for writing to a different memory address in the same address space?

#### MPI's solution: one-sided communication

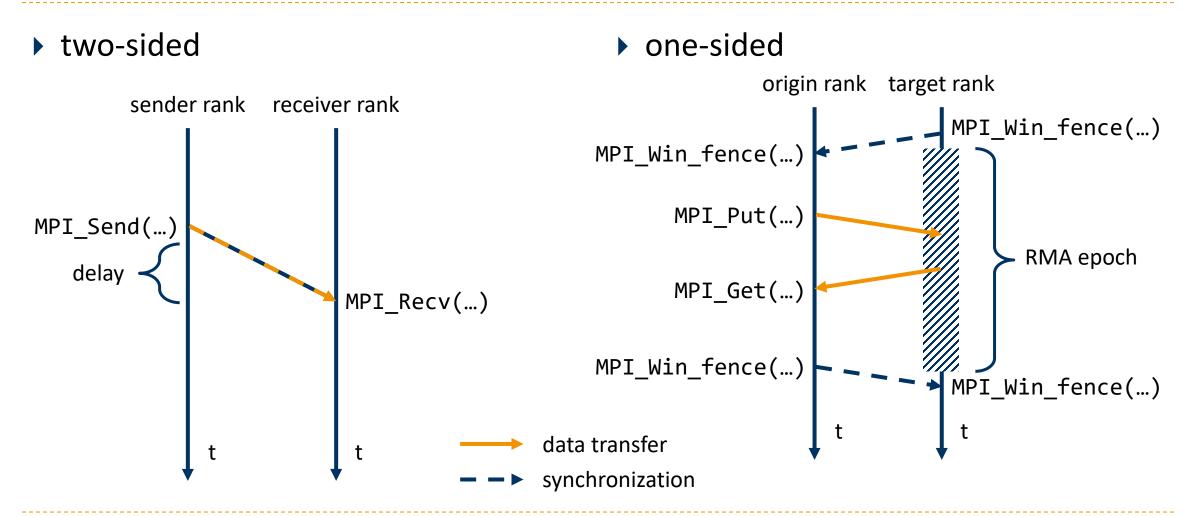
- classic point-to-point ("two-side") communication implies synchronization
  - at every data transfer action
  - incurs a lot of overhead in the presence of many messages
- one-sided communication decouples data movement and synchronization
  - ranks expose a "window" of rank-local memory
  - can be accessed by other ranks using remote memory access (RMA)
  - data accesses do not necessarily require action on the rank exposing memory
  - both read and write are possible
    - ranks no longer identify as "sender" and "receiver" but as "origin" and "target" instead

#### One-sided communication workflow

- allocate buffer and window
  - can ask MPI to allocate fast memory
- open window ("start epoch")
  - synchronization point
  - allows data access by remote ranks
- close window ("end epoch")
  - synchronization point
  - commits data accesses
- deallocate window and buffer



#### MPI one-sided communication cont'd



# Means of synchronization

#### active target synchronization

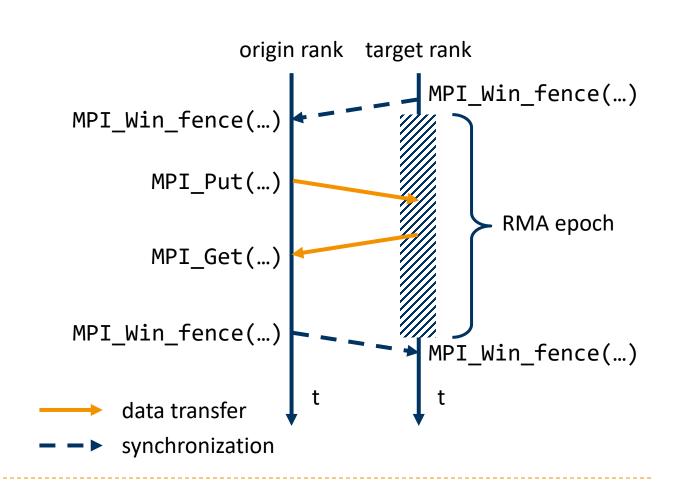
- target participates in synchronization
- similar to message-passing paradigm
- Uses either MPI\_Win\_fence() or "post-start-complete-wait"
- works well for bulk-synchronous parallel programs
  - all ranks execute computation and communication steps more or less in sync
  - e.g. structured grid with ghost cell exchange

#### passive target synchronization

- target does not synchronize
- similar to shared memory paradigm
- uses MPI\_Win\_lock() and
  MPI\_Win\_unlock()
- works well for dynamic, independent access patterns
  - e.g. irregular codes

# Active target synchronization: fence

- collective synchronization
  - origin/target not specified
- all control the epoch
  - starts/ends all epochs on all participating ranks
- fence enforces synchronization



# Active target synchronization: post/start/complete/wait

#### selective synchronization

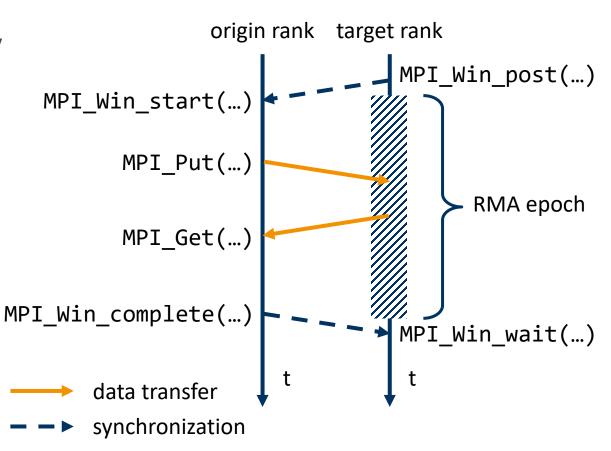
 origin and target specify a group they communicate with

#### both control their epochs

origin: start/complete

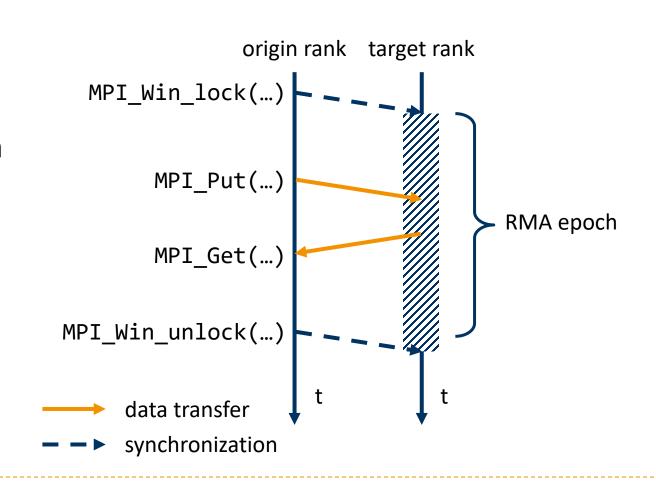
target: post/wait

synchronization calls may block to enforce ordering



# Passive target synchronization: lock/unlock

- target neither involved in data transfer, nor in synchronization
- origin has full control over epoch
- resembles shared memory programming models (e.g. Pthreads, std::mutex, ...)
  - but not the same
  - no critical section!



# Implications of one-sided communication

#### several benefits

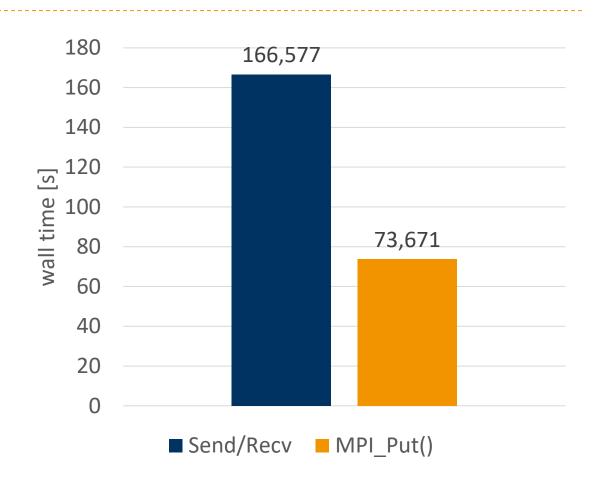
- allows dynamic access patterns (e.g. when target rank does not know number and ranks of origins)
- reduce synchronization overhead for multiple data transfers
- reduce management overhead on receiver side (e.g. tag matching)
- performance gain
- reduce coding effort on receiver side

#### drawbacks

- no send/receive matching
- operations are not explicitly visible on the receiver side
- user responsible for correct order of reads/writes (race conditions)
- only non-blocking communication

# Performance comparison

- LCC2, openmpi/3.1.12 ranks, one per node
- rank 0 sends 10<sup>8</sup> int to rank 1
  - ▶ once using 10<sup>8</sup> plain send/recv calls
  - once using 10<sup>8</sup> MPI\_Put() calls with MPI\_Fence() synchronization
- execution time reduced by 2.26x
  - only between two ranks
  - but an edge case stress test



## Optional window fence assertions

- ▶ MPI\_MODE\_NOSTORE
  - local window was not updated by local store, local get or receive calls since last fence
- MPI\_MODE\_NOPUT
  - local window will not be updated by put or accumulate until next fence
- MPI\_MODE\_NOPRECEDE
  - fence does not complete any sequence of locally issued RMA calls
- ▶ MPI MODE NOSUCCEED
  - fence does not start any sequence of locally issued RMA calls
- none of these are required, but they can improve performance

#### Four window models

- MPI\_Win\_create(...)
  - private memory buffer already allocated, use as window
- MPI\_Win\_allocate(...)
  - allocate buffer and use as window
- MPI\_Win\_create\_dynamic(...)
  - expose a buffer which is not available yet
  - use later with MPI\_Win\_attach()/MPI\_win\_detach()
- MPI\_Win\_allocate\_shared(...)
  - allocate and use buffer in shared memory segment of the OS
  - only works for MPI\_COMM\_TYPE\_SHARED

## Transferring data: put & get

- int MPI\_Put(const void\* origin\_addr, int origin\_count, MPI\_Datatype origin\_datatype, int target\_rank, MPI\_Aint target\_disp, int target\_count, MPI\_Datatype target\_datatype, MPI\_Win win)
  - origin\_addr: local address of data to put
  - origin\_count: number of elements on origin side
  - origin\_datatype: type of elements on origin side
  - target\_rank: rank of target process
  - target disp: offset of target address to base address of target window
  - target count: number of elements on target side
  - target\_datatype: type of elements on target side
  - win: window handle
- ▶ MPI\_Get(...)
  - transfer data from target to origin

### Transferring data: accumulate

#### ▶ MPI Accumulate(...)

- transfer data from origin and accumulate atomically at target
- can only use predefined operations of MPI\_Reduce(...) (e.g. MPI\_SUM)
- use MPI\_REPLACE to get atomic put

#### MPI\_Get\_accumulate(...)

- same as MPI\_Accumulate(...) but store target buffer data in result buffer before accumulating
- use with MPI\_NO\_OP to implement atomic get or MPI\_REPLACE for atomic swap

### Transferring data: single-element atomics

- MPI\_Compare\_and\_swap(...)
  - atomic swap if data at target buffer matches comparison value
  - must be single element
  - must be predefined integer, logical or byte type
- MPI\_Fetch\_and\_op(...)
  - variant of MPI\_Get\_accumulate(...), available for hardware optimization
  - implements a subset of MPI\_Get\_accumulate(...)'s generic functionality
    - must be single-element
    - must be predefined data type

### MPI one-sided communication example

```
// rank 0
MPI Win window;
int buffer[SIZE] = ...;
MPI Win create(&buffer, sizeof(int)*SIZE,
  sizeof(int), MPI INFO NULL,
  MPI COMM WORLD, &window);
MPI_Win_fence(MPI_MODE_NOSTORE |
  MPI MODE NOPUT | MPI MODE NOPRECEDE,
  window);
MPI Put(&buffer, SIZE, MPI_INT, 1, 0,
  SIZE, MPI INT, window);
MPI_Win_fence(MPI_MODE_NOSTORE |
  MPI MODE NOPUT | MPI MODE NOSUCCEED,
  window);
MPI_Win_free(&window);
```

```
// rank 1
MPI Win window;
int buffer[SIZE] = { 0 };
MPI Win create(&buffer, sizeof(int)*SIZE,
  sizeof(int), MPI_INFO_NULL,
  MPI_COMM_WORLD, &window);
MPI_Win_fence(MPI_MODE_NOSTORE |
  MPI MODE NOPRECEDE
  MPI MODE NOSUCCEED, window);
// window open, buffer is being written to
MPI Win fence(MPI MODE NOPUT
  MPI MODE NOPRECEDE
  MPI MODE NOSUCCEED, window);
// use buffer here
MPI_Win_free(&window);
```

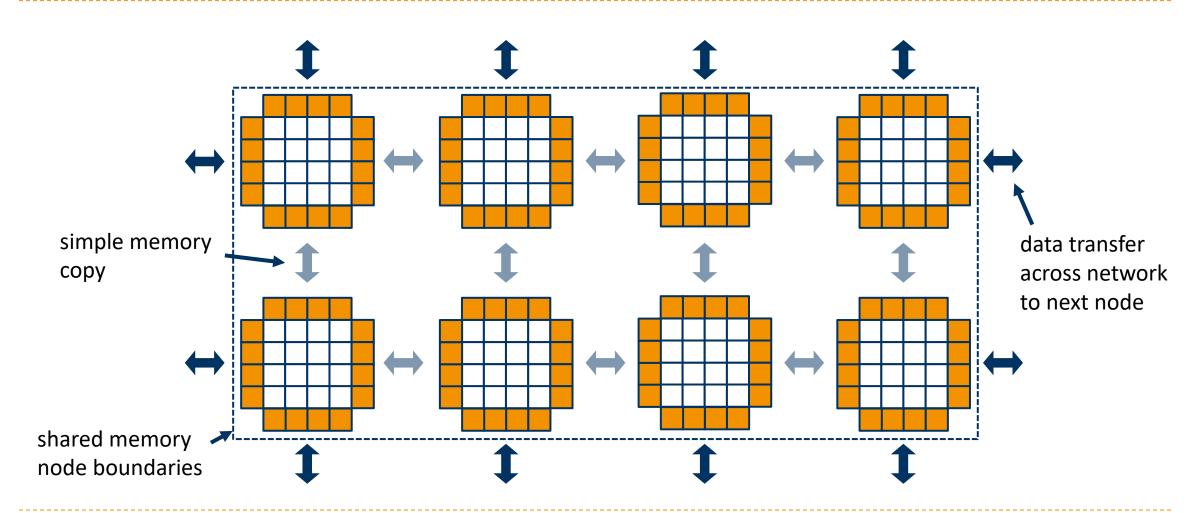
#### RMA semantics

- order of Get and Put/Accumulate is not guaranteed
  - race condition
  - same for multiple Put operations (use Accumulate instead)
- no local access to window during access epoch
  - use an RMA operation if absolutely required
- local vs. remote completion of operation
  - no send buffer re-use after Put until end of access epoch
- no concurrent passive synchronization epochs to same target
  - only relevant in multi-threading context
- lots of MPI fence optimizations
  - where to place, which assertions to use

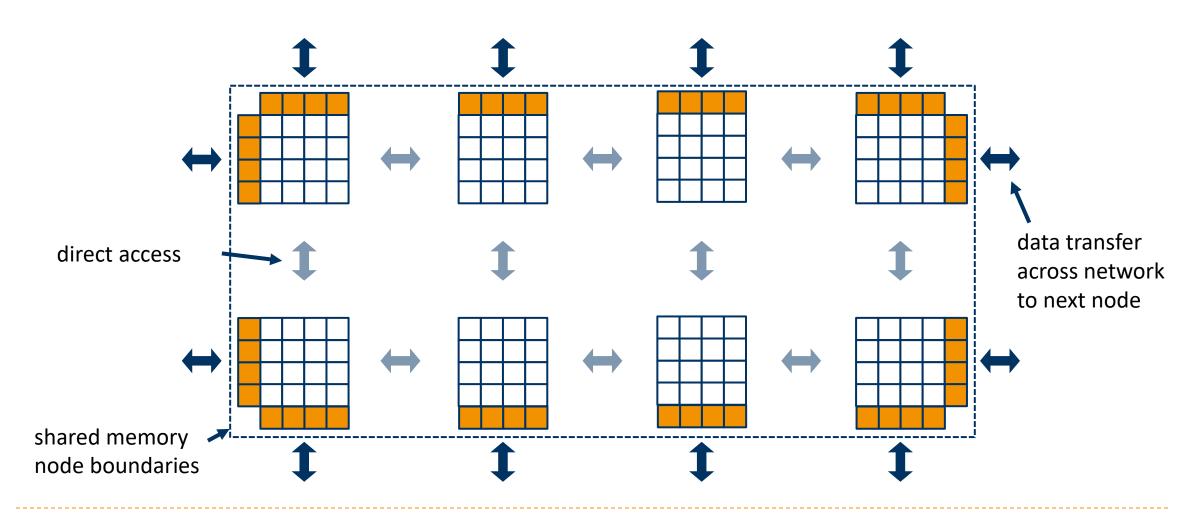
# Shared-memory one-sided communication

- one-sided communication also available in a truly shared memory fashion
  - origin of data transfer will get a pointer to access target memory
  - naturally only works for ranks in the same physical address space
  - c.f. POSIX shared memory segments
- allows to share memory between ranks
  - reduces memory footprint (e.g. no extra buffer for ghost cell exchange of a stencil)
  - even more efficient for intra-node communication than one-sided communication

# Ghost cell exchange (message passing in shared memory)



# Ghost cell exchange (MPI shared memory access)



## Example for shared memory one-sided communication

```
MPI Comm split type(MPI COMM WORLD, MPI COMM TYPE SHARED,
  0, MPI INFO NULL, &comm sm);
MPI Win allocate shared((MPI Aint)(sizeof(int)),
  sizeof(int), MPI INFO NULL, comm sm, &rcv buf ptr,
  &win);
MPI Win fence(0, win);
*rcv buf ptr = ...; // replaces the MPI Put() call!
MPI Win fence(0, win);
MPI Win free(&win);
```

# MPI and multithreading

- one-sided shared memory communication can become quite complex
  - there are alternatives: MPI+OpenMP or std::thread or Pthreads or TBB or ...
  - these are known as hybrid programming models
  - both paradigms have their ups and downs (number of programming models, compiler support, compiler optimizations, ...)
- ▶ MPI+threads needs to be supported by MPI, indicated by one of four safety levels
  - MPI\_THREAD\_SINGLE: only a single thread per rank
  - MPI\_THREAD\_FUNNELED: multithreaded ranks, but only the main thread calls MPI
  - MPI\_THREAD\_SERIALIZED: multithreaded, but only one per time calls MPI
  - MPI\_THREAD\_MULTIPLE: multithreaded, any thread can call MPI any time (with restrictions)
  - MPI implementations are not required to support more than MPI\_THREAD\_SINGLE
    - always check beforehand with MPI\_Query\_thread(...) and call MPI\_Init\_thread() instead of MPI\_Init()

Error Handling

## Error handling

- MPI introduces additional hassle
  - imagine everything that can go wrong with a sequential process
  - add the fact that multiple processes are interacting, across the network
- default behavior: communication errors cause abort of MPI operation
  - causes respective process to exit
  - causes all other MPI processes of the same application to exit
- this only relates to halting crashes
  - also consider deadlocks or hangs

# Error handling cont'd

- ▶ all MPI routines return error codes
  - MPI\_SUCCESS if everything went well
- should always check and act accordingly
  - consider action to take when MPI calls fail
  - compare to a failing malloc in sequential program
  - make sure to free allocated resources (e.g. file handles)

# Error handling cont'd

- error behavior can be altered with MPI\_Comm\_set\_errhandler(...)
  - MPI\_ERRORS\_ARE\_FATAL: abort if error detected (default)
  - MPI\_ERRORS\_RETURN: return error code to user program
- user-defined error handlers can be installed
  - MPI\_Comm\_create\_errhandler(...)
  - MPI\_Comm\_set\_errhandler(...)
  - MPI\_Comm\_get\_errhandler(...)

## Additional topics

- ▶ File I/O
  - data file partitioning among processes, strided file access (derived data types!)
  - asynchronous data transfers
- process management
  - process spawning
  - socket-style communication
- Fortran-specific issues
  - slight differences in MPI function signatures
- profiling support
  - ▶ MPI\_... vs. PMPI\_...

### Summary

- communicators and groups
  - offers high-level control over sets of ranks
- one-sided communication
  - can improve performance, but can be tricky to use
- error handling
  - graceful exits