YAN HOI PUK, PHILIP

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Qualifications

Education

- MEng Electronic and Information Engineering – Imperial College London (2019 – 2023)
    - Courses: High Level Programming, Software System, Communication Network, Embedded Systems, Introduction to Machine Learning, Deep Learning, Optimisation, Advanced Computer Architecture, Instruction Set Architecture and Compiler, Hardware and Software Verification, Signals and Machine Learning for Finance
- International Baccalaureate Diploma Program – Diocesan Boys' School (2014-2019)

Work history

Part-time Undergraduate

COMPANY NAME: ARM LIMITED

April 2022 – April 2023

- Software Engineer at Fast Model Team
- Responsible for Performance Monitoring Unit update for v8/9-A CPU Architecture Model (C++)
- Built Unit tests for testing the new architecture features (Arm Assembly)

Digital Business Analyst Internship

COMPANY NAME: ACCENTURE (HK) LIMITED

August 2021 – October 2021

- Participated in the Agile application development cycle for an application for insurance company
- Formulated User guides and User requirements for the client application

Group Projects

Software Engineering

- "Mars Rover" Project, Responsible for web-based command panel and path finding algorithm (Python, Django)
- Circuit Simulator, Responsible for code implementation (C++)
- Built Decision Tree and Mini Neural Network, Team Project (Python)
- Built and train VAE and DCGAN models (Pytorch)
- Pong Game, Responsible for controller code implementation (FPGA, AWS, UDP)
- Agriculture IOT device built on Raspberry Pi (Python), Digital piano built on STM32 (C++)
- Added new features to ISSIE (Schematic Simulator and Editor) (F#)

Computer Architecture

- CPU Profiling on Arm Cortex A9 for a tensor decomposition program (perf)
- Investigation of lowest power consumption configuration of CPU (Simplescalar)
- Design a simplified MIPS CPU (SystemVerilog)
- Built a simplified C-to-MIPS compiler (Yacc, Bison, C++)
- Built a verification suite for AHBLite peripherals (SystemVerilog)

Skills

C++/ Python / SQL / JavaScript / HTML / Git /SystemVerilog/ TCP/ UDP/ Jira/F#