

Phillip Marlowe

phillipmikhailmarlowe@gmail.com | (650) 867-7535 | <https://phillipmmarlowe.github.io/professionalpm/>

Education

University of California, Santa Cruz

Master of Science (M.S.) in Computer Science & Engineering
Bachelor of Science (B.S.) in Computer Engineering

December 2025
June 2023

Member of Engineering Honor Society Tau Beta Pi: Ca Alpha Delta Chapter

June 2022

Engineering Experience

Hardware Systems Collective Research - UC Santa Cruz

Graduate Student Researcher

September 2023 - December 2025

- Produced a semi-generalized physical design flow to generate eFPGA fabric parasitics for use in observing Bias Temperature Instability (BTI), a form of transistor degradation, on particular nodes and routes
- Produced SPICE test environment to observe BTI effects on metastability in D-FlipFlops and plotted clock-to-q delay to experimentally determine setup and hold times as well as point of failure
- Assisted in setting up lab workstations and enabling remote access for running simulations, facilitating experimental testing, CAD design, etc.

Baskin Engineering Lab Support - UC Santa Cruz

Student Technician

May 2022 - December 2025

- Support instructional labs by verifying, moving, and securing test & measurement equipment, as well as sourcing supplies to ensure that each lab is safe and functional for instruction
- Assist students, faculty, and teaching assistants by reconciling equipment checkouts, assembling/distributing instructional kits, operating specialized tools (poster printers, 3D printers, PCB mill, laser cutters), and creating standard operating procedures
- Maintain lab workstations by installing and updating specialized software, creating and deploying master images, and resolving hardware and software issues
- Operate AV systems for Baskin Engineering event spaces as well as support presenters and resolve live issues
- Manage stockroom, poster, and receiving workflows by tracking inventory, sourcing required materials, overseeing poster printing and documentation, and supporting precise shipping and receiving operations

Capstone Engineering Design Project - UC Santa Cruz

January - June 2023

Electrical & Mechanical Hardware Systems Engineer

- Collaborated on a six person team to build a hands free following utility wagon by integrating electrical and mechanical subsystems to assist workers and/or older adults with carrying physical loads
- Designed wiring and power distribution for motors, microcontrollers, and sensors, enabling reliable communication and consistent power across the system
- Selected and sourced materials to assemble circuits; tested prototype to verify proper functionality

Engineering Projects

Ground Bounce PCB - ECE 173: High-Speed Digital Design

June 2023

- Designed and fabricated a custom printed circuit board to experimentally observe ground bounce
- Measured propagation delays and signal distortion with an oscilloscope to evaluate switching noise, validating results through RC timing and frequency testing

Technical Skills

- **Software/Design** - 3 years of FPGA/ASIC design experience, Cadence ASIC Design tools, Cadence PCB Design tools, Xilinx Vivado, Synopsys VCS, Open-Source ASIC/FPGA design tools, Solidworks, Windows Support, Linux Support, Docker, Git, LaTeX, Markdown, Web Development experience
- **Programming Languages** - SystemVerilog (including various HLS), C/C++, Python, Java, Javascript, Scripting (TCL,bash), SPICE analog simulation, Makefile automation, ISAs (RISC-V,x86,MIPS)
- **Hardware Experience** - Xilinx and Lattice FPGAs, Arduino, Raspberry Pi, Microchip MCUs, Cypress PSoC 6, STM32
- **Fabrication** - 2-Layer PCB Milling, Surface mount/through hole soldering, PLA/SLA printing, Laser cutting, Electronics Diagnosis & Repair