

Proforma

Companies open to your department

Company : GLOBALFOUNDRIES
Company Name : GLOBALFOUNDRIES
Nature Of Business : Service Semiconductor Foundry
Designation : Senior Engineer/ Circuit Design
Tentative Job Location : Bangalore

Summary of Role :

Work with the IP Development team on the circuit design, simulation, FE model development and verification of IPs in various technology nodes.

Essential Responsibilities :

Use industry standard design tools to perform gate/transistor level circuit design, verification, simulation with electrical, physical, FE and timing rules generation for IPs such as Standard Cell Logic libraries, IO libraries, and Efuse Macros & Memories.

Required Qualifications :

- Description :
- Requires a Bachelor, Masters OR PhD degree in Electronics
 - Be able to collaborate with program and technical design leads on multiple concurrent projects.
 - Should have excellent problem solving skills, written & oral communication, teaming & interpersonal skills
 - Perform all activities in a safe and responsible manner and support all Environmental, Health, Safety & Security requirements and programs

Preferred Qualifications:

- Should have good understanding of VLSI Design and CMOS technology
- Knowledge of the end-to-end IP design cycles
- Knowledge in RF technologies (Bulk, CMOS & SOI) process is desired.

Department		BT	BS	MT	Double	Major	dual	dual	B	dual	C	Mdes	MBA	Phd	MSc	MSR
Eligibility :	AE	N	-	N	N		N	N	N	-	-	N	-	N		
	BSBE	N	-	N	N		N	N	N	-	-	N	-	N		
	CE	N	-	N	N		N	N	N	-	-	N	-	N		
	CHE	N	-	N	N		N	N	N	-	-	N	-	N		
	CSE	N	-	N	N		N	N	N	-	-	N	-	N		
	EE	N	-	Y	Y		Y	Y	Y	-	-	Y	-	Y		
	ES	-	N	N	-		N	-	N	-	-	N	-	-		
	ME	N	-	N	N		N	N	N	-	-	N	-	N		
	MSE	N	-	Y	Y		Y	Y	Y	-	-	Y	-	Y		
	PHY	-	N	-	Y		Y	Y	Y	-	-	Y	Y	-		
	CHM	-	N	-	N		N	N	N	-	-	N	N	-		
	MTH	-	N	-	N		N	N	N	-	-	N	N	-		
	ECO	-	N	-	N		N	N	N	-	-	N	-	-		
	DES	-	-	-	-		-	-	N	N	-	N	-	-		
	IME	-	-	N	-		-	N	N	-	N	N	-	-		
	CGS	-	-	-	-		-	-	-	-	-	N	-	N		
	HSS	-	-	-	-		-	-	-	-	-	N	-	-		
	EEM	-	-	N	-		-	N	-	-	-	N	-	-		
	MSP	-	-	N	-		-	-	-	-	-	N	-	-		
	NET	-	-	N	-		-	N	-	-	-	N	-	-		
	PSE	-	-	N	-		-	N	-	-	-	N	-	N		
	Stats	-	-	-	-		-	-	-	-	-	N	N	-		

Cost to Company : For **PhDs** - CTC: 2167480 INR
For **Master's** - CTC: 1288772 INR
Following is the break up for **PhDs** :

Package Details : Base Pay- 1850000, AIP- 185000, Gratuity- 37908, PF- 94572

Following is the break up for **Master's** :

Base Pay- 1100000, AIP- 110000, Gratuity- 22540, PF- 56232

Bond : False

Medical Requirements : Medical Insurance is provided by the company

Resume Shortlist : True

Resume Shortlist Criteria: N/A

Aptitude Test: False

Group Discussion: False

Technical Test: False

Technical Interview: True

Technical Interview Duration: N/A

Number of Technical Interview Rounds: 3

HR Interview: True

HR Interview Duration: N/A
