

Proforma

Companies open to your department

Company :	Texas Instruments
Company Name :	Texas Instruments
Nature Of Business :	Semiconductor R&D
Designation :	Analog Engineer
Tentative Job Location :	Bangalore
Description :	Texas Instruments Incorporated is an American technology company that designs and manufactures semiconductors and various integrated circuits. TI is one of the top-10 semiconductor companies worldwide, based on sales volume and is focused on developing analog chips and embedded processors, which account for more than 80% of their revenue. TI has been a pioneer in many innovations in the semiconductor domain including the development of the first integrated circuit; the first patent on a single-chip microprocessor, the first single-chip linear predictive coding speech synthesizer, developing prototype of the world's first transistor radio and the invention of the digital light processing device (also known as the DLP chip), which serves as the foundation for the award-winning DLP technology and DLP Cinema (used in IMAX theatres). TI India was set up in 1985 and has R&D presence for all the major business units of TI including Analog - (Data Converters, Amplifiers, Clocks & Synthesizers, Motor Drives, Power Management ) and Embedded Processors (Connected Microcontrollers, Radar, ADAS- Advanced Driver Assistance and Infotainment Processors etc.) and caters to products for different market segments - Industrial, automotive, personal Electronics, Communication and Enterprise.
	As an Analog Engineer, youâ€™ll define, design, model, implement and document Integrated Circuits (ICs). You will also have the opportunity to work in exciting areas like audio, energy automation, electronic point of service, industrial automation, infotainment, ADAS, Imaging, high speed interface, clocking medical, high volume linear, automotive, storage, power supply, battery management, linear power, DLP and many more. Your responsibilities will include: 1) Partnering with business teams and systems engineering to develop mutually agreeable design specifications 2) Providing high-level analysis on chip architecture trade-offs to ensure spec compliance and superior performance at a competitive cost 3) Participating in design reviews and creating the necessary design and product documentation 4) Supervising IC Layouts to ensure a high performance standard
Eligibilty :	Department BT BS MT DoubleMajor dual dualB dualC Mdes MBA Phd MSc MSR
	AE N - N N N N N N N - - N - N
	BSBE N - N N N N N N N - - N - N
	CE N - N N N N N N N - - N - N
	CHE N - N N N N N N N - - N - N
	CSE N - N N N N N N N - - N - N
	EE Y - Y Y Y Y Y Y - - Y - Y
	ES - N N - N - N - N - - N - -
	ME N - N N N N N N N - - N - N
	MSE N - N N N N N N N - - N - N
	PHY - N - N N N N N - - N N -
	CHM - N - N N N N N - - N N -
	MTH - N - N N N N N - - N N -
	ECO - N - N N N N N - - N - -
	DES - - - - - - - N N - N - -
IME - - N - - - N N - N N - -	

CGS	-	-	-	-	-	-	-	-	-	N	-	N
HSS	-	-	-	-	-	-	-	-	-	N	-	-
EEM	-	-	N	-	-	N	-	-	-	N	-	-
MSP	-	-	N	-	-	-	-	-	-	N	-	-
NET	-	-	N	-	-	N	-	-	-	N	-	-
PSE	-	-	Y	-	-	Y	-	-	-	Y	-	Y
Stats	-	-	-	-	-	-	-	-	-	N	N	-

B.tech : 21,50,880

M.tech:24,48,905

B.tech : 21,50,880

Fixed: 14,00,000

Variable: 2,10,000

benefits: 1,15,880

Joining Bonus: 350,000 (paid in 2 installments, half immediately on joining and the rest half on completion of 1 year)

Relocation assistance one time: 75,000

M.tech:24,48,905

Fixed: 15,25,000

Variable: 2,28,750

benefits: 1,20,155

Joining Bonus: 500,000 (paid in 2 installments, half immediately on joining and the rest half on completion of 1 year)

Relocation assistance one time: 75,000

False

Medical Requirements :

Resume Shortlist :

Aptitude Test:

Aptitude Test Duration:

Group Discussion:

Technical Test:

Technical Test Duration:

Technical Interview:

Technical Interview Duration:

Number of Techincal

Interview Rounds:

HR Interview:

HR Interview Duration:

15 min