Proforma

Companies open to your department

Company: **Applied Materials** Company Name: **Applied Materials** Nature Of Business: Core Engineering Designation: **Application Engineer**

Tentative Job Location

Bangalore

Applications Engineer:

This position is within the semiconductor process diagnostics and control group, which deals with advanced imaging together with cutting

edge image processing algorithms in order to detect and dispose nanometer size defects.

Application engineers are supposed to maximize the performance of our new defect metrology products through a deep and thorough understanding of use-cases in close technical collaboration with leading-edge chip manufacturing customers.

At a higher expertise level, they also contribute to product development roadmap through spec definition and performance validation.

Description:

• Sound fundamentals of high-NA DUV imaging/SEM and light-matter/electron-matter interaction

• Open to 70% travel to leading semiconductor chip manufacturing fabs overseas

• Lab/fab hands-on experience in semiconductor fabrication with layer-wise detailed knowledge of material, process tech and defectivity

• Detail oriented with strong analytical, problem solving and communication skills

• Ability to work in a team, and ability to work independently

Department BT BS MT DoubleMajor dual dualB dualC Mdes MBA Phd MSc MSR

AE	N	-	N	N	N	N	N	-	-	N	-	N
BSBE	N	-	N	N	N	N	N	-	-	N	-	N
CE	N	-	N	N	N	N	N	-	-	N	-	N
CHE	N	-	N	N	N	N	N	-	-	N	-	N
CSE	N	-	N	N	N	N	N	-	-	N	-	N
EE	N	-	Y	N	Y	Y	Y	-	-	N	-	Y
ES	-	N	N	-	N	-	N	-	-	N	-	-
ME	N	-	N	N	N	N	N	-	-	N	-	N
MSE	N	-	Y	N	Y	Y	Y	-	-	N	-	Y
PHY	-	N	-	N	Y	Y	Y	-	-	N	N	-
CHM	-	N	-	N	N	N	N	-	-	N	N	-
MTH	-	N	-	N	N	N	N	-	-	N	N	-
ECO	-	N	-	N	N	N	N	-	-	N	-	-
DES	-	-	-	-	-	-	N	N	-	N	-	-
IME	-	-	N	-	-	N	N	-	N	N	-	-
CGS	-	-	-	-	-	-	-	-	-	N	-	N
HSS	-	-	-	-	-	-	-	-	-	N	-	-
EEM	-	-	N	-	-	N	-	-	-	N	-	-
MSP	-	-	Y	-	-	-	-	-	-	N	-	-
NET	-	-	N	-	-	N	-	-	-	N	-	-
PSE	-	-	Y	-	-	N	-	-	-	N	-	Y
Stats	-	-	-	-	-	-	-	-	-	N	N	-

Cost to Company:

Eligibilty:

BASIC HRA OTHERS TAKE HOME Cost to Company 583,108 233,243 243845 1,060,196 1,264,236

Stocks (with vesting plan) - 3000 USD (4 yrs) Not included in CTC

Package Details:

Relocation Allowance - Yes

False Bond:

Medical Requirements

Resume Shortlist: True

Resume Shortlist

Yes Criteria:

Aptitude Test: False Group Discussion: False

Technical Test: True

Technical Test Duration: 60 Mins

Technical Interview: True
Technical Interview 2 hrs

Duration:

Number of Techincal Interview Rounds:

2

HR Interview: True

HR Interview Duration:

30 Mins