

Proforma

Companies open to your department

Company : GLOBALFOUNDRIES  
Company Name : GLOBALFOUNDRIES  
Nature Of Business : Service Semiconductor Foundry  
Designation : Principal Design Enablement Engineer/ eFUSE Circuit Design:  
Tentative Job Location : Bangalore

Your tasks:

In this position you will be integrated in our Technology Enablement team, especially in the Advanced Circuit Design (ACD) department.

In close collaboration with other disciplines across our worldwide engineering teams you will be developing One Time Programmable Memory (OTP) IP which enable our customers to perform product designs at highest quality standards based on Globalfoundries' advanced process nodes.

Your responsibilities include:

The development of product grade eFUSE based OTP IP covering the following phases:

- Circuit design and verification
- Layout Guidance
- Sign off and release into dedicated IP validation test chips
- Specification and documentation
- Support of the silicon bring up and characterization
- Customer support at design-in as well as at Si bring up phases

The expansion into novel OTP/MTPM memory approaches needed for emerging security apps

Description : Requirements:

You have a Master’s Degree or PhD in semiconductors/Microelectronics/VLSI engineering from a reputed university/IIT/NIT/BITS.

Ideally you have gained either practical experiences or academic experience as part of your thesis in Custom Mixed-Signal or in Memory design at one or several of the following areas:

- Memory design (OTP, SRAM, DRAM or NVM)
- General analog mixed-signal design

Further desired skills are:

- Good knowledge of CMOS technology
- Knowledge of state of the art memory or analog design flows
- Programming experience applicable to design flow automation tasks
- The capability to work within a very dynamic interdisciplinary environment as well as dedicated knowledge of 45/32/28nm and below technology nodes are an advantage.
- You are flexible, highly motivated and have a team-oriented working style.
- You have shown the ability to communicate as well as work efficiently in an international
- Multi-disciplinary environment.
- Strong written and verbal communication skills in English are a must.

	Department	BT	BS	MT	DoubleMajor	dual	dualB	dualC	Mdes	MBA	Phd	MSc	MSR
	AE	N	-	N	N	N	N	N	-	-	N	-	N
	BSBE	N	-	N	N	N	N	N	-	-	N	-	N
	CE	N	-	N	N	N	N	N	-	-	N	-	N
	CHE	N	-	N	N	N	N	N	-	-	N	-	N
	CSE	N	-	N	N	N	N	N	-	-	N	-	N
	EE	N	-	Y	Y	Y	Y	Y	-	-	Y	-	Y
	ES	-	N	N	-	N	-	N	-	-	N	-	-
	ME	N	-	N	N	N	N	N	-	-	N	-	N
	MSE	N	-	Y	N	Y	Y	Y	-	-	Y	-	Y
	PHY	-	N	-	Y	Y	Y	Y	-	-	Y	Y	-
Eligibility :	CHM	-	N	-	N	N	N	N	-	-	N	N	-

MTH	-	N	-	N	N	N	N	-	-	N	N	-
ECO	-	N	-	N	N	N	N	-	-	N	-	-
DES	-	-	-	-	-	-	N	N	-	N	-	-
IME	-	-	N	-	-	N	N	-	N	N	-	-
CGS	-	-	-	-	-	-	-	-	-	N	-	N
HSS	-	-	-	-	-	-	-	-	-	N	-	-
EEM	-	-	N	-	-	N	-	-	-	N	-	-
MSP	-	-	N	-	-	-	-	-	-	N	-	-
NET	-	-	N	-	-	N	-	-	-	N	-	-
PSE	-	-	N	-	-	N	-	-	-	N	-	N
Stats	-	-	-	-	-	-	-	-	-	N	N	-

Cost to                   For **PhDs** - CTC: 2167480 INR

Company :             For **Master's** - CTC: 1288772 INR

Following is the break up for **PhDs** :

Package Details     Base Pay- 1850000, AIP- 185000, Gratuity- 37908, PF- 94572

:                       Following is the break up for **Master's** :

Base Pay- 1100000, AIP- 110000, Gratuity- 22540, PF- 56232

Bond :               False

Medical             Medical Insurance is provided by the company  
Requirements :

Resume             True  
Shortlist :

Resume             N/A  
Shortlist

Criteria:

Aptitude Test:     False

Group               False  
Discussion:

Technical Test:    False

Technical           True  
Interview:

Technical           N/A  
Interview

Duration:

Number of           3  
Techincal  
Interview  
Rounds:

HR Interview:      True

HR Interview       N/A  
Duration: