Proforma

Companies open to your department

Samsung Semiconductor India Research Center Company: Company Samsung Semiconductor India Research Center Name: Nature Of Semiconductor Business: Designation: Senior Engineer - Hardware Tentative Job Bangalore Location: Hardware: Analog and Digital design (RTL, Synthesis Layout) for CIS, PMIC, HI Speed Interface like PCIe, LPDDR4, SERDES, SATAe, NVMe, UFSÂ and etc, Standard cell and Memory Compiler, PDK, Verification, Description: layout, enabling fabrication on latest Nodes, Signal/Image processing with DSP, GPGPU, CPU-SIMD, solutions enabling & tuning on best/latest smartphone platforms. Department BT BS MT DoubleMajor dual dualB dualC Mdes MBA Phd MSc MSR N -Ν Ν Ν Ν Ν Ν Ν **BSBE** N -N N N N N N N CE Ν N N N N N N _ Ν CHE N _ N N N N N N N **CSE** Ν Ν Ν N Ν N Ν N -EE Y _ Y Y Y Y Y Ν Y ES N N N N Ν ME N -N Ν Ν N Ν N Ν **MSE** N N N N N N N N _ PHY Ν N Ν Ν N N N Eligibilty: **CHM** Ν N N N N N N MTH Ν Ν Ν N Ν N Ν **ECO** Ν N Ν Ν N Ν DES N N N **IME** N N N N N **CGS** N N **HSS** N **EEM** N N N **MSP** N N NET N N N **PSE** N N N N Stats Ν Ν Bachelors: 22,75,000 Cost to Company: Masters: 23,75,000 Bachelors 15,75,000 + 7,00,000 RB 1st year +5,00,000 2nd year +2,00,000 3rd year **Package** Details: Masters 16,75,000 + 7,00,000 RB 1st year +5,00,000 2nd year +2,00,000 3rd year False Bond: Medical Requirements Resume True Shortlist: Resume Shortlist 7.5 cgpa Criteria: Aptitude False Test: Group False Discussion: **Technical** True

Duration:

90 mins

Test: Technical Test

Technical True Interview:

Technical

Interview 45 mins

Duration:

Number of Techincal 2 Interview

Rounds:

HRInterview:

True

HR Interview 20

Duration: