

Proforma

Companies open to your department

Company : Taiwan Semiconductor Manufacturing Company
Company Name : Taiwan Semiconductor Manufacturing Company
Nature Of Business : Semiconductor
Designation : 3DIC Chip Design Methodology Development Engineer
Tentative Job Location : Hsinchu City, Taiwan

Responsibilities

Develop the APR and Sign-Off design flow for 3DIC.

Description :

Requirements

- Familiar with Programming (shell, python, perl, TCL) or C/C++ is a plus.
- Farimiar w/ circuit design and Have EE background.
- Familiar with P&R, IREM, physical verification and timing signoff is a plus.

	Department	BT	BS	MT	DoubleMajor	dual	dualB	dualC	Mdes	MBA	Phd	MSc	MSR	
Eligibilty :	AE	N	-	N	N		N	N	N	-	-	N	-	N
	BSBE	N	-	N	N		N	N	N	-	-	N	-	N
	CE	N	-	N	N		N	N	N	-	-	N	-	N
	CHE	N	-	N	N		N	N	N	-	-	N	-	N
	CSE	N	-	Y	N		Y	Y	Y	-	-	Y	-	Y
	EE	N	-	Y	N		Y	Y	Y	-	-	Y	-	Y
	ES	-	N	N	-		N	-	N	-	-	N	-	-
	ME	N	-	N	N		N	N	N	-	-	N	-	N
	MSE	N	-	Y	N		Y	Y	Y	-	-	N	-	Y
	PHY	-	N	-	N		N	N	N	-	-	N	N	-
	CHM	-	N	-	N		N	N	N	-	-	N	N	-
	MTH	-	N	-	N		Y	Y	Y	-	-	Y	Y	-
	ECO	-	N	-	N		N	N	N	-	-	N	-	-
	DES	-	-	-	-		-	-	N	N	-	N	-	-
	IME	-	-	N	-		-	N	N	-	N	N	-	-
	CGS	-	-	-	-		-	-	-	-	-	N	-	N
	HSS	-	-	-	-		-	-	-	-	-	N	-	-
	EEM	-	-	N	-		-	N	-	-	-	N	-	-
	MSP	-	-	Y	-		-	-	-	-	-	N	-	-
	NET	-	-	N	-		-	N	-	-	-	N	-	-
	PSE	-	-	Y	-		-	Y	-	-	-	N	-	Y
	Stats	-	-	-	-		-	-	-	-	-	Y	Y	-

Technical Interview: True
Technical Interview Duration: 60 min
Number of Techincal Interview Rounds: 1
HR Interview: True
HR Interview Duration: 60 min
