## **Proforma**

Companies open to your department

LAM Research Company: Company Name LAM Research

Core

Nature Of Business: Designation:

Senior Electrical Engineer

Tentative Job Location:

Bangalore

- · Design of components and subassemblies for semiconductor processing equipment, with specific focus on high voltage power distribution systems, PCBs, electronic parts, components or integrated circuitry for electronic equipment, or other hardware systems and/or electrical engineering applications.
- Description: • Coordinate design activities with mechanical engineers to drive robust design decisions in a fast-moving environment
  - Collaborate with counterparts in Lam Research locations to obtain requirements and validate design against the requirements

Department BT BS MT DoubleMajor dual dualB dualC Mdes MBA Phd MSc MSR

AE	N	-	N	N	N	N	N	-	-	N	-	N
BSBE	N	-	N	N	N	N	N	-	-	N	-	N
CE	N	-	N	N	N	N	N	-	-	N	-	N
CHE	N	-	N	N	N	N	N	-	-	N	-	N
CSE	N	-	N	N	N	N	N	-	-	N	-	N
EE	N	-	Y	N	Y	Y	Y	-	-	N	-	Y
ES	-	N	N	-	N	-	N	-	-	N	-	-
ME	N	-	N	N	N	N	N	-	-	N	-	N
MSE	N	-	N	N	N	N	N	-	-	N	-	N
PHY	-	N	-	N	N	N	N	-	-	N	N	-
CHM	-	N	-	N	N	N	N	-	-	N	N	-
MTH	-	N	-	N	N	N	N	-	-	N	N	-
ECO	-	N	-	N	N	N	N	-	-	N	-	-
DES	-	-	-	-	-	-	N	N	-	N	-	-
IME	-	-	N	-	-	N	N	-	N	N	-	-
CGS	-	-	-	-	-	-	-	-	-	N	-	N
HSS	-	-	-	-	-	-	-	-	-	N	-	-
EEM	-	-	N	-	-	N	-	-	-	N	-	-
MSP	-	-	N	-	-	-	-	-	-	N	-	-
NET	-	-	N	-	-	N	-	-	-	N	-	-
PSE	-	-	N	-	-	N	-	-	-	N	-	N
Stats	-	-	-	-	-	-	-	-	-	N	N	-

Cost to Company:

Eligibilty:

• M.Tech / MS Compensation : Rs. 20,20,119/- Per Annum

• Masters Dual Compensation : Rs. 21,16,741/- Per Annum

Package Details

• M.Tech / MS Compensation : Rs. 20,20,119/- Per Annum

• Masters Dual Compensation : Rs. 21,16,741/- Per Annum

Bond: False

Medical

Requirements:

Resume Shortlist:

False

Aptitude Test:

True

**Aptitude Test** Duration:

45

Group

False

Discussion: Technical Test: True

**Technical Test Duration:** 

45

**Technical** 

Interview: Technical Interview

30

True

Duration:

Number of Techincal Interview

2

Rounds:

HR Interview: False