

Proforma

Companies open to your department

Company : Centre for Development of Advanced Computing
Company Name : Centre for Development of Advanced Computing
Nature Of Business : R&D in Electronics, Information Technology and allied areas
Designation : Knowledge Associate
Tentative Job Location : Any of the location at Noida, Mohali, Kolkata, Patna, Mumbai, Pune, Bengaluru, Hyderabad

Description :

Sr. No.	Project Code	No. Of Requirements	Preferred Qualification	Branch	Specialization (For Mtech/PhD)	Desired Skill sets
1	BLR1	2	M Tech / PhD	ELECTRICAL /ECE	Microelectronics / VLSI / COMPUTER SCIENCE WITH VLSI DESIGN RELATED PROJECT	Verilog / VHDL / OTHER HARDWARE DESCRIPTION LANGUAGES / High Level languages /Â PROCESSOR MICROARCHITECTURE / SECURED PROCESSOR MICROARCHITECTURES / CRYPTO INSTRUCTION SET EXTENSIONS AND CRYPTO FUNCTIONAL UNITS IN HARDWARE DESIGN; FORMAL VERIFICATION OF HARDWARE
2	BLR2	2	M Tech / PhD	ELECTRICALÂ / ECE	Microelectronics / VLSI / COMPUTER SCIENCE WITH VLSI DESIGN RELATED PROJECT or THESIS	ANALOG IC / CIRCUITS / SYSTEMS DESIGN; ANALOG IC LAYOUT; Digital IC design - RTL to GDS II; mixed signal IC design
3	BLR3	2	M Tech	Electrical/ ECE/CS	Communications/Power Systems Analysis	Modelling & Simulations
4	BLR4	2	M Tech	Electronics/ CS	Communications Protocols	DNS Security
5	BLR5	2	M Tech / PhD	CS/Info Sci/ AI/Multimedia/UX	AI/ML, Deep Learning, Visualization, HCI, Usability, Real-time processing	Transfer Learning, Pre-trained models, Immersive technologies, time-series and process data handling
6	HYD1	1	B.Tech/M.Tech	CSE/ECE/IT	-	Proficient in any one of the programming languages such as C, C++, Java, Python, Android.Â Basic understanding of OOPs concepts, Cyber Security & Network Security concepts. Good understanding of the core OS concepts
7	HYD2	2	B.Tech/M.Tech	Electronics/ CS	Microelectronics/ VLSI Design	â€¢ Expertise on System-Verilog/Verilog/VHDL and C languages â€¢ Experience in RTL development for IP/ASIC blocks â€¢ Experience in Physical Design & Verification â€¢ Experience in FPGA/Silicon Validation â€¢ Experience in Board Design â€¢ Knowledge of CPU architecture like ARM, RISC-V â€¢ Knowledge of Peripherals like DDR, HBM, PCIe, Cache Controller etc.
8	KOL1	3	B.Tech /M.Tech	CSE/ECE/IT		1. Basic Knowledge in Cybersecurity/cyberforensics with Programming Skill in Python ,JAVA,PHP,ASP.NET etc. 2. AI/ML/Image processing/Language technology Knowledge with Programming Skill in Python, JAVA, C/C++ etc.
9	MOH1	2	Ph.D	CSE,ECE	Cyber security	Malware analysis, Machine learning, Python programming, SCADA security, IoT security
10	MOH2	1	Ph.D	CSE	Mathematical modeling, Computational complexity , Algorithm analysis and optimization	Python programming, Mathematical modeling, Algorithm analysis & design
11	MOH3	2	Ph.D/M.tech	CSE,ECE	Cyber security,Machine learning, Data analytics	Cyber security, machine learning, Python, Malware analysis
12	MUM1	4	B.Tech	Computer Science, Information Technology	Not Applicable	excellent software development/engineering space, Testing,Â Analytical skills, UI/UX
13	NO1	1	M. Tech. or Ph.D.	CSE, ECE or EE (with specialization in communication)	Signal Processing, Mobile Communication, Machine Learning	Proficient inÂ Digital Communications and Networking Technologies, 5G testbeds, application and network layer QoS monitoring, MIMO communication and mmWave. Experience in ML & NLP algorithms (ASR solutions) development, Java/C/C++ coding and mobile application

						development is desirable.
14	NO2	2	B. Tech	Computer Science		Languages - Python, R, Java, JSP DB - NoSQL, Hadoop or other Big Data DB Functional - Big Data Analytics, ML, AI
15	PAT1	2	B.Tech / M.Tech.	CS/IT/ Electronics	Artificial Intelligence	Artificial Intelligence/ Programming Skill/ Data Analytics
16	PAT2	2	B.Tech / M.Tech.	CS/IT/ Electronics/ Instrumentation	IoT, Embedded System	Robotics / IoT/ Image Processing/ Drone/ Programming Skill/ Embedded System
17	PAT3	2	B.Tech / M.Tech.	CS/IT	Cyber Security	Cyber Security/ Cyber Forensic/IT Infrastructure / Data Analytics
18	PU1	2	B.Tech/ M.Tech /Ph.D	Bio-info/ Computational Biology/ Scientific Computing/Â Modeling & Simulation		Molecular Modeling Simulations, Machine Learning algorithms for biological data. Scripting language like Perl/Python, shell scripting and familiarity withÂ Linux environment desirable.
19	PU2	3	B Tech / M Tech	Electronics and Allied fields		Hardwar Design Principles, Hardware Description language, System Design, PCB Design, Embedded Design
20	PU3	1	B Tech / M Tech	Electronics and Allied fields		Knowledge of VLSI, DHL Language, Hardware design principles
21	PU4	1	B Tech / M Tech	CSE/IT/E&TC/ECE		Strong knowledge of C, C++ and Linux kernel internals; knowledge of Linux system programming and Embedded programming is must; Knowledge of Device driver development on Linux/Window is essential
	Total	41				

Department BT BS MT DoubleMajor dual dualB dualC Mdes MBA Phd MSc MSR

	AE	N	-	Y	N		N	N	N	-	-	N	-	Y
	BSBE	N	-	Y	N		N	N	N	-	-	N	-	Y
	CE	N	-	Y	N		N	N	N	-	-	N	-	Y
	CHE	N	-	Y	N		N	N	N	-	-	N	-	Y
	CSE	Y	-	Y	Y		Y	Y	Y	-	-	N	-	Y
	EE	Y	-	Y	Y		Y	Y	Y	-	-	N	-	Y
	ES	-	N	Y	-		N	-	N	-	-	N	-	-
	ME	N	-	Y	N		N	N	N	-	-	N	-	Y
	MSE	N	-	Y	N		N	N	N	-	-	N	-	Y
	PHY	-	N	-	N		N	N	N	-	-	N	N	-
Eligibilty :	CHM	-	N	-	N		N	N	N	-	-	N	N	-
	MTH	-	N	-	N		N	N	N	-	-	N	N	-
	ECO	-	N	-	N		N	N	N	-	-	N	-	-
	DES	-	-	-	-		-	-	N	N	-	N	-	-
	IME	-	-	Y	-		-	N	N	-	N	N	-	-
	CGS	-	-	-	-		-	-	-	-	-	N	-	Y
	HSS	-	-	-	-		-	-	-	-	-	N	-	-
	EEM	-	-	Y	-		-	N	-	-	-	N	-	-
	MSP	-	-	Y	-		-	-	-	-	-	N	-	-
	NET	-	-	Y	-		-	N	-	-	-	N	-	-
	PSE	-	-	Y	-		-	N	-	-	-	N	-	Y
	Stats	-	-	-	-		-	-	-	-	-	N	N	-

BTech â€“ Rs. 7,80,6000 per annum

Cost to Company : MTech â€“ Rs. 8,49,600 per annum

Ph.D â€“ Rs. 9,18,600 per annum

BTech â€“ Rs. 7,80,6000 per annum

Package Details : MTech â€“ Rs. 8,49,600 per annum

Ph.D â€“ Rs. 9,18,600 per annum

Bond : False

Medical Requirements :

Resume Shortlist : False

Aptitude Test: False

Group False

Discussion:

Technical
Test: False

Technical
Interview: False

HR
Interview: False
