Proforma

Companies open to your department

Company:

Texas Instruments

Company Name:

Texas Instruments

Nature Of Business:

Semiconductor R&D

Designation: Digital Engineer

Tentative Job Location:

Bangalore

Texas Instruments Incorporated is an American technology company that designs and manufactures semiconductors and various integrated circuits. TI is one of the top-10 semiconductor companies worldwide, based on sales volume and is focused on developing analog chips and embedded processors, which account for more than 80% of their revenue. TI has been a pioneer in many innovations in the semiconductor domain including the development of the first integrated circuit;Â the first patent on a singlechip microprocessor, the first single-chip linear predictive coding speech synthesizer, developing prototype of the world's first transistor radio and the invention of the digital light processing device (also known as the DLP chip), which serves as the foundation for the award-winning DLP technology and DLP Cinema.

TI India was set up in 1985 and has R&D presence for all the major business units of TI including Analog -(Data Converters, Amplifiers, Clocks & Synthesizers, Motor Drives, Power Management) and Embedded Processors (Connected Microcontrollers, Radar, ADAS- Advanced Driver Assistance and Infotainment and Industrial Processors etc.) and caters to products for different market segments - Industrial, automotive, personal Electronics, Communication and Enterprise.

Description:

As a Digital Engineer at TI, You'II define, design, model, implement and document Integrated Circuits (ICs). You will also have the opportunity to work in exciting areas like audio, energy automation, electronic point of service, industrial automation, infotainment, ADAS, Imaging, high speed interface, clocking medical, high volume linear, automotive, storage, power supply, battery management, linear power, DLP and many more.

Your responsibilities will include:

- 1) Partnering with business teams and systems engineering to develop mutually agreeable design specifications
- 2) Providing high-level analysis on chip architecture trade-offs to ensure spec compliance and superior performance at a competitive cost
- 3) Participating in design reviews and creating the necessary design and product documentation
- 4) Supervising IC Layouts to ensure a high performance standard
- 5) Characterizing prototypes, developing test specifications and coordinating with test/product engineering to drive product releases

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6) Driving behavioral models

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EEM

Department BT BS MT DoubleMajor dual dualB dualC Mdes MBA Phd MSc MSR

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Eligibilty:

MSP N NET N N **PSE** Y Y Y N Stats N B.tech: 21,50,880 Company: M.tech:24,48,905 B.tech: 21,50,880 Fixed: 14,00,000 Variable: 2,10,000 benefits: 1,15,880 Joining Bonus: 350,000 (paid in 2 installments, half immediately on joining and the rest half on completion of 1 year) Relocation assistance one time: 75,000 M.tech:24,48,905 Fixed: 15,25,000 Variable: 2,28,750 benefits: 1,20,155 Joining Bonus: 500,000 (paid in 2 installments, half immediately on joining and the rest half on completion of 1 year) Relocation assistance one time: 75,000 Requirements False Shortlist: True **Aptitude Test** 30 min Duration: False Discussion: Technical True Technical 45 min Technical True Interview: Technical Interview 1 hour Number of **Techincal** 2

Cost to

Package Details:

Bond: Medical

Resume

Aptitude

Test:

Group

Test:

Test

Duration:

Duration:

Interview Rounds: HR

Interview: HR Interview

Duration:

True

15 min