## **Proforma**

Companies open to your department

Company : Apple Company Apple

Name: Nature Of Business:

Hardware Technology

Designation: Software Engineer, Hardware Technology

Tentative Job Location :

Bangalore

Title: Embedded 5G/4G Cellular RF Software/Firmware Engineer

## **Job Summary:**

Do you have a passion for invention and self-challenge?

Do you thrive on pushing the limits of what  $\hat{a} \in \mathbb{T}^m$  s considered feasible?

As part of a world class modem team, you'll craft sophisticated leading-edge embedded firmware that deliver more performance in our products than ever before. You'll work across disciplines to transform improved hardware elements into a single, integrated design.

Join us, and youâ $\in$  Ml help us innovate new wireless systems technologies that continually outperform the previous iterations. By collaborating with other product development groups across Apple, youâ $\in$  Ml push the industry boundaries of what wireless systems can do and improve the product experience for our customers across the world.

As a Wireless Firmware Engineer on the physical layer development team, you will be at the center of the embedded 5G/4G/multimode cellular firmware effort within a silicon design group responsible for designing and productizing state-of-the-art cellular SoCs.

This position requires someone comfortable will all aspects of embedded software development, that thrives in a dynamic cross-functional organization, is not afraid to debate ideas openly, and is flexible enough to pivot on constantly evolving requirements.

## Description: **Key Qualifications:**

- $\bullet$  Knowledge of mobile communications in 4G/5G 3GPP protocols and physical layer & radio aspects
- Solid understanding of Digital signal processing and RF fundamentals.
- Solid understanding of software engineering principles, and core computer science fundamentals.
- Excellent command of C and C++, and working experience with compilers, build and source code control tools.
- Working knowledge of CPU architecture, HW/SW interfaces and memory management.
- Proficiency with debugging in embedded software systems. Familiarity with JTAG and ETM Trace tools.
- Knowledge of Communication theory, Wireless standards, Radio architecture and Interfaces

## **Description:**

- Specify, design, and develop the physical layer embedded software architecture of a state-ofthe-art mobile wireless communications system.
- Design, develop and optimize the cellular modem processing algorithms pertaining to Baseband and RF components in a cellular system.
- Work with Baseband & RF systems and hardware designers to define hardware architecture and software computing requirements.
- Work with cross functional teams to manage dependencies while driving the key Radio features.

Department BT BS MT DoubleMajor dual dualB dualC Mdes MBA Phd MSc MSR

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BSBE	N - N N	N N	N -	-	N -	N
CE	N - N N	N N	N -	-	N -	N
CHE	N - N N	N N	N -	-	N -	N
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Cost to Company :	Break up of CTC - Base $16,50,000  \text{\^A} + 2,00,000  \text{Joining Bonus} + \hat{\text{A}}  \text{USD } 58,800  \text{worth of RSUs over 4}$ years split equally (it will be $1,073,100  \text{INR}$ per year)  Total CTC : $60-62  \text{LPA}$ CTC : $\hat{\text{A}}  \text{INR\^A}  3,061,700$														
Package Details :	Break up of CTC - Base $16,50,000  \hat{A} + 2,00,000  \text{Joining Bonus} + \hat{A}  \text{USD } 58,800  \text{worth of RSUs over 4 years split equally (it will be 1,073,100 INR per year)}$														
Bond : Medical Requirements :	Total CTC : False	60-6	52 LI	PA											
Resume Shortlist :	True														
Resume Shortlist Criteria:	N/A														
Aptitude Test:	False														
Group Discussion:	False														
Technical Test: Technical	True														
Test Duration:	N/A														
Technical Interview:	True														
Technical Interview Duration:	N/A														
Number of Techincal Interview Rounds:	0														
HR Interview:	True														
HR Interview Duration:	N/A														