

Lecture # 14.1 CHE331A

Multiple Reactions
Occur in the
Chemical Industry

Categorization of
Multiple Reactions

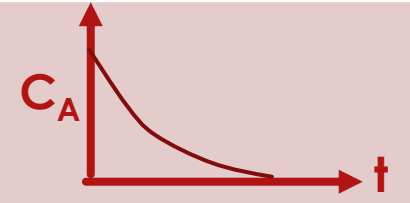
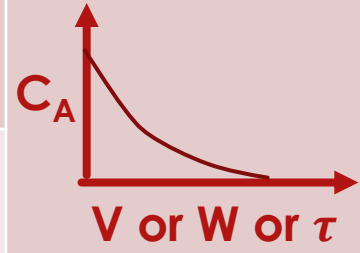
Parallel, Series,
Complex and
Independent
Reactions

Definitions of
instantaneous and
overall Selectivity
and Yields

**Operating
conditions and
Reactor schemes
for Multiple
Reactions**



Basic design equations for ideal reactors revisited

Reactor	Design Eq ⁿ	C _A vs. t or V or W
BR	$\frac{dN_A}{dt} = r_A \cdot V$	
CSTR	$V = \frac{F_{A0} - F_A}{-r_A}$	Conc. is at the minimum
PFR	$\frac{dF_A}{dV} = r_A$	
PBR	$\frac{dF_A}{dW} = r'_A$	



Selection of Reactor type and conditions for

Parallel reactions $A \xrightarrow{k_D} D$ and $A \xrightarrow{k_U} U$

- ▶ Rate laws: $r_D = k_D C_A^{\alpha_1}$ and $r_U = k_U C_A^{\alpha_2}$
- ▶ Rate of disappearance of A: $-r_A = k_D C_A^{\alpha_1} + k_U C_A^{\alpha_2}$
- ▶ $S_{D/U} = \frac{r_D}{r_U} = \frac{k_D}{k_U} C_A^{\alpha_1 - \alpha_2}$ Methods to increase $S_{D/U}$ depends $\alpha_1 - \alpha_2$
- ▶ **Case I:** $\alpha_1 > \alpha_2$ and $\alpha_1 - \alpha_2 = a$ then $S_{D/U} = \frac{k_D}{k_U} C_A^a$
- ▶ To increase $S_{D/U}$, we need to keep C_A^a as high as possible
 - No inerts and operate at high pressures if gas phase reaction
 - Batch or PFR/PBR, instead of a CSTR, since conc is higher



Selection of Reactor type and conditions for Parallel reactions $A \xrightarrow{k_D} D$ and $A \xrightarrow{k_U} U$ continued

- ▶ **Case 2:** $\alpha_2 > \alpha_1$ and $\alpha_2 - \alpha_1 = b$ then $S_{D/U} = \frac{k_D}{k_U} \frac{1}{C_A^b}$
- ▶ To increase $S_{D/U}$, we need to keep C_A^b as low as possible
 - Use inerts and operate at low pressures if gas phase reaction
 - Use a CSTR instead of Batch or PFR/PBR
 - Recycle reactor could also be used
- ▶ Temperature effects can be rationalized if the relative activation energies are known
 - Recommendations can be made for equal reaction orders



Temperature effects for Parallel reactions

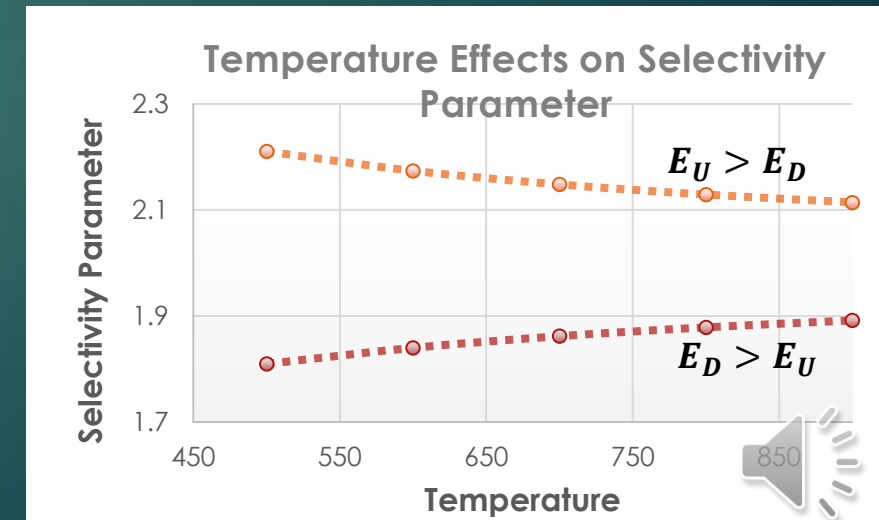
► $r_D = k_D C_A^{\alpha_1}$ and $r_U = k_U C_A^{\alpha_1}$ $S_{D/U} = \frac{r_D}{r_U} = \frac{k_D}{k_U} = \frac{A_D}{A_U} \exp \left[-\frac{(E_D - E_U)}{RT} \right]$

► Case 4': $E_D > E_U$

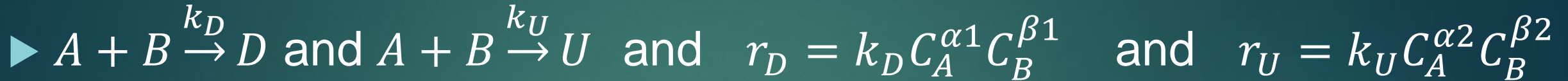
- k_D increases more rapidly with increase in temp compared to k_U
- Reaction operated at the highest possible temp

► Case 5': $E_U > E_D$

- k_U increases more rapidly with increase in temp compared to k_D
- Reaction should be operated at the lowest possible temp



Parallel reactions for two reactants – Reactor selection



► $S_{D/U} = \frac{r_D}{r_U} = \frac{k_D}{k_U} C_A^{\alpha_1 - \alpha_2} C_B^{\beta_1 - \beta_2}$ is to be maximized $\rightarrow \alpha_1 - \alpha_2$ & $\beta_1 > \beta_2$

► Case I: $\alpha_1 > \alpha_2$ and $\beta_1 > \beta_2$

- $\alpha_1 - \alpha_2 = a$ & $\beta_1 - \beta_2 = b \rightarrow$

- To maximize $S_{D/U} = \frac{k_D}{k_U} C_A^a C_B^b$ C_A and C_B should be high

- Tubular reactor (PFR/PBR)

- Batch reactor

- High pressures and no inerts



Parallel reactions for two reactants – Reactor selection - Case II

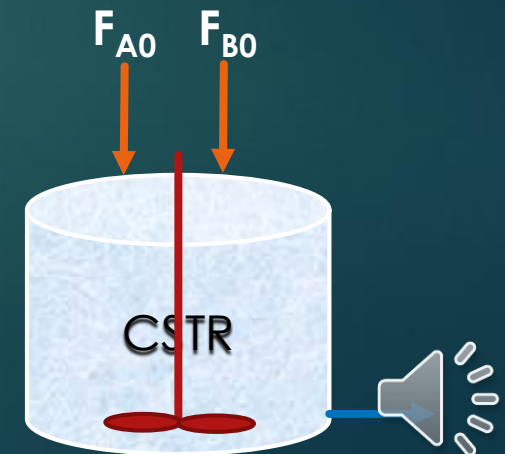
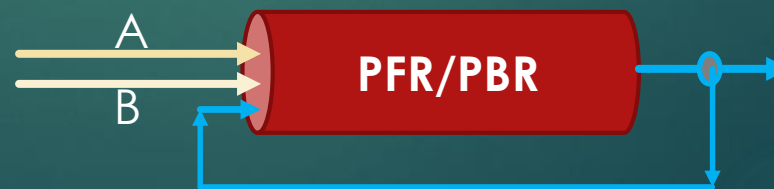
► $S_{D/U} = \frac{r_D}{r_U} = \frac{k_D}{k_U} C_A^{\alpha_1 - \alpha_2} C_B^{\beta_1 - \beta_2}$ $\alpha_1 - \alpha_2$ and $\beta_1 - \beta_2$

► Case II: $\alpha_2 > \alpha_1$ and $\beta_2 > \beta_1$ \rightarrow $\alpha_2 - \alpha_1 = a$ & $\beta_2 - \beta_1 = b$

○ To maximize $S_{D/U} = \frac{k_D}{k_U} \frac{1}{C_A^a C_B^b}$ C_A and C_B should be low

○ CSTR or a PFR/PBR with a large recycle

○ Low pressures (gas-phase) and use inerts



Parallel reactions for two reactants – Reactor selection - Case III/IV

- Case III: $\alpha_1 > \alpha_2$ and $\beta_2 > \beta_1$ $\rightarrow \alpha_1 - \alpha_2 = a$ & $\beta_2 - \beta_1 = b$
- To maximize $S_{D/U} = \frac{k_D}{k_U} \frac{C_A^a}{C_B^b}$ C_A should be high and C_B should be low
 - Semi-Batch reactor with A initially present and B slowly fed
 - Tubular reactor (or membrane reactor) with side streams of B fed
 - Series of small CSTRs with A fed to the first reactor and B fed to each

► Case IV: is similar

$\alpha_2 > \alpha_1$ and
 $\beta_1 > \beta_2$

