

Pedro Henrique Neves dos Santos

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Date of birth: 15 Aug 1987 (32 y.o.)

EDUCATION

University of Sao Paulo - USP

Bachelor's degree in Electrical Engineering

Dec 2014

- *Undergraduate research project:* Boost DC-DC converters.
- *Exchange program:* **Hansa Language Centre - Toronto, Canada**

TECHNICAL SKILLS

Engineering projects

- Electrical schematics and PCB layout. Proficient in **Altium Designer**.
- Power and digital simulations - **LTSpice**.
- PCB bring-up; Prototype validation tests.
- Hardware documentation.
- Proficient in Linux environment.
- C, Matlab, PHP, Python, \LaTeX .

Language skills

- Portuguese: Native speaker.
- English: Advanced speaker, fluent writer.

WORKING EXPERIENCE

Embraer - Campinas/SP

Product Development Engineer

Jan 2018 - present

- Hardware design; PCB debug and bring-up;
- Product development assistance; EMI/EMC testings assistance; Hardware documentation. Product documentation.

Padtec S/A - Campinas/SP

Hardware Engineer

Jul 2016 - Jan 2018

- Hardware design for DWDM optical systems (schematic and PCB layout); PCB debug and bring-up;
- Product development assistance; EMI/EMC testings assistance; Hardware documentation. Maintenance of legacy products. EOL management.

Ibramed - Amparo/SP

Hardware Engineer

Nov 2015 - Jun 2016

- Hardware design for medical equipment. Projects based on microcontrollers Atmel[®], Microchip[®] and Renesas[®]. Hardware for solid state lasers for medical application.

Padtec S/A - Campinas/SP

Internship

Jan 2014 - Dec 2014

- Small hardware projects; Debug of a proprietary embedded computer with Intel[®] Atom; Test setup; **Altium** libraries maintenance and updates.