

# Zhixian Huang

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## SUMMARY

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- Staff Software Development Engineer in Fortinet Inc.
- Strong experience on Unix/Linux OS development.
- Familiar with Computer Networking, Computer Security, Computer Architecture, Embedded Systems, Robotics.
- Language: C/C++, Python, Shell Script.

## EMPLOYMENT

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Staff Software Development Engineer 04/2022 - present  
Fortinet Inc., Sunnyvale, California

- Develop FortiOS for the Next Generation Firewall (NGFW).
- Lead on bringing up new products.
- Tune up system performance and enforce system security for FortiGate.

Software Engineer 02/2018 - 04/2022  
Juniper Networks Inc., Sunnyvale, California

- Contribute system level features and supports on enterprise networking devices in the platform team.
- Lead six mini projects across four new products, including SRX-SPC3, SRX-RE3 and ACX7100.
- Involve in expertise of device firmware, system resiliency and hardware diagnostics.

Embedded Systems Engineer 05/2015 - 02/2018  
Rufus Labs Inc., Los Angeles, California

- Brought up a wearable device starting from designing its schematics.
- Wrote drivers for subsystems including display, power management, sensors, and other peripherals.
- Ported Android Marshmallow and got it running on the product.
- Maintained the Android code base and supporting the application team on system related issues.

Research & Development Engineer 10/2013 - 06/2014  
Shenzhen MinDe Electronics Technology Ltd., Shenzhen, China

## PROJECTS

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Single Foot Balance of Humanoid Robot

- Achieved balancing task on a humanoid robot with 25 Degrees of Freedom.
- Implemented code of trajectory planning, inverse kinematics, PID controller and Kalman filter.
- Fine-tuned the robot to step in place continuously and smoothly.

Weenix Operating System Kernel Implementation

- Developed a primitive kernel for a UNIX-like operating system.
- Wrote major components including process & thread, virtual file system, and virtual memory.
- Realized running multiple applications concurrently on this system.

## EDUCATION

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M.S. in Electrical Engineering (GPA: 3.60/4.0) 08/2014 - 05/2016  
University of Southern California, Viterbi School of Engineering

B.E. in Telecommunications Engineering (GPA: 3.67/4.0) 09/2009 - 07/2013

