UDS-107

DMI10x

Unit Data Sheet

PART NUMBER/ MNEMONIC	NAME	CLEI	ECI/ BAR CODE	CPR	STATUS
625611-000-001/ DMI101	Drop Module Interface Plug-in Unit	SNCLYV05AA	681543--3	A72327	MD
625611-000-002/ DMI102	Drop Module Interface Plug-in Unit	SNC1FKJ6AA	203259	B72463	Active

FEATURES AND APPLICATION NOTES

- DMI101, 625611-000-001, stores on-board provisioning database information for the Virtual Tributary Group (VTG) plug-in units. The DMI101 is designed to complement the COA20x series of plug-in units.
- DMI102, 625611-000-002, replaces the DMI101. The DMI102 does not store on-board provisioning data (supports centralized system database management). The DMI102 is designed to complement the COA3xx and later series plug-in units.
- Provides interface between low speed facilities (i.e., asynchronous Digital Signal Level 1 [DS1] traffic) and the Synchronous Transport Signal Level 1 (STS1) group bus.
- Supports floating Virtual Tributary (VT) 1.5 groups (i.e., VT group to STS1 mux/demux).
- Provides performance monitoring for the VT path and DS1 facilities.
- Front panel mounted Light Emitting Diode (LED) status indicators: green LED for active unit condition and red LED for failed unit condition.
- Mounts in the lower, mid-section of the 1603 SM shelf (under the power supplies).

DESCRIPTION

The DMI10x, 625611-000-00x, is a Drop Module Interface (DMI) plug-in unit for the 1603 SM system. The DMI10x serves as an interface between low speed VTG101 facilities and the Synchronous Transport Signal Level 1 (STS1) group bus. The DMI10x operates only in conjunction with VT Group (VTG1xx) plug-in units. The DMI10x supports asynchronous DS1 interfaces and floating Virtual Tributary (VT) 1.5 groups.
Figure 1 is a functional block diagram of the DMI10x. The DMI10x circuitry consists of the following major functional blocks:

- Microprocessor control core - responsible for control functions.
- Flash Electrically Erasable Programmable Read-Only Memory (EEPROM) and Random

Access Memory (RAM) blocks - these two sections contain program and data memory for the microprocessor.

- Miscellaneous Timing and Control Logic - provides interfaces between the microprocessor and various hardware control points within the low speed subsystem.

NOTE: *The DMI102 incorporates High Level Data Link Controller (HDLC) circuitry. Future system software releases will utilize this HDLC circuitry, which reduces the DMI download programming time.*

Figure 2 shows the mechanical layout of the DMI10x plug-in assembly.

Figure 1. DMI10x, 625611-000-00x, Functional Block Diagram

Figure 2. DMI102, 625611-000-002, Plug-in Unit