**Mr. Vicheka PHOR**

46





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**Hardware and Software Engineering**

**TECHNICAL SUMMARY**

**Operating Systems** : Unix, Linux, Windows 7

**Languages** :Java, Java Swing, Android, Verilog, VHDL, C, MATLAB, C++

**Software** :CADENCE, XILINX, ModelSIM, SYNOPSYS, Lattice Diamond,

Altium Designer, Active-HDL, STK (Satellite Tool Kit), AUTOCAD 2D,

CodeBlock, Microsoft Visual Studio, NetBeans, Adobe Photoshop, Android Studio

**Hardware** :FPGA, RISC microprocessor, LatticeECP3 Video Protocol boards

**Web development** : HTML5, CSS, JavaScript, JQuery, MySQL, PHP, Adobe Dreamweaver, Drupal, Servlet and JSP

**Office** :Word, Excel, PowerPoint, Open office

**EDUCATION**

**2012 – 2014** : **Université Paul Sabatier** – **Toulouse, France**

*Master's degree in Embedded Systems - Hardware and Software Engineering*

**2010 – 2011** : **ENSEEIHT – Toulouse, France**

*Master's degree in Telecommunication and IT*

**2005 – 2010** : **Institute of Technology of Cambodia** – **Phnom Penh, Cambodia**

*Bachelor's degree in Electrical and Energy Engineering*

**HONORS**

SUMITOMO Scholarship, 2009

Agence Universitaire de la Francophonie (AUF) Scholarship, 2010

**ACADEMIC PROJECTS**

RISC microprocessor design, December 2012 – February 2013

Renewable Energies Investment Calculation (Group of 3 members),October – December 2009

Electric Power Supply in Industry,April – June 2009

**EXPERIENCE**

**IT Lecturer**

*Kirirom Institute of Technology – Kompong Speu, Cambodia*

November 2015 – October 2016

* Internship supervisor: drone programming project, projection mapping project, AI, EDM
* Teaching: Android, servlet&jsp, website development (HTML, CSS, JS), DBMS, MySQL

**Software Engineer**

*PATHMAZING Inc Company – Phnom Penh, Cambodia*

July 2015 – October 2015

* Android mobile software development: designing and customization UI, using third libraries (Blur Behind, HideOnScroll, SlidingLayer, Sliding Menu…), implementing functionalities, integrating social network (Twitter, Google, Facebook) and multiple languages (Khmer and English), designing, coding, debugging and testing
* Basic Knowledge of NodeJs: express framework, user management system, third libraries of NodeJs (Sequelize, MySQL, MongoDB, …)

**Hardware Engineer Intern**

*Enciris Technologies Company – Gaillac, France*

April 2014 – September 2014

* Design, develop, test, integrate, and verify FPGA designs for video processing hardware
* Working on the SDI video interface by demonstrating the implementation of a Lattice tri-rate SDI PHY IP core on a Lattice FPGA of the Enciris LT-125 board in order to provide an SMPTE 3G/HD/SD SDI video input functionality on the board without using any external chipset like the Gennum chipset which is used today on Enciris boards
* Working on the H.264/AVC video decoding algorithm, particularly the H.264/AVC bitstream decoder hardware architecture design and implementation

**Satellite Communication Engineer Intern**

*Telecommunications for Space and Aeronautics (TéSA) Laboratory – Toulouse, France*

March 2011 – August 2011

* Determination of the orbital mechanics for different type of orbit
* Optimization satellite constellation
* Calculation the link budget
* Analysis of the impact of orbit types, frequency band and modulation types on link budget

**Mobile Communication Engineer Intern**

*INTechCom Services Co., Ltd Company – Phnom Penh, Cambodia*

February 2010 – May 2010

* [GSM, Cell Planning & Frequency Reuse](http://fr.slideshare.net/sanjida2222/presentationj)
* Installation, Configuration and Commissioning of BTS and Microwave
* Preparation documents for PAT (Provisional Acceptance Test)
* Preparation documents for Fixed Assets

**Mobile Communication Engineer Intern**

*HUAWEI TECHNOLOGIES CO., LTD Company – Phnom Penh, Cambodia*

July 2008 – August 2008

* General principle of GSM and BTS
* Installation, Configuration and Commissioning of BTS and Microwave

**LANGUAGES**

**French** : Good

**English** : Good

**HOBBIES**

Football, tennis, badminton, Music