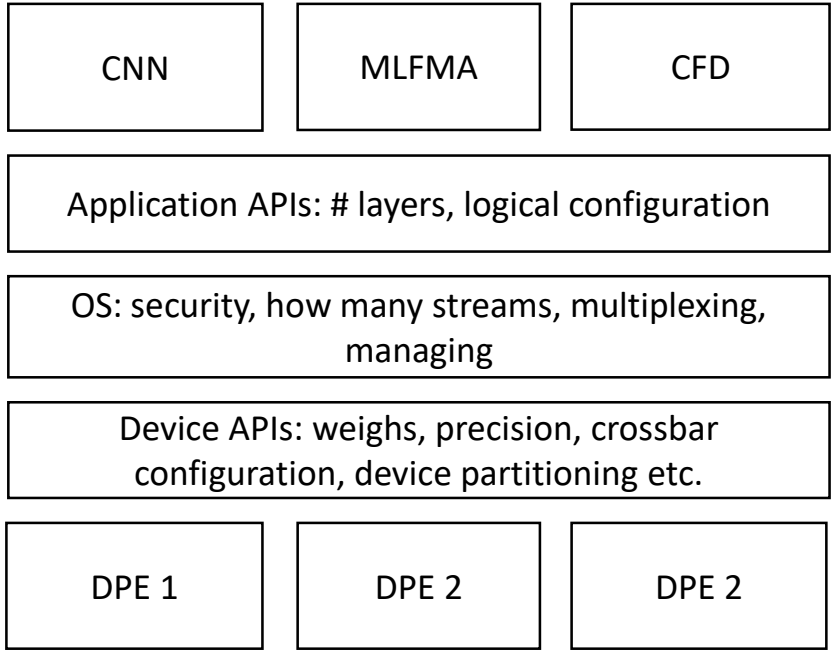


Hardwired device architecture and application



Abstract device architecture and application through APIs