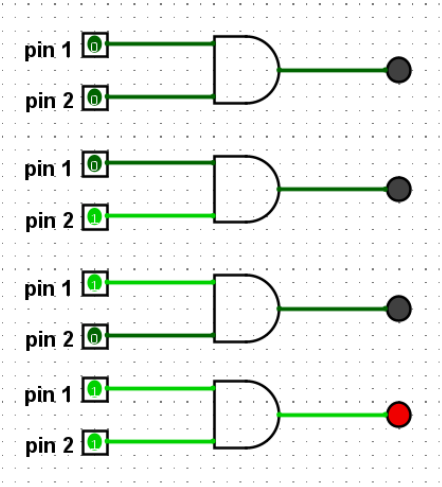
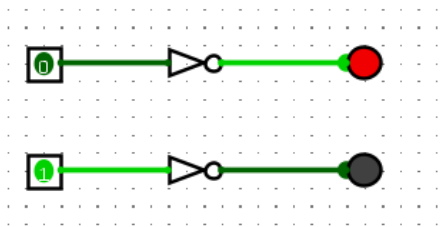


AND GATE



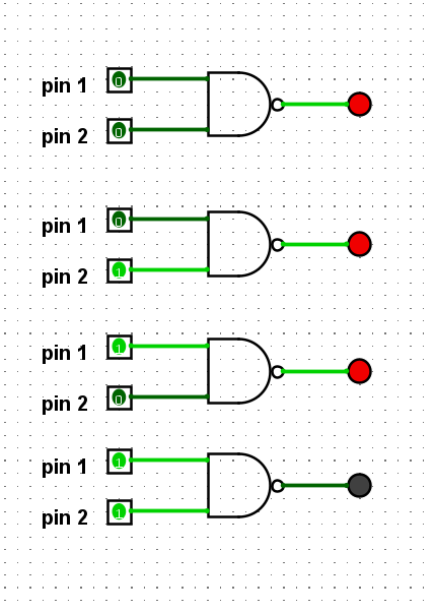
Pin 1	Pin 2	Output
0	0	0
0	1	0
1	0	0
1	1	1

NOT GATE



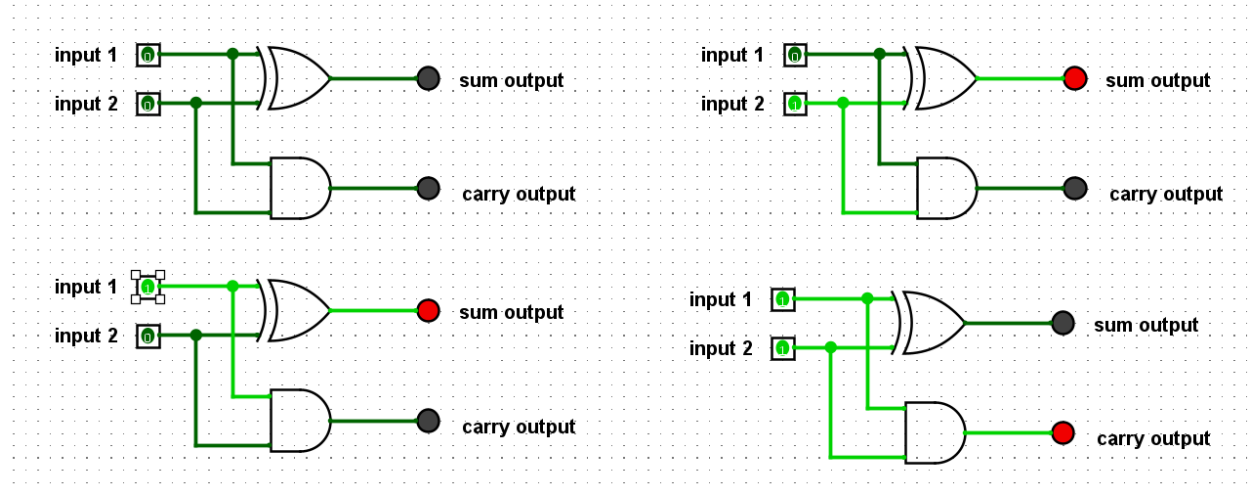
Pin	Output
0	1
1	0

NAND GATE



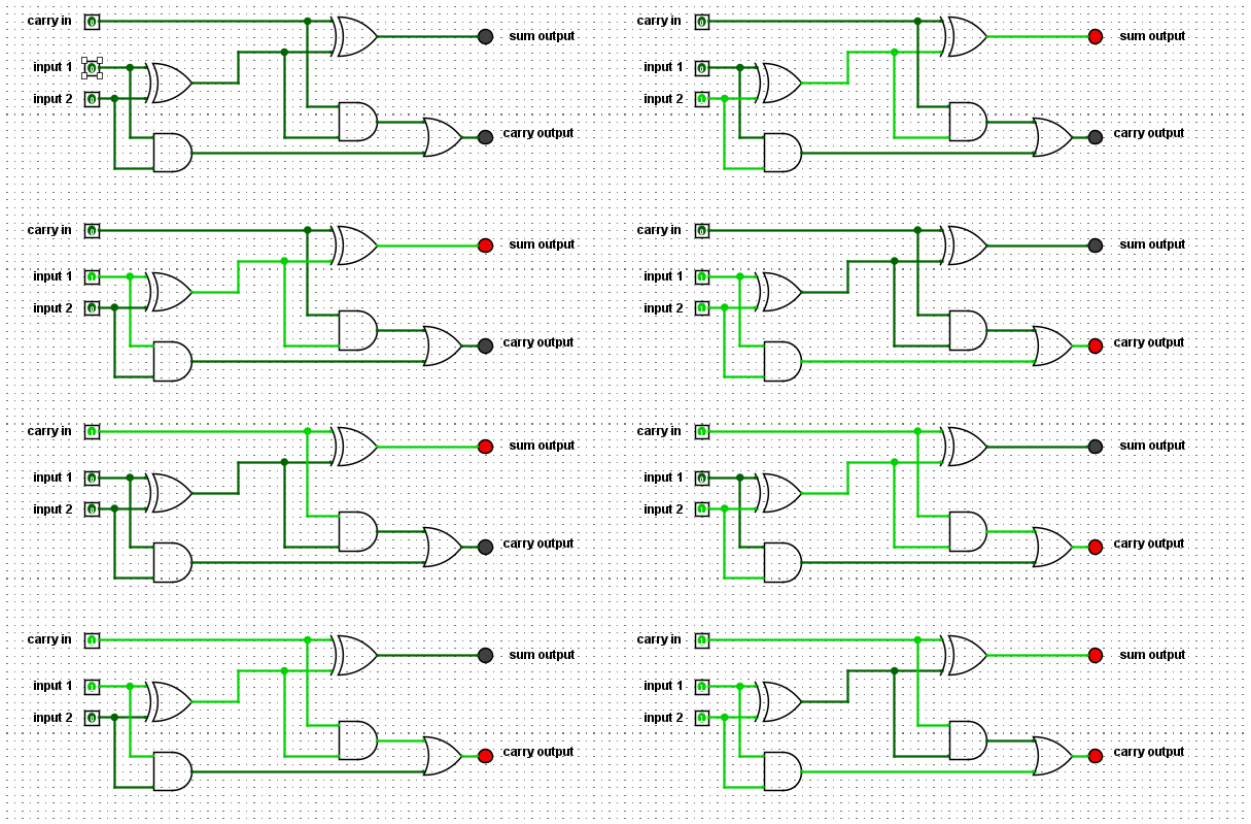
Pin 1	Pin 2	Output
0	0	1
0	1	1
1	0	1
1	1	0

HALF ADDER



Input 1	Input 2	Sum Output	Carry Output
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

FULL ADDER



Input 1	Input 2	Carry In	Sum Output	Carry Output
0	0	0	0	0
0	1	0	1	0
1	0	0	1	0
1	1	0	0	1
0	0	1	1	0
0	1	1	0	1
1	0	1	0	1
1	1	1	1	1