



Bus is only driven when any of the Read/Write lines goes high
 default direction is READ (aka put value of counter onto bus)
 74LS593 needs clock pulse both for setting the counter and increasing the counter
 High Byte is only increased if overflow output RCO goes low

Sheet: /
 File: PC_counter.sch

Title:

Size: A4

Date:

KiCad E.D.A. kicad (5.1.4)-1

Rev:

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