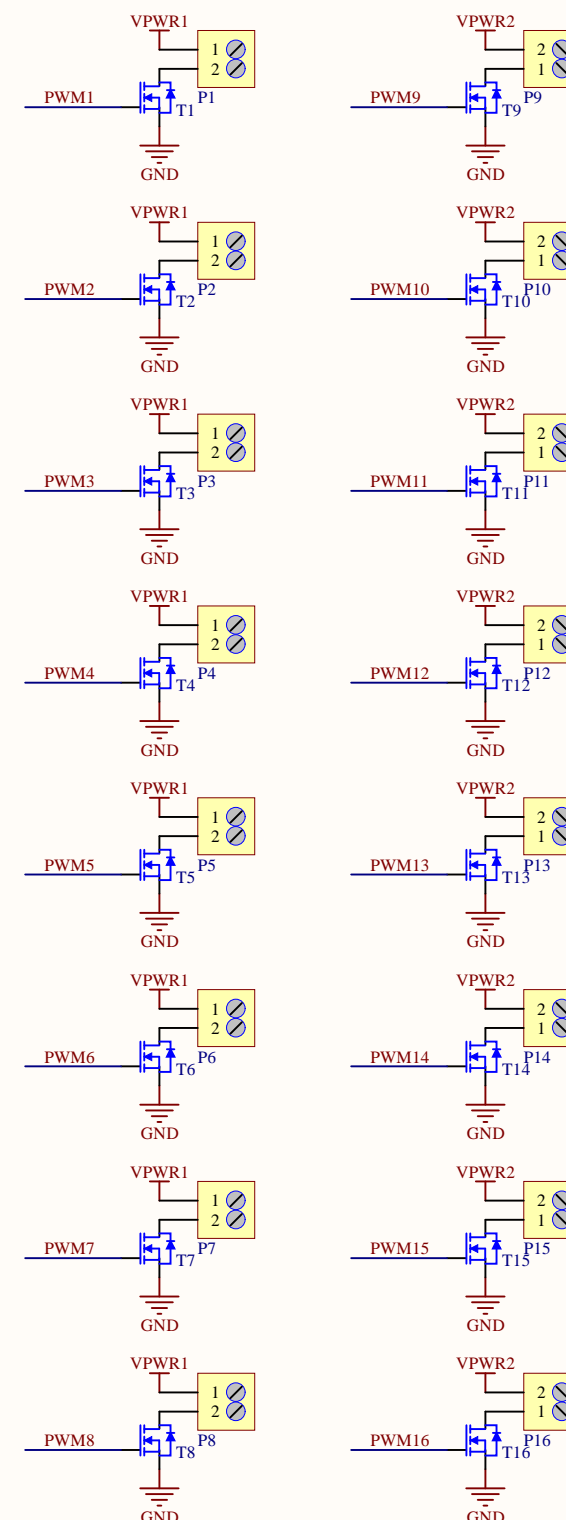
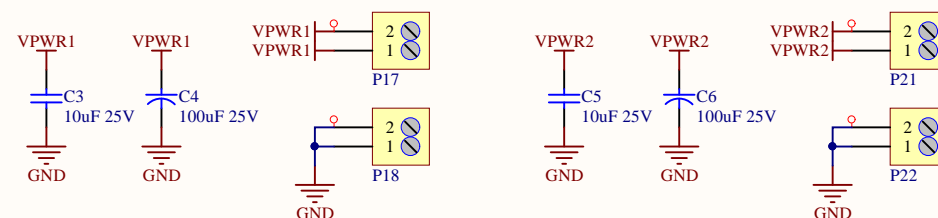
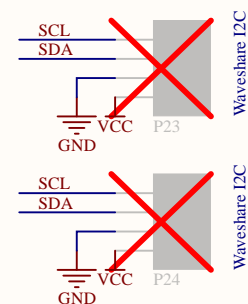
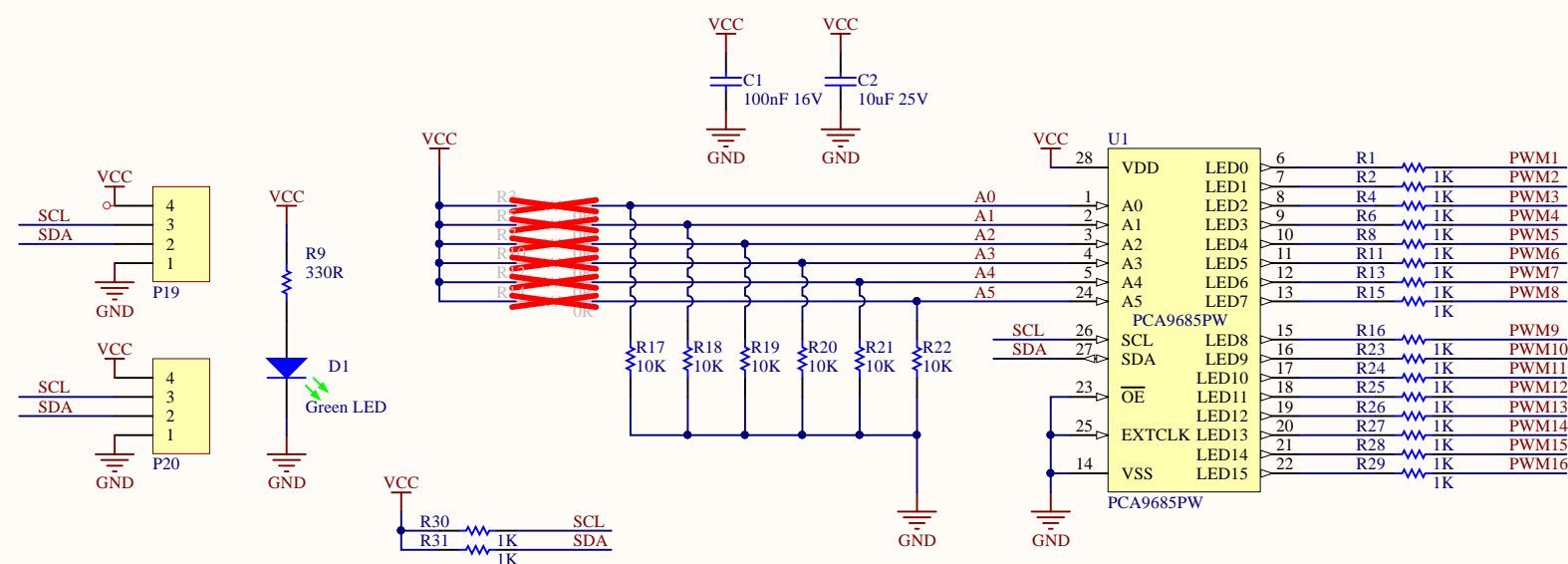
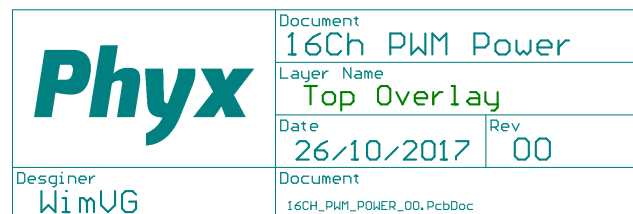
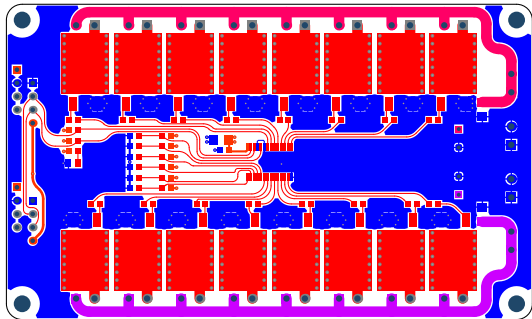


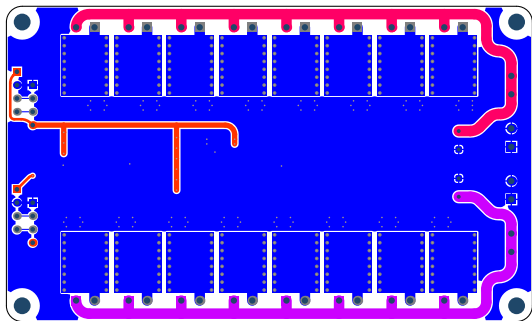
# 16Ch PWM Power



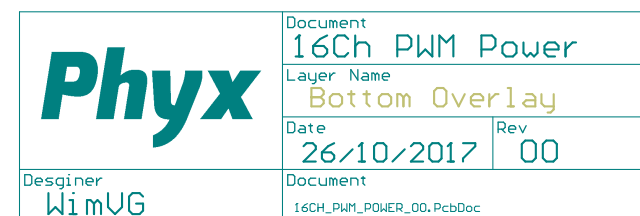


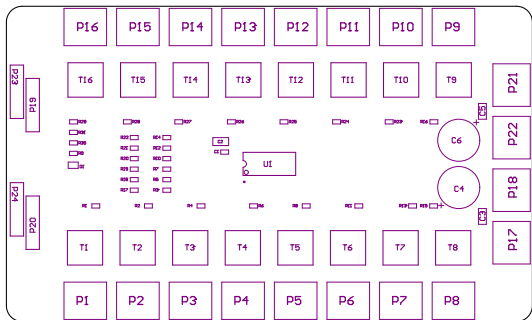



<b>Phyx</b>	Document	
	16Ch PWM Power	
	Layer Name	
	Top Layer	
Designer	Date	Rev
	26/10/2017	00
WimVG		Document
		16CH_PWM_POWER_00.PcbDoc

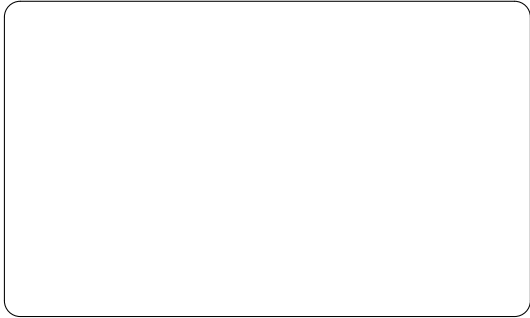


<b>Phyx</b>	Document	
	16Ch PWM Power	
	Layer Name	
	Bottom Layer	
	Date	Rev
	26/10/2017	00
Designer	Document	
WimVG	16CH_PWM_POWER_00.PcbDoc	





	Document	
	16Ch PWM Power	
	Layer Name	
	Assembly Top	
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16Ch PWM Power	
Layer Name	
Assembly Bottom	
Date	Rev
26/10/2017	00
Document	
16CH_PWM_POWER_00.PcbDoc	

