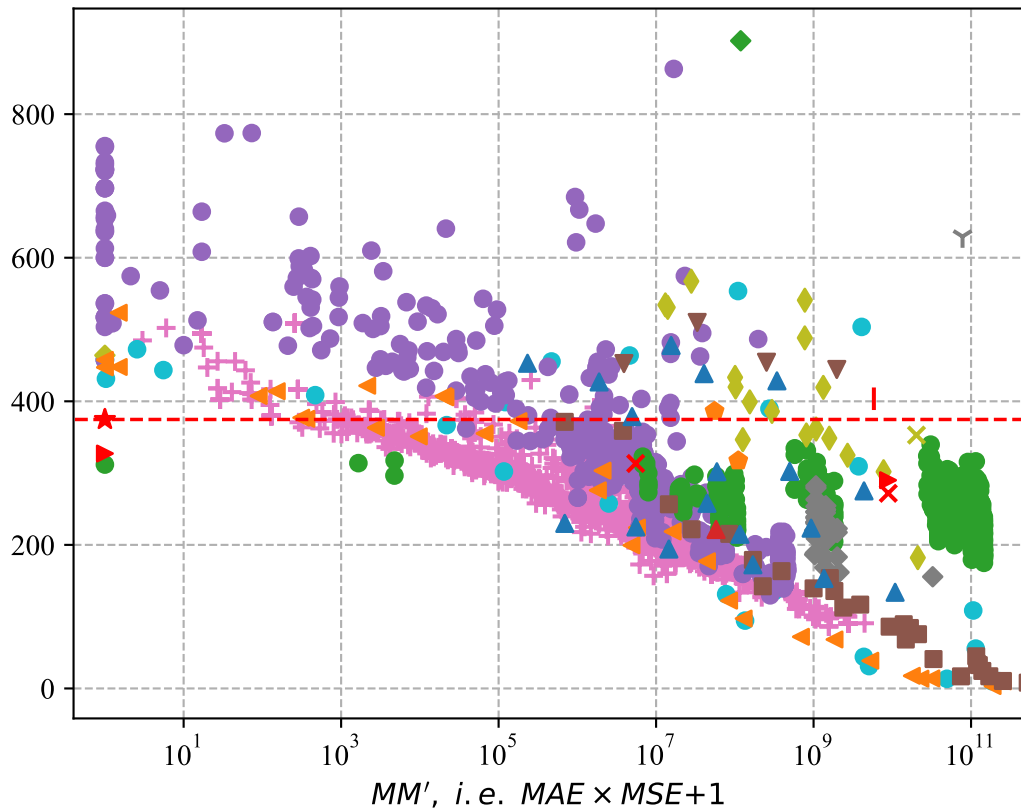


$PDA, i.e. Power (W) \times Delay (ns) \times LUT Count$



- ★ Xilinx Default IP (exact)
- ◆ Wallace (exact)
- Truncation
- ▲ SDLC
- | KMap
- × AC
- ◆ RoBA
- ⬡ CR
- Y OU
- ▼ DRUM
- ◆ TOSAM
- ▲ PPAM
- EvoApprox8
- EvoApproxLite
- ◆ FPT 22
- × CaCc
- SMApplLib
- ▲ ApproxFPGAs
- ▲ TCAD22
- + This thesis