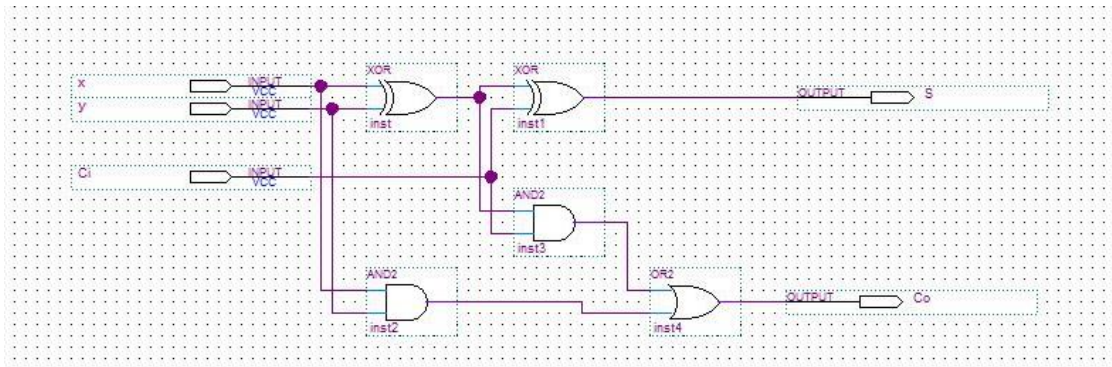


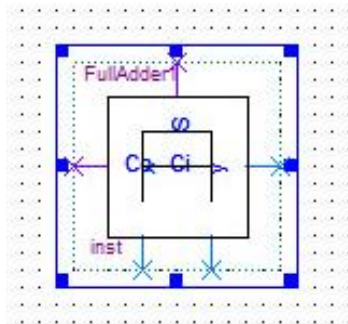
# Switching Circuit and Logic Design B10901151 林祐群

## I. 4-bit Adder :

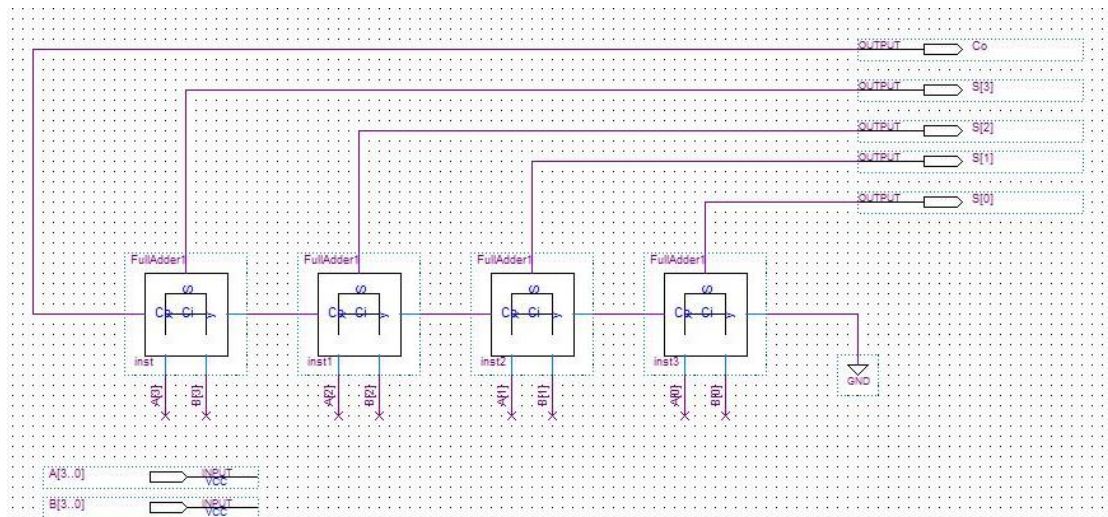
### 1. Full Adder for 1-bit



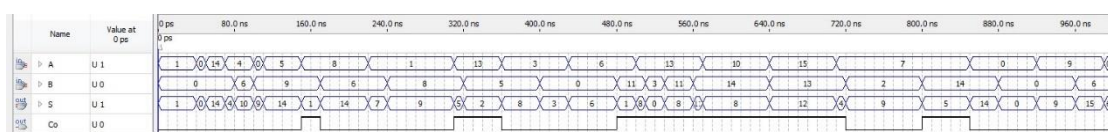
### 2. Full Adder for 1-bit in bsf document



### 3. Full Adder for 4-bit

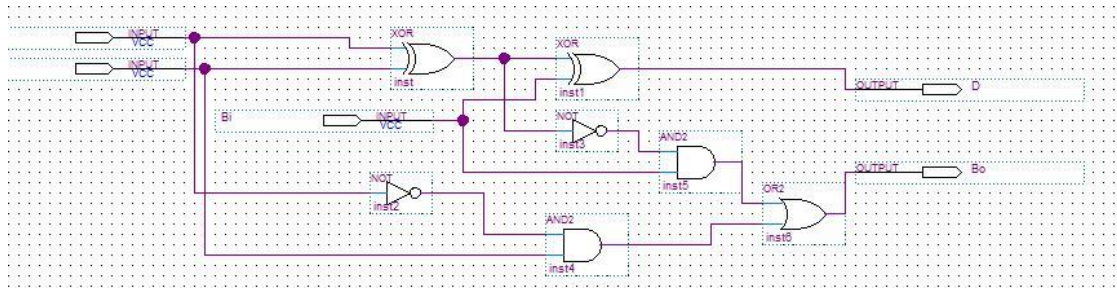


### 4. Waveform of A Full Adder for 4-bit

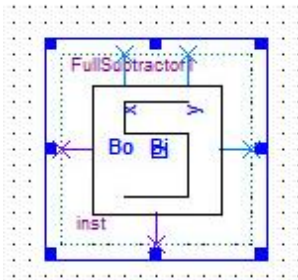


## II. 4-bit Subtractor :

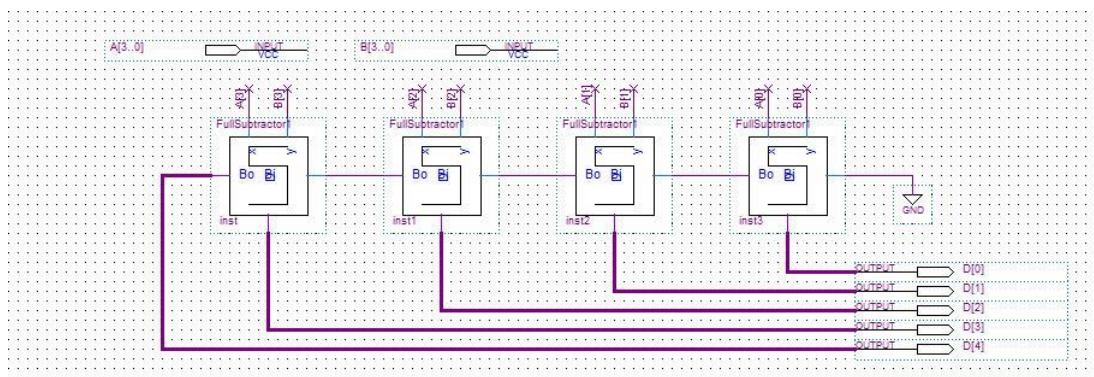
### 1. Full Subtractor for 1-bit



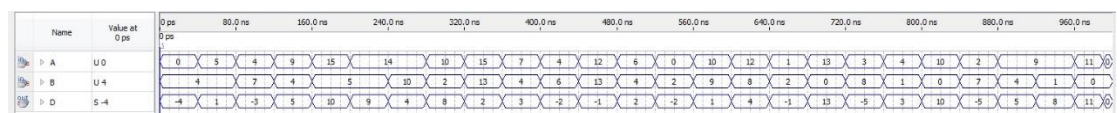
### 2. Full Subtractor for 1-bit in bsf document



### 3. Full Subtractor for 4-bit

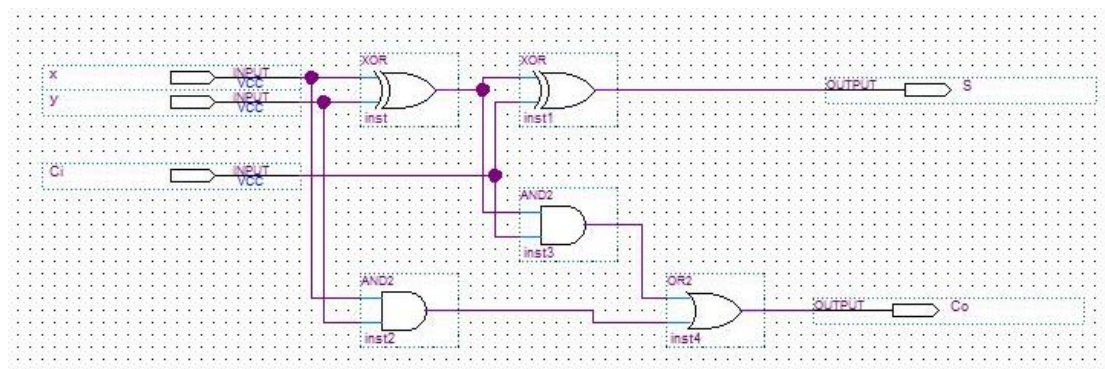


### 4. Waveform of A Full Adder for 4-bit

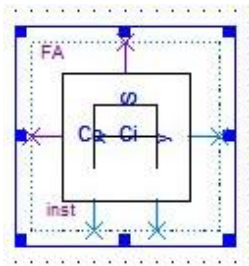


## III. 4-bit Multiplier :

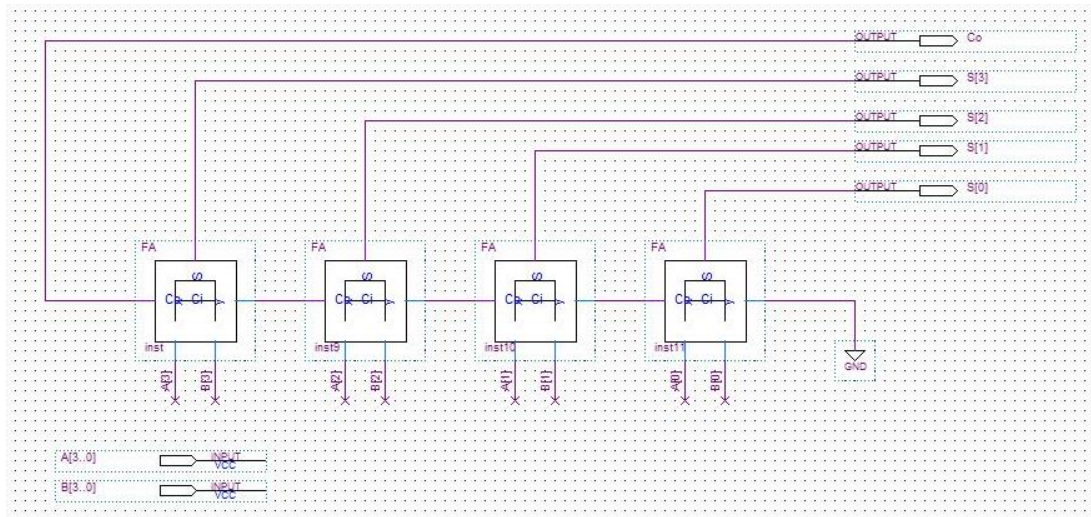
### 1. Full Adder for 1-bit (same as I did)



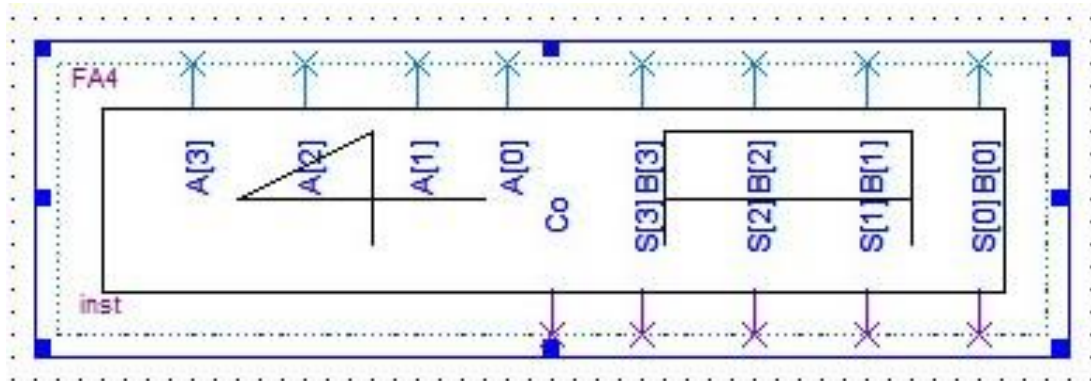
## 2. Full Adder for 1-bit in bsf document



## 3. Full Adder for 4-bit (same as I did)

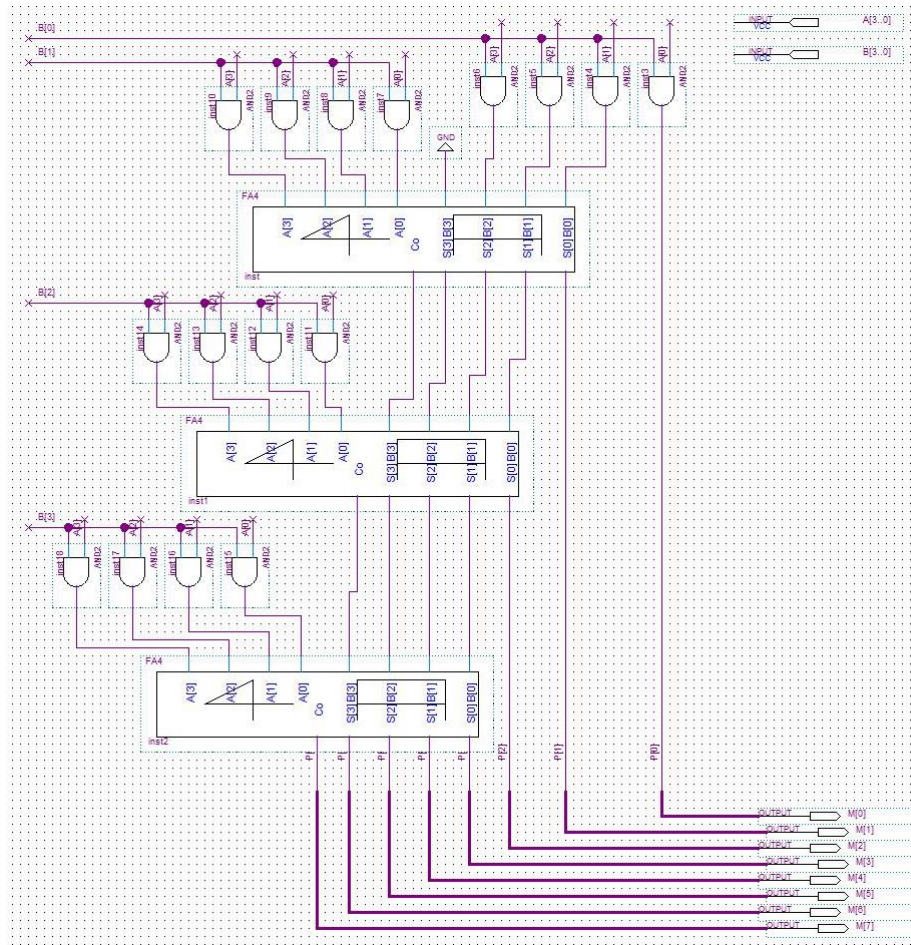


## 4. Full Adder for 4-bit in bsf document



## 5. Multiplier for





## 6. Waveform of a Multiplier for 4-bit

