

CS4230 Programming HW3 Part1

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2.a) 16 x 16

Specify TB-size-x,TB-size-y: 16 16

Trial 0: GFLOPS: 469.24

Trial 1: GFLOPS: 493.90

Trial 2: GFLOPS: 493.11

Trial 3: GFLOPS: 493.55

Trial 4: GFLOPS: 493.10

2.b)

Specify TB-size-x,TB-size-y: 1 1

Trial 0: GFLOPS: 47.64

Trial 1: GFLOPS: 47.25

Trial 2: GFLOPS: 46.69

Trial 3: GFLOPS: 46.34

Trial 4: GFLOPS: 45.91

Specify TB-size-x,TB-size-y: 2 2

Trial 0: GFLOPS: 150.54

Trial 1: GFLOPS: 153.79

Trial 2: GFLOPS: 153.49

Trial 3: GFLOPS: 153.16

Trial 4: GFLOPS: 152.94

Specify TB-size-x,TB-size-y: 4 4

Trial 0: GFLOPS: 344.38

Trial 1: GFLOPS: 344.39

Trial 2: GFLOPS: 344.36

Trial 3: GFLOPS: 344.32

Trial 4: GFLOPS: 344.24

Specify TB-size-x,TB-size-y: 8 8

Trial 0: GFLOPS: 480.60

Trial 1: GFLOPS: 489.79

Trial 2: GFLOPS: 489.35

Trial 3: GFLOPS: 490.02

Trial 4: GFLOPS: 488.09

Specify TB-size-x,TB-size-y: 16 16

Trial 0: GFLOPS: 451.10

Trial 1: GFLOPS: 488.83

Trial 2: GFLOPS: 488.99

Trial 3: GFLOPS: 489.03

Trial 4: GFLOPS: 487.91

Specify TB-size-x,TB-size-y: 32 32

Trial 0: GFLOPS: 269.32

Trial 1: GFLOPS: 276.34

Trial 2: GFLOPS: 276.26

Trial 3: GFLOPS: 276.38

Trial 4: GFLOPS: 276.41

Specify TB-size-x,TB-size-y: 64 16

Trial 0: GFLOPS: 125.56

Trial 1: GFLOPS: 126.41

Trial 2: GFLOPS: 126.29

Trial 3: GFLOPS: 125.74

Trial 4: GFLOPS: 126.20

Specify TB-size-x,TB-size-y: 128 8

Trial 0: GFLOPS: 26.13

Trial 1: GFLOPS: 26.14

Trial 2: GFLOPS: 26.18

Trial 3: GFLOPS: 26.17

Trial 4: GFLOPS: 26.15

Specify TB-size-x,TB-size-y: 256 4

Trial 0: GFLOPS: 15.81

Trial 1: GFLOPS: 15.81

Trial 2: GFLOPS: 15.83

Trial 3: GFLOPS: 15.88

Trial 4: GFLOPS: 15.88

Specify TB-size-x,TB-size-y: 512 2

Trial 0: GFLOPS: 13.70

Trial 1: GFLOPS: 13.72

Trial 2: GFLOPS: 13.74

Trial 3: GFLOPS: 13.74

Trial 4: GFLOPS: 13.75

Specify TB-size-x,TB-size-y: 1024 1

Trial 0: GFLOPS: 12.95

Trial 1: GFLOPS: 12.95

Trial 2: GFLOPS: 12.98

Trial 3: GFLOPS: 12.97

Trial 4: GFLOPS: 12.98

Specify TB-size-x,TB-size-y: 33 32

Cuda error: kernel launch failure: invalid configuration argument.