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NOV ,2001.

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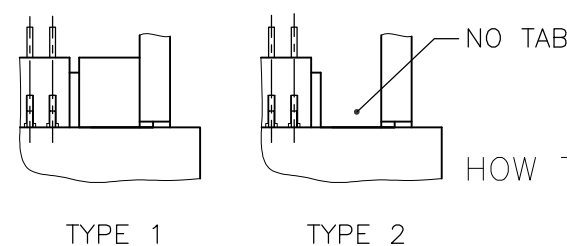
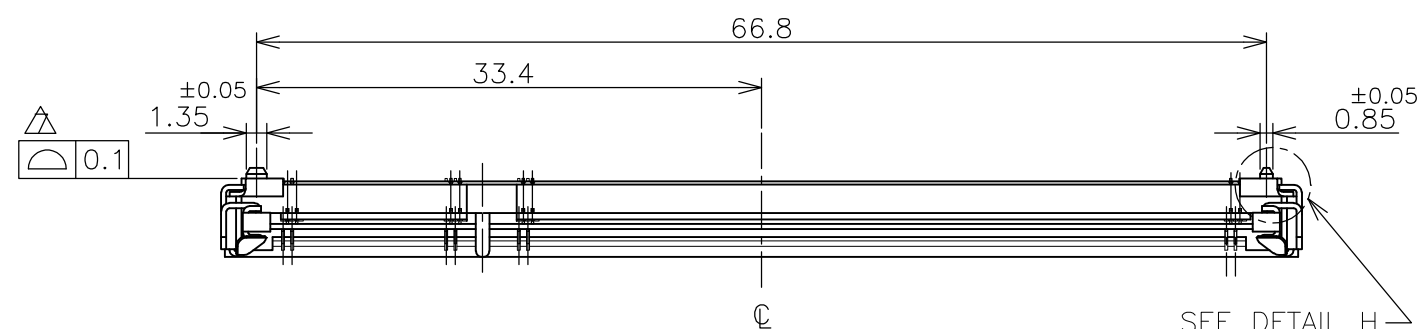
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LOC  
J

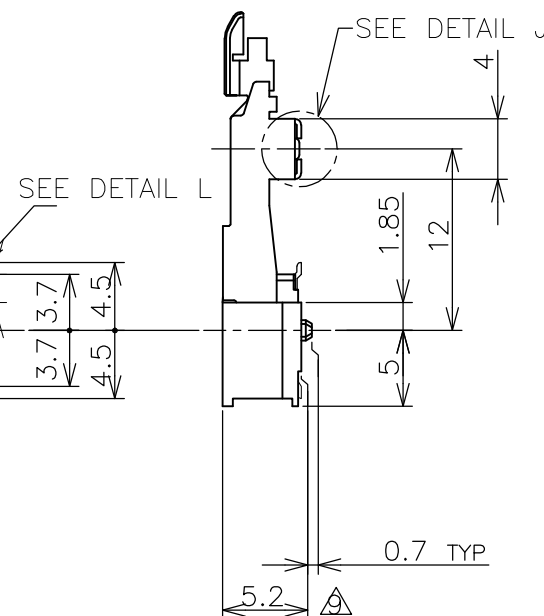
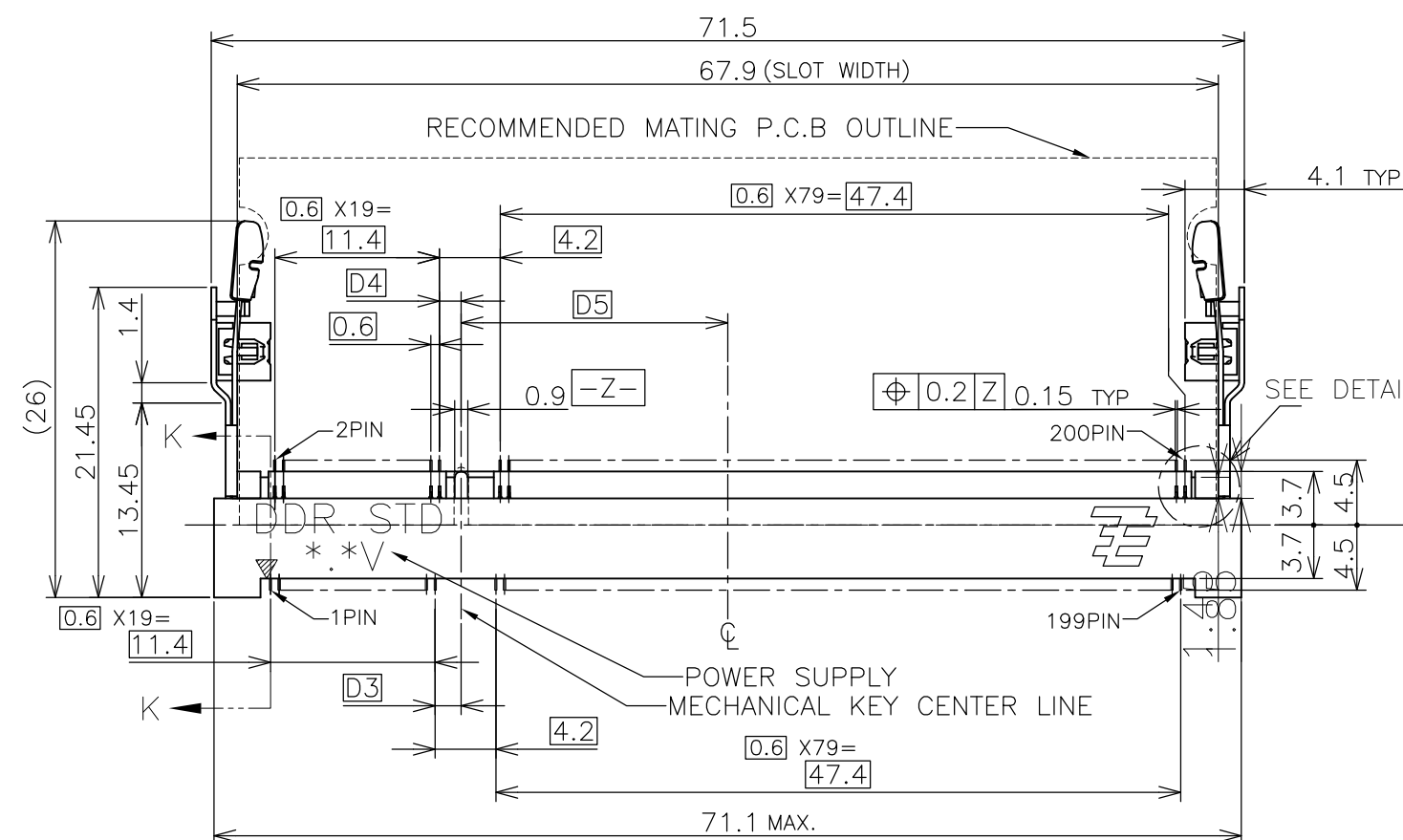
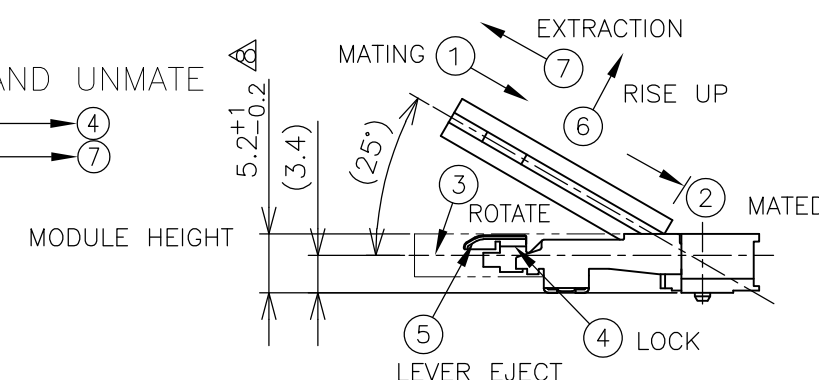
DIST

REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	J2	REVISED PER ECO-11-005033	24MAR11	RK	HMR



HOW TO MATE AND UNMATE

MATE: ① → ④  
UNMATE: ⑤ → ⑦DETAIL L  
(SCALE 5:1)

(APPLIED TO SHEET 1-2)

1. MATERIAL ; HOUSING: HIGH TEMPERATURE THERMO PLASTIC UL94V-0  
CONTACT: COPPER ARROY  
LOCK LEVER: STAINLESS STEEL  
SOLDER PEG: COPPER ARROY

△ FINISH ; CONTACT AREA: GOLD FLASH ON 0.0013MIN ALL OVER NICKEL.  
SOLDERING AREA: GOLD FLASH PLT.  
SOLDER PEG: TIN PLATING.

△ MECHANICAL KEY POSITION OF CONNECTOR SEE TABLE.

△ TOLERANCES NON-CUMULATIVE.

△ NOT TO SCALE.

△ INSIDE MUST BE RESIST COAT EXCEPT SOLDER PATTERN.

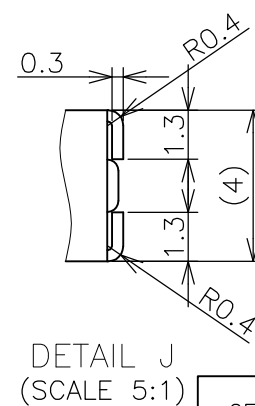
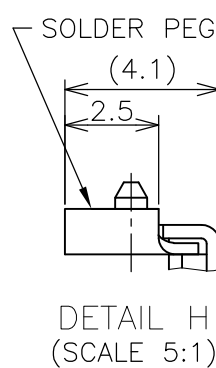
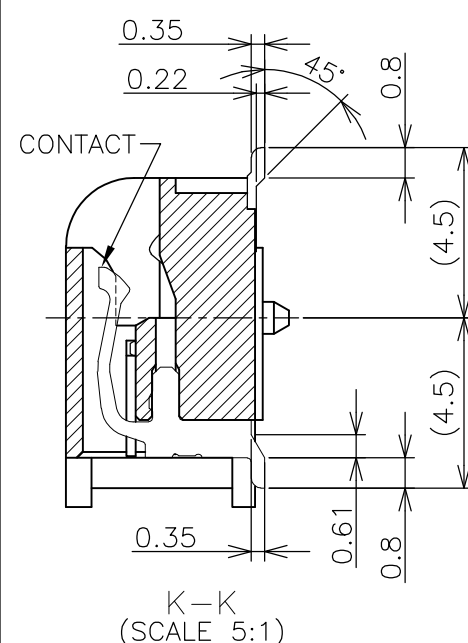
△ COPLANARITY : 0.1 MAX.

△ THE FLOATING VALUE BY SOLDERING IS NOT INCLUDED.

△ NOT MATING CONDITION.

△ FINISH ; CONTACT AREA: 0.00076 GOLD ON 0.0013MIN ALL OVER NICKEL.  
SOLDERING AREA: 0.00076 GOLD PLT.  
SOLDER PEG: TIN PLATING.

11 IF THE MODULE DOESN'T INSERT AND LOCK IN THE LATCH SMOOTHLY,  
OPEN WIDE BOTH LATCHES BY MANUAL. AND THE MODULE MUST BE  
INSERTED AND LOCKED IN THE LATCH.



GENERAL TOLERANCE  
10 ≥ : ±0.2  
30 ≥ > 10 : ±0.25  
100 ≥ > 30 : ±0.3  
ANGLES : ±3°

TYPE 1	△	AVAILABLE	—H—	2.5V (DDR1)	18.45	1.5	1.8	200	1473005-5
TYPE 2	△	AVAILABLE	BOSSLESS	1.8V (DDR2)	17.55	2.4	2.7	200	1-1473005-4
TYPE 2	△	AVAILABLE	—H—	1.8V (DDR2)	17.55	2.4	2.7	200	1473005-4
TYPE 1	△	AVAILABLE	HOUSING COLOR:GRAY BOSSLESS	2.5V (DDR1)	18.45	1.5	1.8	200	6-1473005-1
TYPE 1	△	AVAILABLE	BOSSLESS	2.5V (DDR1)	18.45	1.5	1.8	200	1-1473005-1
TYPE 1	△	AVAILABLE	—H—	2.5V (DDR1)	18.45	1.5	1.8	200	1473005-1
TAB TYPE	FINISH	TOOLING STATUS	REMARK	POWER SUPPLY (TYPE)	D5	D4	D3	POS	PART NO.

THIS DRAWING IS A CONTROLLED DOCUMENT.

DWN B.WONG 15JUL10

CHK C.WANG 15JUL10

APVD S.YAO 15JUL10

PRODUCT SPEC

108-5701

APPLICATION SPEC

WEIGHT 4.7g

CUSTOMER DRAWING

STE TE Connectivity

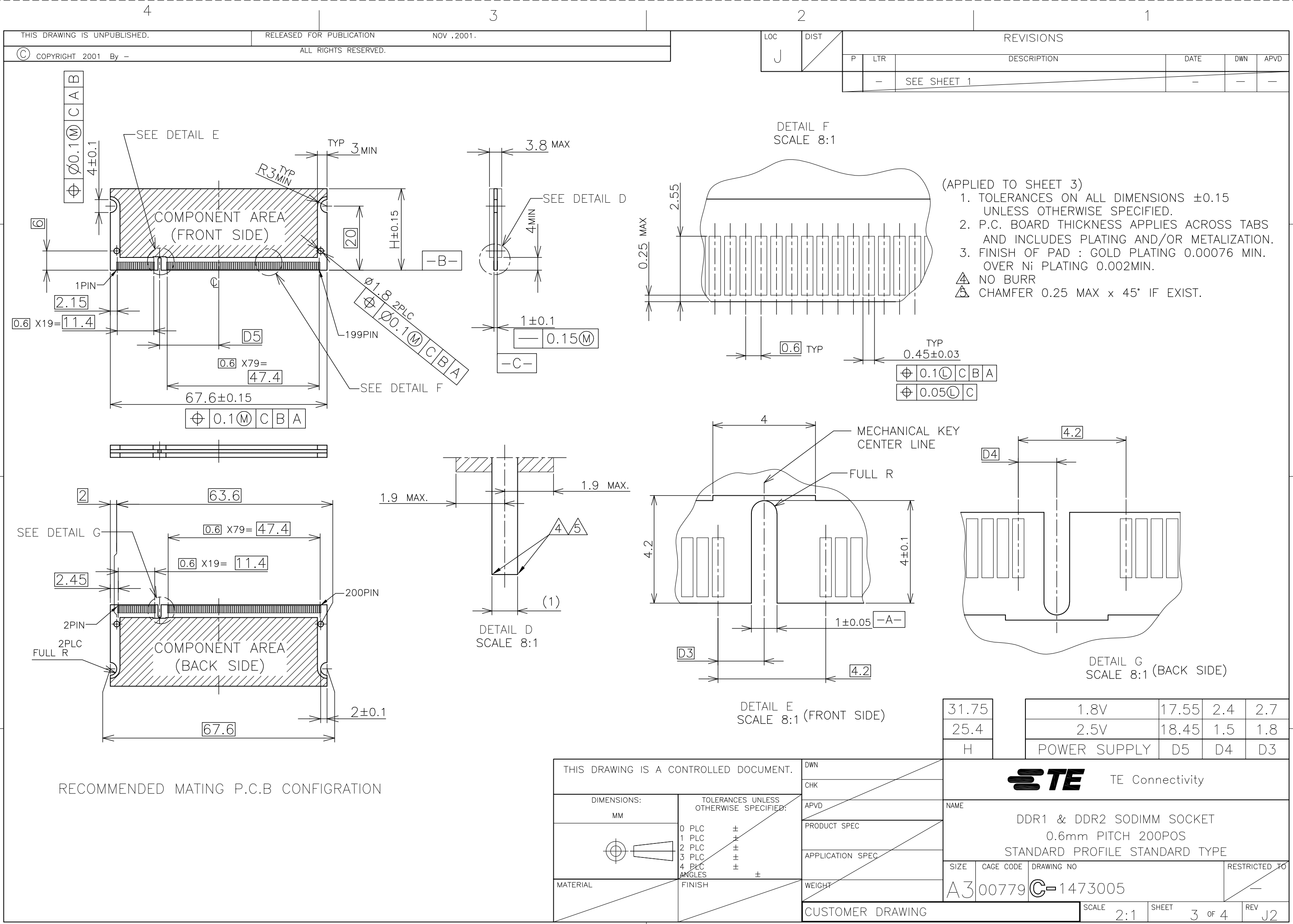
DDR1 & DDR2 SODIMM SOCKET  
0.6mm PITCH 200POS  
STANDARD PROFILE STANDARD TYPE

SIZE CAGE CODE DRAWING NO RESTRICTED TO

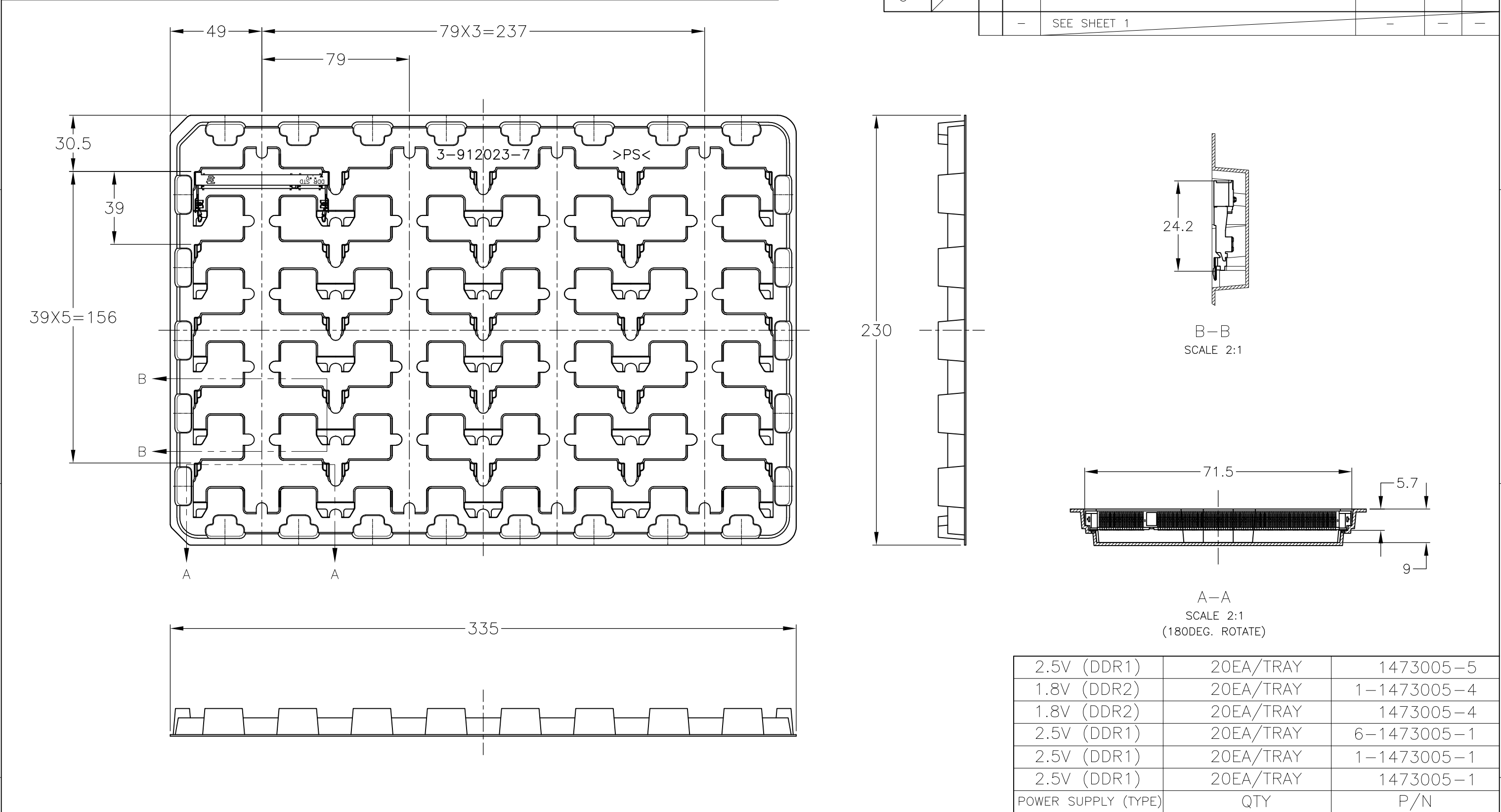
A300779 C-1473005

SCALE 2:1 SHEET 1 OF 4 REV J2


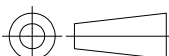




LOC		DIST		REVISIONS					
P	LTR	DESCRIPTION			DATE	DWN	APVD		
	-	SEE SHEET 1			-	-	-		



- (APPLIED TO SHEET 4)
1. THE DIRECTION OF EACH CONNECTOR ON A TRAY IS REFERRED TO THIS DRAWING.
  2. GENERAL TOLERANCE:  $\pm 0.5$

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN		<div> TE Connectivity</div>					
		CHK							
DIMENSIONS: MM		TOLERANCES UNLESS OTHERWISE SPECIFIED:		NAME					
		0 PLC ±		APVD					
		1 PLC ±		PRODUCT SPEC					
		2 PLC ±		APPLICATION SPEC					
		3 PLC ±		SIZE					
4 PLC ±		±		CAGE CODE		DRAWING NO		RESTRICTED TO	
MATERIAL		FINISH		WEIGHT		A300779		C-1473005	
CUSTOMER DRAWING				SCALE		1:2		SHEET	
						4 OF 4		REV	
								J2	

# Mouser Electronics

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[1473005-4](#)