

# HyperRam PMOD

## General Description

HyperRAM PMOD Module with 8 or 16 Megabytes of storage, using a PMOD 3.3v interface.

## Key features

- Input voltage 3.3v.
- Uses 3.3v HyperRam chips.
- 8bit wide DDR HyperBus.

## Applications

- FPGA memory prototype

## Pinout

### J1 Connector

Pin	Pin name	Description
1	NC	Not connected
2	CS	Chip select. HyperRam chip select.
3	CK	Clock. HyperRam clock.
4	~CK	Inverted clock. HyperRam inverted clock used by 1.8v versions.
5	GND	Ground
6	3V3	3.3V power
7	NC	Not connected
8	NC	Not connected
9	~RESET	Reset. HyperRam reset (Active low)
10	RWDS	HyperRam read/write signal
11	GND	Ground
12	3V3	3.3V power

### J2 Connector

Pin	Pin name	Description
1	DQ0	Data bus bit[0]. Bi-directional hyperBus.
2	DQ1	Data bus bit[1]. Bi-directional hyperBus.
3	DQ2	Data bus bit[2]. Bi-directional hyperBus.
4	DQ3	Data bus bit[3]. Bi-directional hyperBus.

5	GND	Ground
6	3V3	3.3V power
7	DQ7	Data bus bit[7]. Bi-directional hyperBus.
8	DQ6	Data bus bit[6]. Bi-directional hyperBus.
9	DQ5	Data bus bit[5]. Bi-directional hyperBus.
10	DQ4	Data bus bit[4]. Bi-directional hyperBus.
11	GND	Ground
12	3V3	3.3V power

## Part Numbers

Mfr#	Part Reference
HRPM-8M-3V01	IS66WVH8M8BLL-100B1LI
HRPM-16M-3V01	IS66WVH16M8DBLL-100B1LI

## Links

Github: <https://github.com/picolemon/hyperram>

Product page: <https://picolemon.com/board/HRPM-3V01>

## Change history

15-02-23: Initial version.

21-03-23: Description & link changes.

## Contact & support

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