



## Advanced Chip Design, Practical Examples in Verilog

---

By MR Kishore K Mishra

Createspace, United States, 2013. Paperback. Book Condition: New. 252 x 178 mm. Language: English . Brand New Book \*\*\*\*\* Print on Demand \*\*\*\*\*.Designing a complex ASIC/SoC is similar to learning a new language to start with and ultimately creating a masterpiece using experience, imagination, and creativity. Digital design starts with RTL such as Verilog or VHDL, but it is only the beginning. A complete designer needs to have a good understanding of the Verilog language, digital design techniques, system architecture, IO protocols, and hardware-software interaction. Some of it will come from experience, and some will come with concerted effort. Graduating from college and entering into the world of digital system design becomes an overwhelming task, as not all the information is readily available. In this book, we have made an effort to explain the concepts in a simple way with real-world examples in Verilog. The book is intended for digital and system design engineers with emphasis on design and system architecture. The book is broadly divided into two sections - chapters 1 through 10, focusing on the digital design aspects and chapters 11 through 20, focusing on the system aspects of chip design. This book can be used by students...



**READ ONLINE**  
[ 1.57 MB ]

### Reviews

*Extensive guide for ebook lovers. It generally does not cost excessive. Your way of life span will likely be convert the instant you complete looking at this ebook.*

-- **Rocky Dach**

*Certainly, this is the very best work by any author. It is amongst the most remarkable publication i have got study. I am just happy to inform you that this is actually the greatest pdf i have got study inside my individual daily life and can be he very best publication for at any time.*

-- **Gilbert Rippin**