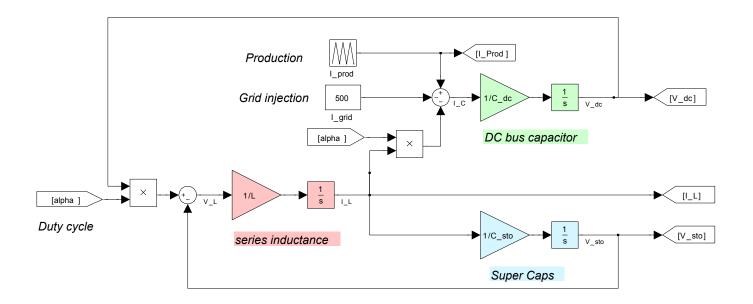
## Control of the DCDC converter of the supercaps to stabilize the DC bus voltage

Pierre Haessig - september 2013



## C\_dc = 50e-3 % [F] C\_sto = 20 % [F] L = 3e-3 % [H] % Initial conidition V\_dc0 = 1300 % [V] V\_sto0 = 1000 % [V] % Set point V\_dc\_set = 1300 % [V]

%% Control parameters %%
%Current loop:
K\_i=0.00406
T\_i=0.274
% Voltage loop:
K\_v=10.2
T\_v=0.003

## Cascaded Control of the duty cycle of the DCDC converter

