

Sine
Modulator



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graph TD; A[Sine Modulator] --> B[Sine Carrier]; B --> C[*]; D[Amplitude] --> C; C --> E[Output]
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The diagram illustrates the process of sine modulation. It begins with a 'Sine Modulator' block, which outputs a 'Sine Carrier' signal. This carrier signal then passes through a multiplication operation, represented by an asterisk (*). The 'Amplitude' of the signal is applied to this operation, as indicated by a horizontal arrow pointing to the asterisk. The final result of the multiplication is the 'Output' signal.

Sine
Carrier

*

Amplitude

Output