#### A Study Of Uniformity In Boolean Circuits

A Project Report

submitted by

#### RAMNATH J

in partial fulfilment of the requirements for the award of the degree of

#### MASTER OF TECHNOLOGY

under the guidance of **Dr. Ravindran Balaraman** 



DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING INDIAN INSTITUTE OF TECHNOLOGY, MADRAS.

May 2013

THESIS CERTIFICATE

This is to certify that the thesis entitled A Study Of Uniformity In Boolean Circuits, submitted by

Ramnath J, to the Indian Institute of Technology, Madras, for the award of the degree of Master of

**Technology**, is a bona fide record of the research work carried out by him under my supervision. The

contents of this thesis, in full or in parts, have not been submitted to any other Institute or University for

the award of any degree or diploma.

Jayalal Sarma M.N

Research Guide **Assistant Professor** Dept. of Computer Science and Engineering IIT-Madras, 600 036

Place: Chennai

Date:

# **ACKNOWLEDGEMENTS**

# **ABSTRACT**

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# **CHAPTER 1**

Introduction

# **CHAPTER 2**

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