

Zhengyu Hua

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Education

Zhejiang University, Computer Science

Hangzhou, China

2024 – 2028

- Cross-disciplinary studies in CS and EE
- Coursework: Digital Signal Processing, Analog Circuit Design, Machine Learning, Computer Architecture, etc.

Experience

Encode lab, Westlake University visiting student

Hangzhou, China

2024 – present

Embedded systems and edge AI deployment.

2 years

- Deployed deep learning models for UAV detection on NVIDIA Jetson using TensorRT and MNN framework.
- Built Docker-based reproducible development environment.

Skills

Programming Languages

Tools & Frameworks

Hardware & Embedded

EE Fundamentals

Projects

UAV Model Deployment on NVIDIA Jetson

2024 – present

Deployed deep learning models for UAV detection on NVIDIA Jetson using TensorRT and MNN framework. Achieved real-time inference with optimized performance in Docker containers.

- TensorRT INT8 quantization
- MNN framework performance optimization
- Docker environment for reproducible deployment

Multi-stage BJT Operational Amplifier Design

2023 – 2024

Designed and simulated a multi-stage BJT op-amp with targeted gain, bandwidth, and stability specifications using SPICE simulation.

- Frequency response analysis
- Stability margin optimization

Languages

Chinese

Native speaker

English

Fluent

Interests

Embedded Systems & Edge AI

Circuit Design & Signal Processing