

# Zhengyu Hua

✉ yuhuaazheng@zju.edu.cn    📍 Zhejiang Hangzhou    🌐 zhengyuhua

## Education

**Zhejiang University**, Computer Science Hangzhou, China  
• Cross-disciplinary studies in CS and EE 2024 – 2028  
• Coursework: Digital Signal Processing, Analog Circuit Design, Machine Learning, Computer Architecture, etc.

## Experience

**Encode lab**, Westlake University visiting student Hangzhou, China  
Embedded systems and edge AI deployment. 2024 – present  
• Deployed deep learning models for UAV detection on NVIDIA Jetson using TensorRT and MNN framework. 2 years  
• Built Docker-based reproducible development environment.

## Skills

**Programming Languages**

**Tools & Frameworks**

**Hardware & Embedded**

**EE Fundamentals**

## Projects

**UAV Model Deployment on NVIDIA Jetson** 2024 – present  
Deployed deep learning models for UAV detection on NVIDIA Jetson using TensorRT and MNN framework. Achieved real-time inference with optimized performance in Docker containers.  
• TensorRT INT8 quantization  
• MNN framework performance optimization  
• Docker environment for reproducible deployment

**Multi-stage BJT Operational Amplifier Design** 2023 – 2024  
Designed and simulated a multi-stage BJT op-amp with targeted gain, bandwidth, and stability specifications using SPICE simulation.  
• Frequency response analysis  
• Stability margin optimization

## Languages

**Chinese**  
Native speaker

**English**  
Fluent

## Interests

**Embedded Systems & Edge AI**

**Circuit Design & Signal Processing**