Used in dificul communication system to improve pentormance.

in date - needed for diffet! Your synchron/zazion, between ob and is even when not Will look at ones to feneruse many transitions present

Manchemer Encoding:

Fig. 4.18 Manchester encoding

Logic 0 Logic 1

Time

Efficiency = 50%

a, M. E. Jevean can be

dx fascer as date seven.

or inductively coupled for induction

PRODUCES and RFID Jechnolys.

Cransitions so one can select on s cont with frequent Add n-m extra bit to each daza big

8584

Table 4.7 4B5B conversion table

Number 0	Binary 0000	4B5B
The state of the s	0001	01001
	0010	10100
4	0100	01010
5	0101	01
	0110	01
A	0111	01
8	1000	10
	1001	10011
10	1010	10
	1011	10
12	1100	11
13	1101	11(
4	11110	11
5		П

100 mbp Fase Esherner

and 80% exticiony.

Some wed in majness recording

~

-> chy not 86/96? Wenos of 86/166 + 80% obsident (m. E. 1 50% obsidion) and 36/46 code (and risse) has hise 5 sion -) Used in Gijabir Exherner, IntiniBent Fibe Chancel + Produces PC believe, bounded dispensey Apatenced by IAM (expiral now) and sufficienc Transicions for clock recovery 8 b 196 problemass found implementation of

2

required. A chellonge. Mean with and 25% overhead 12.5 Gbp lies Using 8 b/10 b 11 0 0 10 6bp Etherner

So inscend map 64 sio -> 66% sio (3-125% overhow)

- For 64 bics of doce pretix is OI For 8 lie Type Self+ 56 bie conen/ messye Prela is 10.
- (4) Evenices six transition any 66 sit Prets 00 and 11 rec wer product oran

Mean time to FALSE PACKET ACCEPTANCE (M77750A) 65 and ar 65 tenes occur one ever 1900 years ~ I billion year for 106 pg. Esterna, 64 pro of deer "scrambled" in hendwore 648/668 addrew performance in a statistice way, not deceministic. 15 Though a linear Reedback register. az 10 6/2

14

-0 ch. cod: 648/678 Used in 10 6 kg, Et herner Some Fider Chamel veropay 100 6\$ps 57 hemer 10 6 bg Enterne: Presse Opesied Not work 1288/1316 128 / 130 \$ he cerein despir jues C/0 6- E Paul

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