# 物聯網與微處理機系統設計 Internet of Things and Microprocessor System Design

Lecture 05 - I2C, SPI, Accelerometer

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YZU CSE



# Outline

- IMU
- SPI
- **1**2C
- Lab



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- IMU
- SPI
- **1**2C
- Lab



### IMU

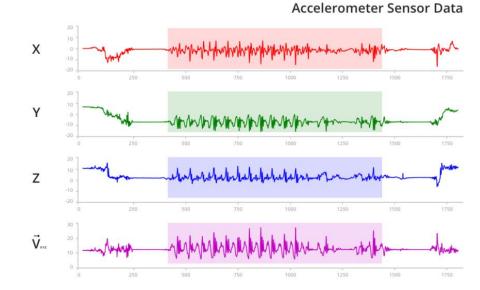
- Inertial Measurement Unit (IMU)
  - 3-axis accelerometer
  - 3-axis gyroscope
  - 3-axis magnetometer



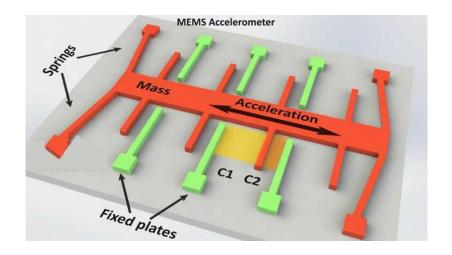


### Accelerometer

- Accelerations in 3-axis
  - Including gravity



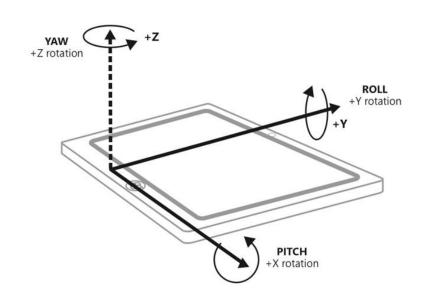
Measure acceleration by measuring change in capacitance.



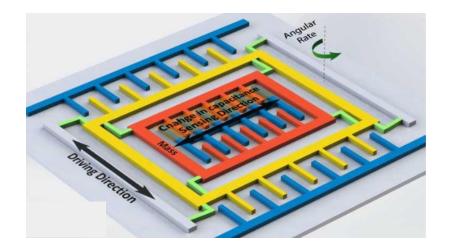


### Gyroscope

Measure angular velocity



 External angular rate will be applied a flexible part of the mass would move and make the perpendicular displacement.





### Applications (1/5)

- How Do Smartphones Know Which Is The Right Side Up?
  - https://www.scienceabc.com/innovation/smartphones-change-orientation-horizontallandscape-gravity-sensor-accelerometer.html





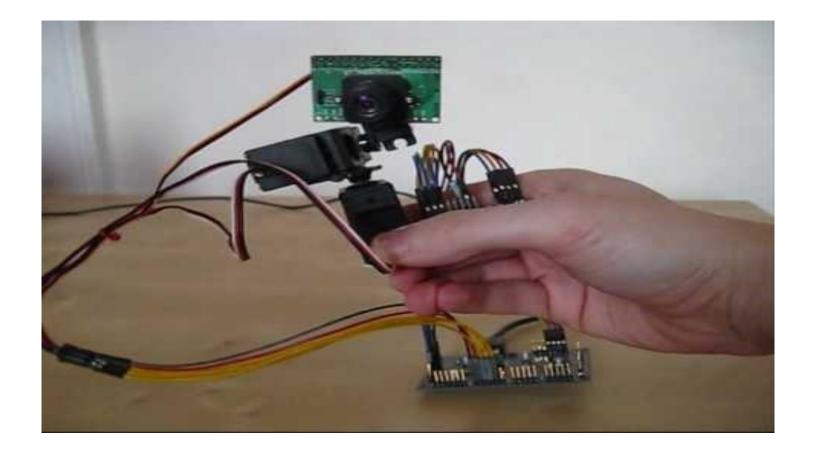


https://youtu.be/KZVgKu6v808



## Applications (2/5)

- Camera control / stabilization
  - https://youtu.be/7GVXqNLLH7Q





## Applications (3/5)

- Gesture recognition
  - https://youtu.be/EvaaAxwY0nc?t=323
- Magic wand
  - https://youtu.be/gw4NCvRSzGo?t=96







### Applications (4/5)

- Motion capture system
  - https://youtu.be/KqKa2Gc7lh8?t=36



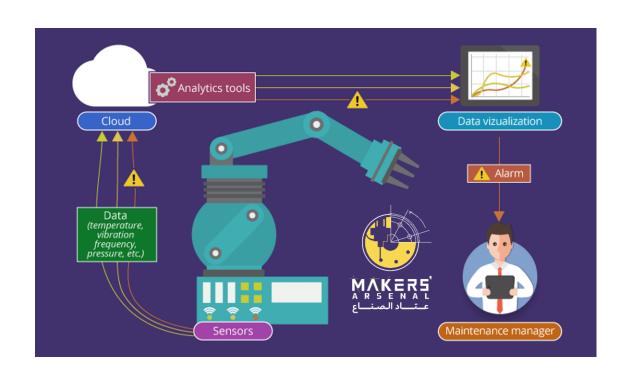
- Nintendo Joy-Con
  - https://en.wikipedia.org/wiki/Joy-Con
  - Each Joy-Con contains an accelerometer and gyroscope, which can be used for motion tracking.

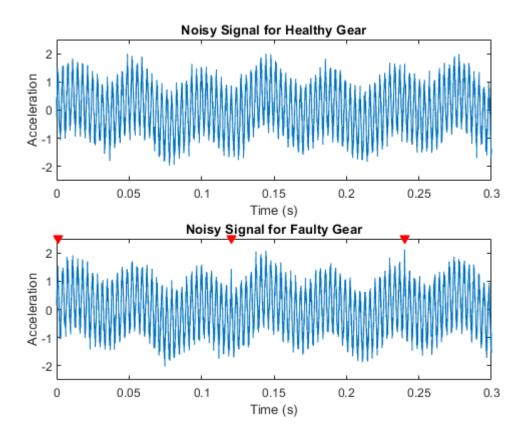




## Application (5/5)

Industry 4.0



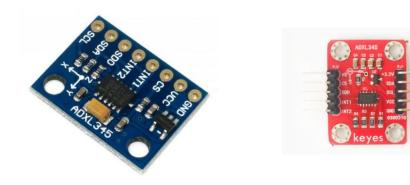




# ADXL345 (1/2)

- A small, thin, ultralow power, 3-axis accelerometer with high resolution (13-bit) measurement at up to  $\pm 16$  g.
- SPI (3- and 4-wire) and I<sup>2</sup>C digital interfaces
- https://www.analog.com/media/en/technical-documentation/data-sheets/ADXL345.pdf

**GND** 





#### ADXL345 **POWER** MANAGEMENT CONTROL $\bigcirc$ INT1 SENSE ELECTRONICS **ADC AND** DIGITAL FILTER INTERRUPT 3-AXIS LOGIC **SENSOR** SDA/SDI/SDIO 32 LEVEL SERIAL I/O SDO/ALT ADDRESS **FIFO**

**FUNCTIONAL BLOCK DIAGRAM** 

SCL/SCLK



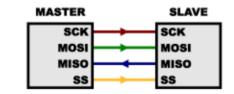
# Outline

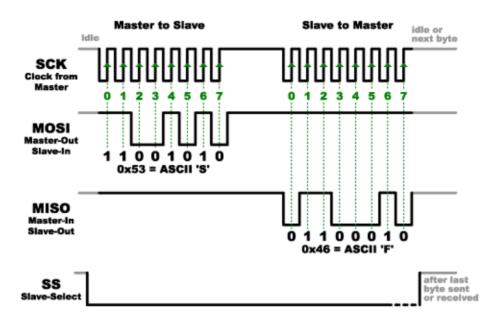
- SPI
- **1**2C
- Lab

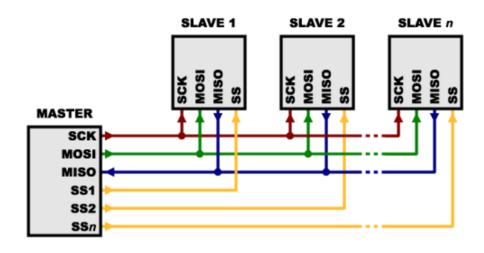


### SPI

- Serial Peripheral Interface (SPI)
  - four-wire serial bus
  - full duplex
  - higher speed

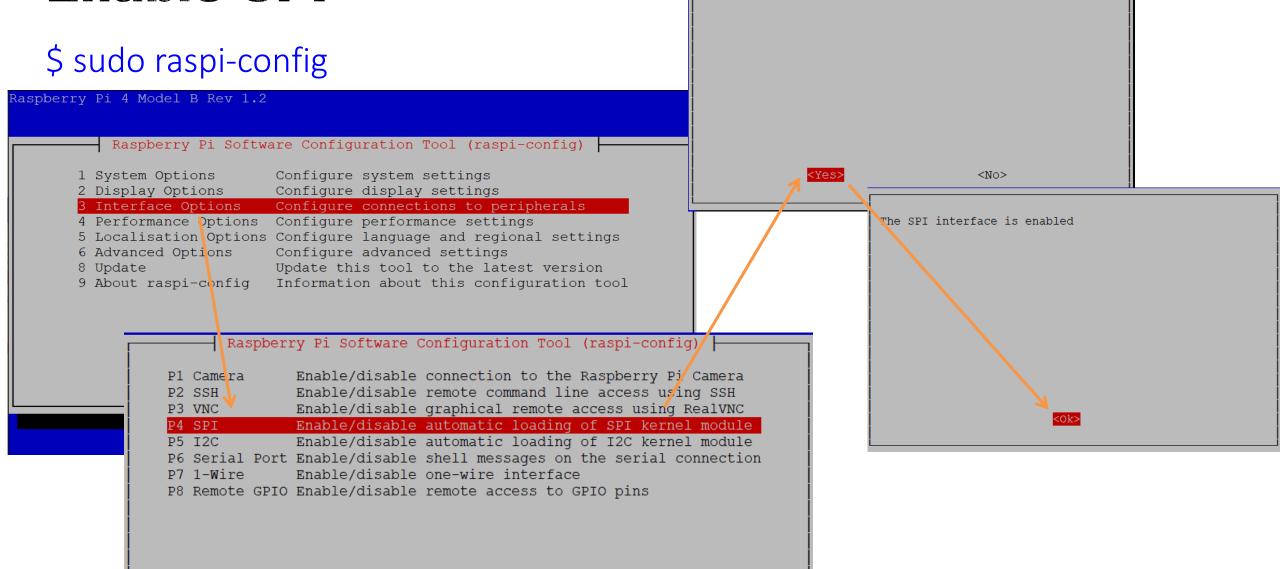








<Select>



<Back>

Would you like the SPI interface to be enabled?



#### SPI Device

SPI Device

\$ Is -I /dev/spi\*

```
pi@rpi4-A00:~/iot/lec05 $ ls -1 /dev/spi*
crw-rw---- 1 root spi 153, 0 Oct 18 01:01 /dev/spidev0.0
crw-rw---- 1 root spi 153, 1 Oct 18 01:01 /dev/spidev0.1
```

| 3v3 Power           | 1  | • •        | 2  | 5v Power            |
|---------------------|----|------------|----|---------------------|
| GPIO 2 (12C1 SDA)   | 3  | •          | 4  | 5v Power            |
| GPIO 3 (12C1 SCL)   | 5  | •          | 6  | Ground              |
| GPIO 4 (GPCLK0)     | 7  | • •        | 8  | GPIO 14 (UART TX)   |
| Ground              | 9  | • •        | 10 | GPIO 15 (UART RX)   |
| GPIO 17 (SPI1 CE1)  | 11 | <b>O</b>   | 12 | GPIO 18 (SPI1 CE0)  |
| GPIO 27             | 13 | •          | 14 | Ground              |
| GPIO 22             | 15 | • •        | 16 | GPIO 23             |
| 3v3 Power           | 17 | •          | 18 | GPIO 24             |
| GPIO 10 (SPI0 MOSI) | 19 | • e        | 20 | Ground              |
| GPIO 9 (SPI0 MISO)  | 21 | <b>9</b> • | 22 | GPIO 25             |
| GPIO 11 (SPI0 SCLK) | 23 | <u> </u>   | 24 | GPIO 8 (SPI0 CE0)   |
| Ground              | 25 | • 🔘        | 26 | GPIO 7 (SPI0 CE1)   |
| GPIO 0 (EEPROM SDA) | 27 | 9 0        | 28 | GPIO 1 (EEPROM SCL) |
| GPIO 5              | 29 | •          | 30 | Ground              |
| GPIO 6              | 31 | • •        | 32 | GPIO 12 (PWM0)      |
| GPIO 13 (PWM1)      | 33 | •          | 34 | Ground              |
| GPIO 19 (SPI1 MISO) | 35 | <u> </u>   | 36 | GPIO 16 (SPI1 CE2)  |
| GPIO 26             | 37 | <b>O</b>   | 38 | GPIO 20 (SPI1 MOSI) |
| Ground              | 39 | • 💿        | 40 | GPIO 21 (SPI1 SCLK) |



#### SPI

For SPI, either 3- or 4-wire configuration is possible, as shown in the connection diagrams in Figure 34 and Figure 35. Clearing the SPI bit (Bit D6) in the DATA\_FORMAT register (Address 0x31) selects 4-wire mode, whereas setting the SPI bit selects 3-wire mode. The maximum SPI clock speed is 5 MHz with 100 pF maximum loading, and the timing scheme follows clock polarity (CPOL) = 1 and clock phase (CPHA) = 1. If power is applied to the ADXL345 before the clock polarity and phase of the host processor are configured, the  $\overline{\text{CS}}$  pin should be brought high before changing the clock polarity and phase. When using 3-wire SPI, it is recommended that the SDO pin be either pulled up to  $V_{\text{DD} \text{I/O}}$  or pulled down to GND via a 10 k $\Omega$  resistor.

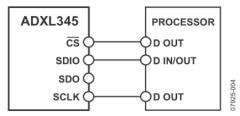


Figure 34. 3-Wire SPI Connection Diagram

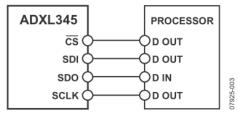


Figure 35. 4-Wire SPI Connection Diagram

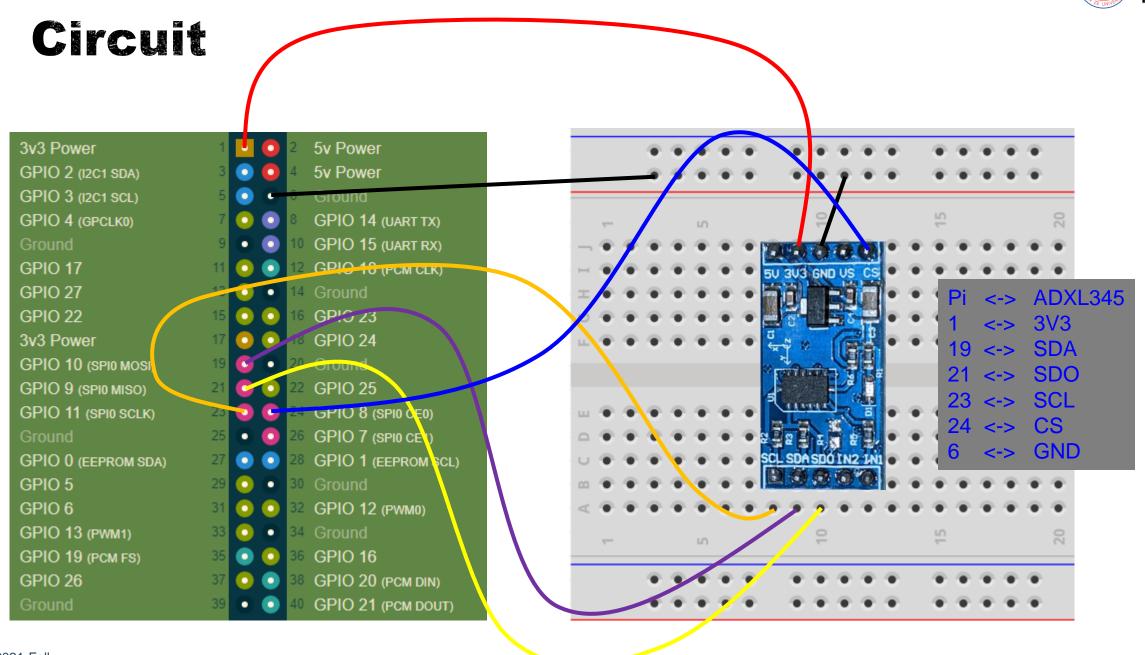
CS is the serial port enable line and is controlled by the SPI master. This line must go low at the start of a transmission and high at the end of a transmission, as shown in Figure 37. SCLK is the serial port clock and is supplied by the SPI master. SCLK should idle high during a period of no transmission. SDI and SDO are the serial data input and output, respectively. Data is updated on the falling edge of SCLK and should be sampled on the rising edge of SCLK.

To read or write multiple bytes in a single transmission, the multiple-byte bit, located after the R/W bit in the first byte transfer (MB in Figure 37 to Figure 39), must be set. After the register addressing and the first byte of data, each subsequent set of clock pulses (eight clock pulses) causes the ADXL345 to point to the next register for a read or write. This shifting continues until the clock pulses cease and  $\overline{\text{CS}}$  is deasserted. To perform reads or writes on different, nonsequential registers,  $\overline{\text{CS}}$  must be deasserted between transmissions and the new register must be addressed separately.

The timing diagram for 3-wire SPI reads or writes is shown in Figure 39. The 4-wire equivalents for SPI writes and reads are shown in Figure 37 and Figure 38, respectively. For correct operation of the part, the logic thresholds and timing parameters in Table 9 and Table 10 must be met at all times.

Use of the 3200 Hz and 1600 Hz output data rates is only recommended with SPI communication rates greater than or equal to 2 MHz. The 800 Hz output data rate is recommended only for communication speeds greater than or equal to 400 kHz, and the remaining data rates scale proportionally. For example, the minimum recommended communication speed for a 200 Hz output data rate is 100 kHz. Operation at an output data rate above the recommended maximum may result in undesirable effects on the acceleration data, including missing samples or additional noise.







### adxI345\_spi.py

```
import time
from enum import Enum
import RPi.GPIO as GPIO
import spidev
import struct
spi = spidev.SpiDev()
spi.open(0, 0)
spi.mode = 0b11
spi.max speed hz = 2000000
def writeByte(reg, val):
   spi.xfer2([reg, val])
def writeRegBytes(reg, vals):
    packet = [0] * (len(vals) + 1)
   packet[0] = reg \mid 0x40
   packet[1:(len(vals)+1)] = vals
    spi.xfer2(packet)
def readByte(reg):
    packet = [0] * 2
   packet[0] = reg \mid 0x80
   reply = spi.xfer2(packet)
    return reply[1]
deviceID = readByte(0x00)
print("ID: %x" % deviceID)
# Select power control register, 0x2D(45)
# 0x08(08) Auto Sleep disable
writeByte(0x2D, 0x00)
time.sleep(0.1)
writeByte(0x2D, 0x08)
# Select data format register, 0x31(49)
# 0x08(08) Self test disabled, 4-wire interface
# Full resolution, Range = +/-2g
writeByte(0x31, 0x08)
time.sleep(0.5)
```

```
try:
    while True:
        # Read data back from 0x32(50), 2 bytes
       accel = \{'x' : 0, 'y' : 0, 'z' : 0\}
        # X-Axis LSB, X-Axis MSB
       data0 = readByte(0x32)
       data1 = readByte(0x33)
       # Convert the data to 10-bits
       xAccl = struct.unpack('<h', bytes([data0, data1]))[0]</pre>
        accel['x'] = xAccl / 256
        # Read data back from 0x34(52), 2 bytes
       # Y-Axis LSB, Y-Axis MSB
       data0 = readByte(0x34)
       data1 = readByte(0x35)
       # Convert the data to 10-bits
       yAccl = struct.unpack('<h', bytes([data0, data1]))[0]</pre>
       accel['y'] = yAccl / 256
       # Read data back from 0x36(54), 2 bytes
       # Z-Axis LSB, Z-Axis MSB
       data0 = readByte(0x36)
       data1 = readByte(0x37)
       # Convert the data to 10-bits
       zAccl = struct.unpack('<h', bytes([data0, data1]))[0]</pre>
       accel['z'] = zAccl / 256
        # Output data to screen
       print ("Ax Ay Az: %.3f %.3f %.3f" % (accel['x'], accel['y'], accel['z']))
        time.sleep(0.1)
except KeyboardInterrupt:
   print("Ctrl+C Break")
    spi.close()
```



#### Results

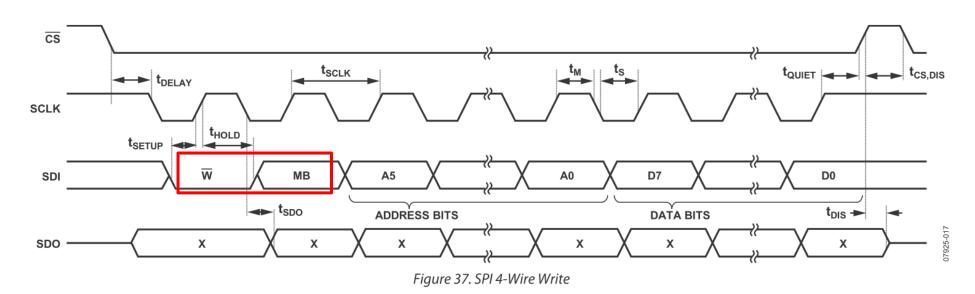
\$ wget https://raw.githubusercontent.com/yachentw/yzucseiot/main/lec05/adxl345\_spi.py

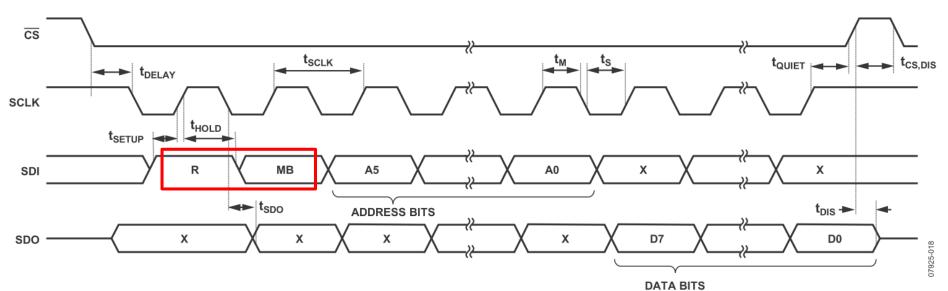
\$ python3 adxl345\_spi.py

```
pi@rpi4-A00:~/iot/lec05 $ python3 adx1345 spi.py
ID: e5
Ax Ay Az: 0.055 0.059 0.969
Ax Ay Az: 0.047 0.066 0.969
Ax Ay Az: 0.055 0.059 0.973
Ax Ay Az: 0.059 0.059 0.973
Ax Ay Az: 0.055 0.059 0.973
Ax Ay Az: 0.051 0.059 0.973
Ax Ay Az: 0.051 0.062 0.969
Ax Ay Az: 0.055 0.062 0.969
Ax Ay Az: 0.055 0.062 0.973
Ax Ay Az: 0.051 0.059 0.973
Ax Ay Az: 0.059 0.055 0.969
Ax Ay Az: 0.055 0.055 0.969
Ax Ay Az: 0.055 0.059 0.973
Ax Ay Az: 0.059 0.059 0.973
Ax Ay Az: 0.055 0.059 0.977
Ax Ay Az: 0.055 0.062 0.973
```



## **Operations**







# Register Map

| Address      |         |                |      |             |   |  |  |  |
|--------------|---------|----------------|------|-------------|---|--|--|--|
| Hex          | Dec     | Name           | Type | Reset Value | Description   |  |  |  |
| 0x00         | 0       | DEVID          | R    | 11100101    | Device ID   |  |  |  |
| 0x01 to 0x1C | 1 to 28 | Reserved       |      |             | Reserved; do not access                                   |  |  |  |
| 0x1D         | 29      | THRESH_TAP     | R/W  | 00000000    | Tap threshold   |  |  |  |
| 0x1E         | 30      | OFSX           | R/W  | 00000000    | X-axis offset   |  |  |  |
| 0x1F         | 31      | OFSY           | R/W  | 00000000    | Y-axis offset   |  |  |  |
| 0x20         | 32      | OFSZ           | R/W  | 00000000    | Z-axis offset   |  |  |  |
| 0x21         | 33      | DUR            | R/W  | 00000000    | Tap duration  |  |  |  |
| 0x22         | 34      | Latent         | R/W  | 00000000    | Tap latency   |  |  |  |
| 0x23         | 35      | Window         | R/W  | 00000000    | Tap window  |  |  |  |
| 0x24         | 36      | THRESH_ACT     | R/W  | 00000000    | Activity threshold  |  |  |  |
| 0x25         | 37      | THRESH_INACT   | R/W  | 00000000    | Inactivity threshold                                      |  |  |  |
| 0x26         | 38      | TIME_INACT     | R/W  | 00000000    | Inactivity time   |  |  |  |
| 0x27         | 39      | ACT_INACT_CTL  | R/W  | 00000000    | Axis enable control for activity and inactivity detection |  |  |  |
| 0x28         | 40      | THRESH_FF      | R/W  | 00000000    | Free-fall threshold                                       |  |  |  |
| 0x29         | 41      | TIME_FF        | R/W  | 00000000    | Free-fall time  |  |  |  |
| 0x2A         | 42      | TAP_AXES       | R/W  | 00000000    | Axis control for single tap/double tap                    |  |  |  |
| 0x2B         | 43      | ACT_TAP_STATUS | R    | 00000000    | Source of single tap/double tap                           |  |  |  |
| 0x2C         | 44      | BW_RATE        | R/W  | 00001010    | Data rate and power mode control                          |  |  |  |
| 0x2D         | 45      | POWER_CTL      | R/W  | 00000000    | Power-saving features control                             |  |  |  |
| 0x2E         | 46      | INT_ENABLE     | R/W  | 00000000    | Interrupt enable control                                  |  |  |  |
| 0x2F         | 47      | INT_MAP        | R/W  | 00000000    | Interrupt mapping control                                 |  |  |  |
| 0x30         | 48      | INT_SOURCE     | R    | 00000010    | Source of interrupts                                      |  |  |  |
| 0x31         | 49      | DATA_FORMAT    | R/W  | 00000000    | Data format control                                       |  |  |  |
| 0x32         | 50      | DATAX0         | R    | 00000000    | X-Axis Data 0   |  |  |  |
| 0x33         | 51      | DATAX1         | R    | 00000000    | X-Axis Data 1   |  |  |  |
| 0x34         | 52      | DATAY0         | R    | 00000000    | Y-Axis Data 0   |  |  |  |
| 0x35         | 53      | DATAY1         | R    | 00000000    | Y-Axis Data 1   |  |  |  |
| 0x36         | 54      | DATAZ0         | R    | 00000000    | Z-Axis Data 0   |  |  |  |
| 0x37         | 55      | DATAZ1         | R    | 00000000    | Z-Axis Data 1   |  |  |  |
| 0x38         | 56      | FIFO_CTL       | R/W  | 00000000    | FIFO control  |  |  |  |
| 0x39         | 57      | FIFO_STATUS    | R    | 00000000    | FIFO status   |  |  |  |

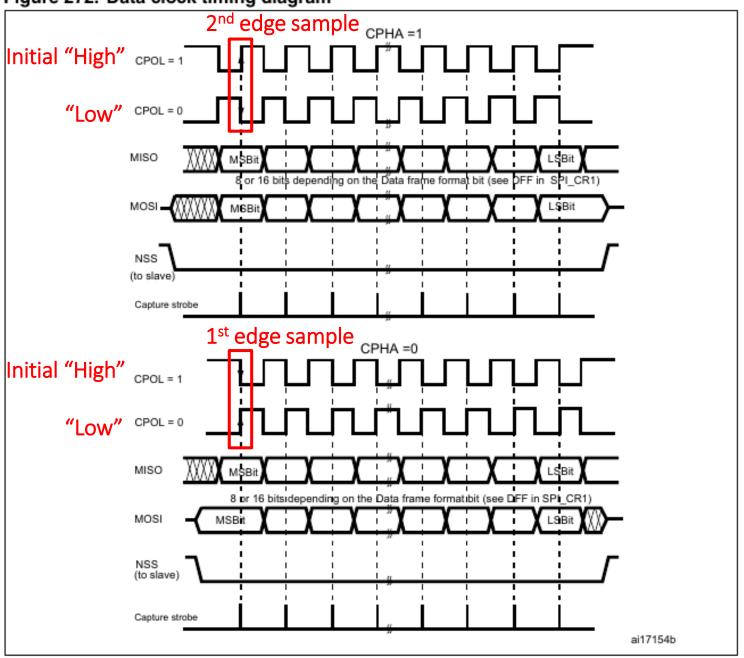


#### **Parameters**

- Datasheet
  - https://www.analog.com/media/en/technical-documentation/data-sheets/ADXL345.pdf
  - The maximum SPI clock speed is 5 MHz
  - Clock polarity (CPOL) = 1 and clock phase (CPHA) = 1









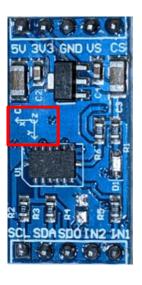
### **Unit Conversion**

| Parameter                             | Test Conditions               | Min  | Typ <sup>1</sup> | Max  | Unit            |
|---------------------------------------|-------------------------------|------|------------------|------|-----------------|
| SENSITIVITY                           | Each axis                     |      |                  |      |                 |
| Sensitivity at Xout, Yout, Zout       | All g-ranges, full resolution |      | 256              | 282  | LSB/g           |
|                                       | $\pm 2 g$ , 10-bit resolution | 230  | 256              | 282  | LSB/g           |
|                                       | $\pm 4 g$ , 10-bit resolution | 115  | 128              | 141  | LSB/g           |
|                                       | ±8 g, 10-bit resolution       | 57   | 64               | 71   | LSB/g           |
|                                       | ±16 g, 10-bit resolution      | 29   | 32               | 35   | LSB/g           |
| Sensitivity Deviation from Ideal      | All g-ranges                  |      | ±1.0             |      | %               |
| Scale Factor at Xout, Yout, Zout      | All g-ranges, full resolution | 3.5  | 3.9              | 4.3  | m <i>g/</i> LSB |
|                                       | $\pm 2 g$ , 10-bit resolution | 3.5  | 3.9              | 4.3  | m <i>g/</i> LSB |
|                                       | ±4 g, 10-bit resolution       | 7.1  | 7.8              | 8.7  | m <i>g/</i> LSB |
|                                       | ±8 g, 10-bit resolution       | 14.1 | 15.6             | 17.5 | m <i>g/</i> LSB |
|                                       | ±16 g, 10-bit resolution      | 28.6 | 31.2             | 34.5 | m <i>g/</i> LSB |
| Sensitivity Change Due to Temperature |                               |      | ±0.01            |      | %/°C            |



#### Observation

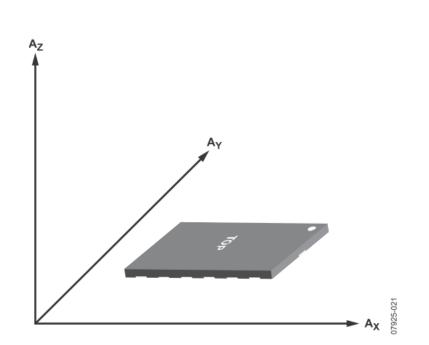
- Observe the changes of values in accelerations.
- Let X, Y, Z axes point to the sky and stop for a while respectively.



Repeat the observation by pointing to the ground instead.



## Gravity



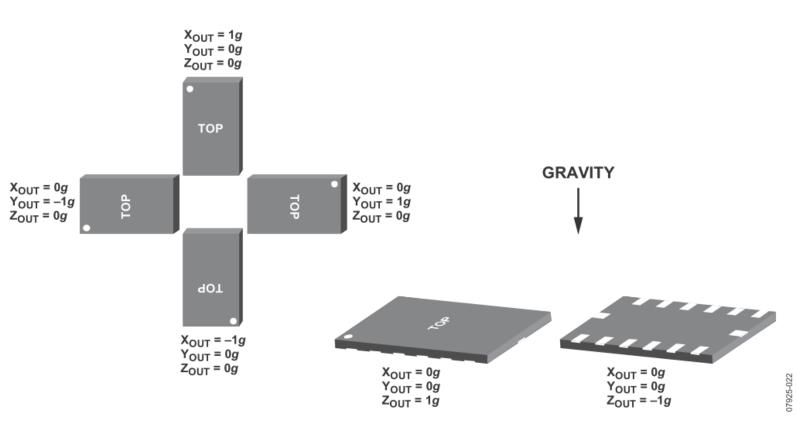


Figure 58. Output Response vs. Orientation to Gravity



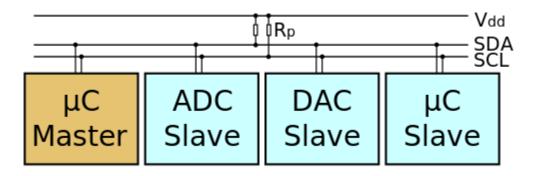
### Outline

- SPI
- **1**2C
- Lab



### I2C-Bus

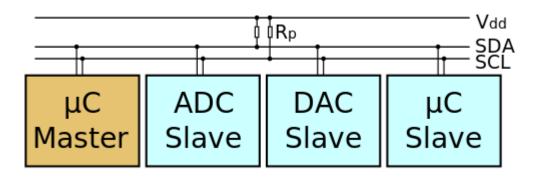
- I<sup>2</sup>C (Inter-Integrated Circuit) Bus
  - Synchronous
  - Multi-master and multi-slave
  - Packet switched
  - Invented in 1982 by Philips Semiconductor (NXP Semiconductors)
  - For lower-speed peripheral ICs to processors and microcontrollers in short-distance, intra-board communication





### I2C-Bus

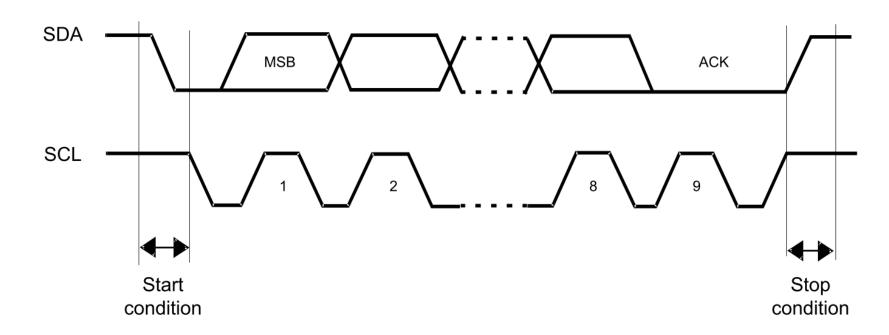
- Features
  - Only two bus lines are required; a serial data line (SDA) and a serial clock line (SCL).
  - Each device connected to the bus is software addressable by a unique address and simple master/slave relationships exist at all times.
  - Standard Speed (up to 100 kHz)
  - Fast Speed (up to 400 kHz)
  - The I2C bus frequency can be increased up to 1 MHz.





### **I**<sup>2</sup>C-Bus

START, STOP conditions

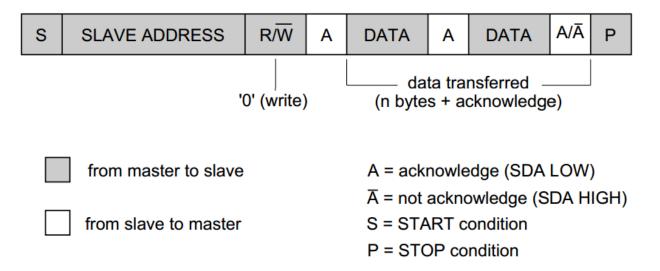


Start by a master.

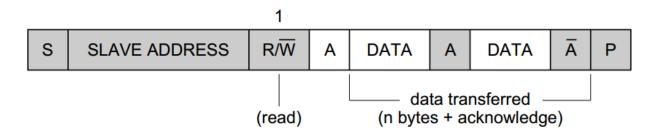


### I2C-Bus

Master transmits data to a slave

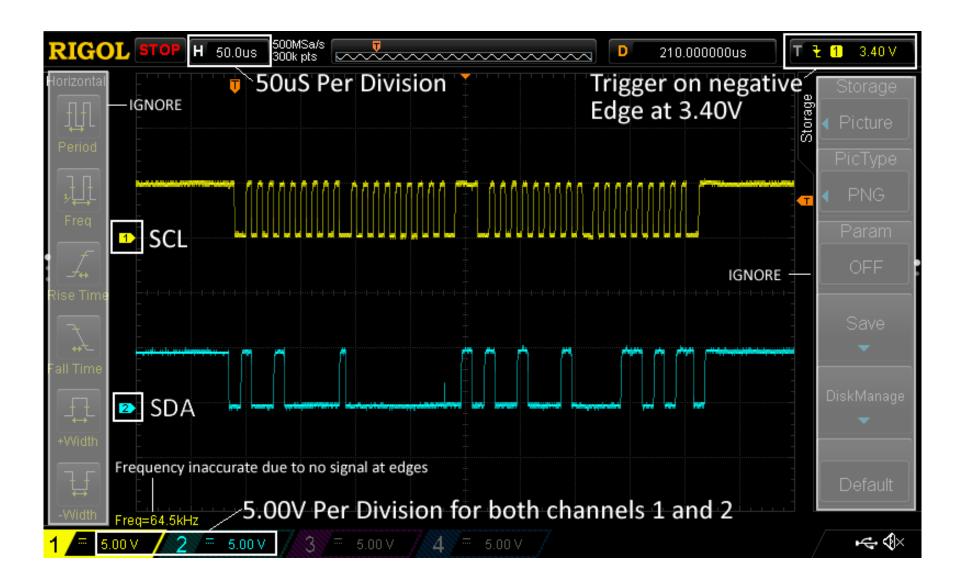


Master receives data from slave





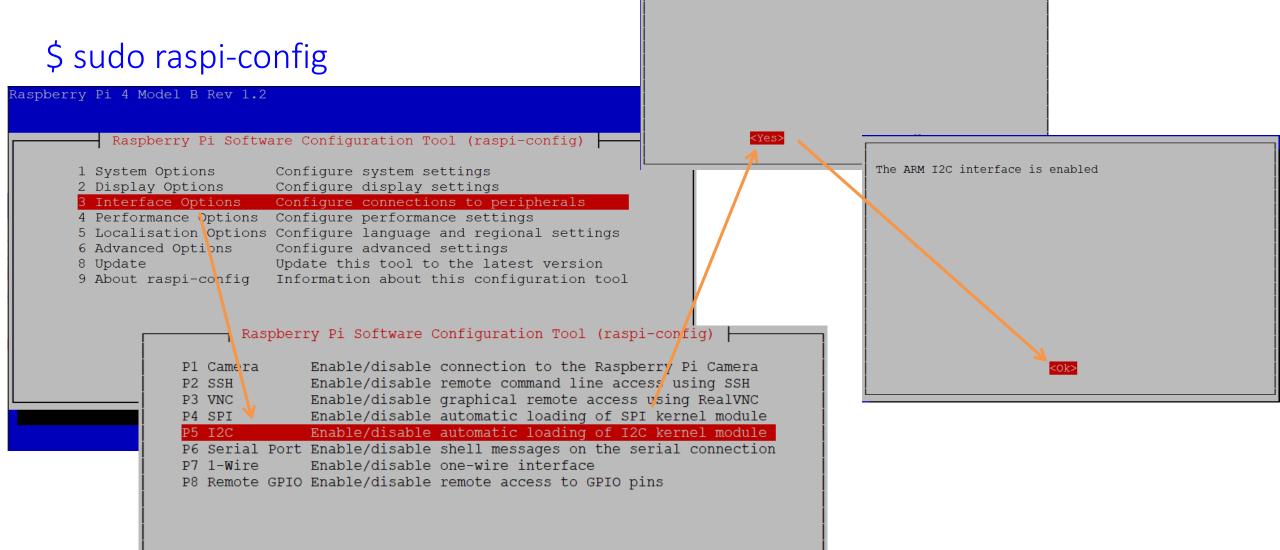
### I2C-Bus





### Enable I<sup>2</sup>C

<Select>



<Back>

Would you like the ARM I2C interface to be enabled?



# 12C Device

\$ ls -l /dev/i2c\*

```
pi@rpi4-A00:~ $ ls -l /dev/i2c*
crw-rw---- 1 root i2c 89, 1 Oct 17 17:54 /dev/i2c-1
```

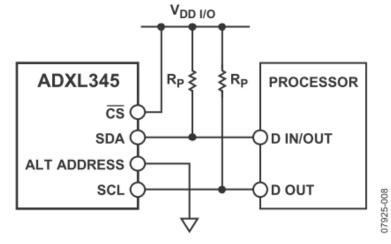
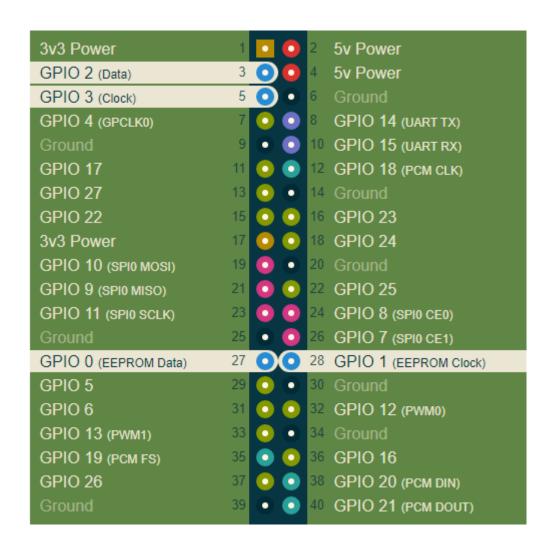
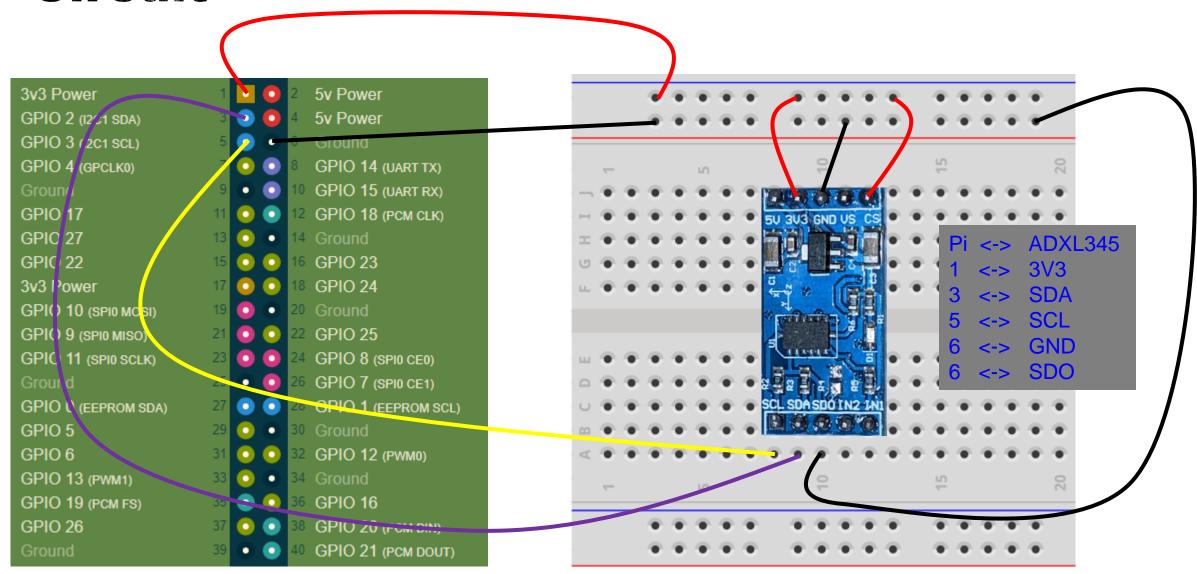


Figure 40. I<sup>2</sup>C Connection Diagram (Address 0x53)

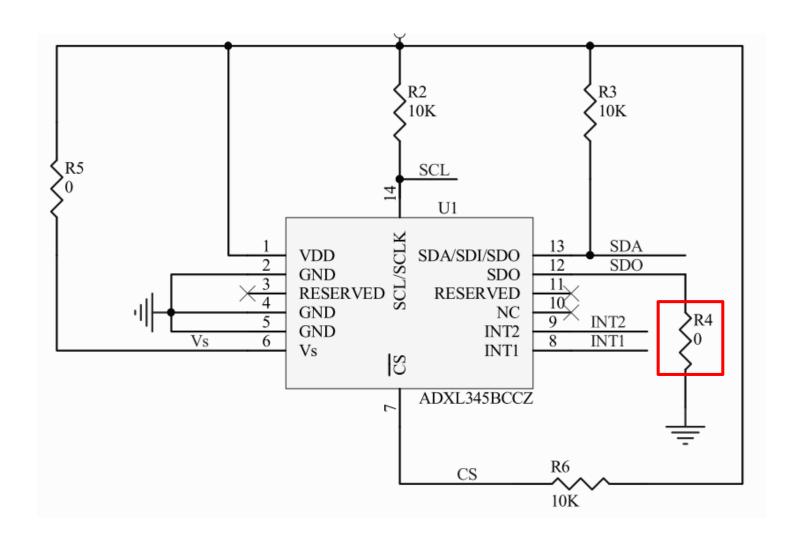




#### Circuit









#### 2C Test

Detect devices on I2C Bus

\$ i2cdetect -y 1

#### 一个一个

### adxI345\_i2c.py

```
import smbus
import time
import struct
# Get I2C bus
bus = smbus.SMBus(1)
deviceID = bus.read byte data(0x53, 0x00)
print("ID: %x" % deviceID)
# ADXL345 address, 0x53(83)
# Select power control register, 0x2D(45)
# 0x08(08) Auto Sleep disable
bus.write byte data(0x53, 0x2D, 0x00)
bus.write byte data(0x53, 0x2D, 0x08)
# ADXL345 address, 0x53(83)
# Select data format register, 0x31(49)
# 0x08(08) Self test disabled, 4-wire interface
# Full resolution, Range = +/-2g
bus.write byte data(0x53, 0x31, 0x08)
time.sleep(0.5)
```

```
try:
   while True:
        # ADXL345 address, 0x53(83)
        # Read data back from 0x32(50), 2 bytes
        accel = \{'x' : 0, 'y' : 0, 'z' : 0\}
        # X-Axis LSB, X-Axis MSB
        data0 = bus.read byte data(0x53, 0x32)
        data1 = bus.read byte data(0x53, 0x33)
        # Convert the data to 10-bits
        xAccl = struct.unpack('<h', bytes([data0, data1]))[0]</pre>
        accel['x'] = xAccl / 256
        # ADXL345 address, 0x53(83)
        # Read data back from 0x34(52), 2 bytes
        # Y-Axis LSB, Y-Axis MSB
        data0 = bus.read byte data(0x53, 0x34)
        data1 = bus.read byte data(0x53, 0x35)
        # Convert the data to 10-bits
        yAccl = struct.unpack('<h', bytes([data0, data1]))[0]</pre>
        accel['y'] = yAccl / 256
        # ADXL345 address, 0x53(83)
        # Read data back from 0x36(54), 2 bytes
        # Z-Axis LSB, Z-Axis MSB
        data0 = bus.read byte data(0x53, 0x36)
        data1 = bus.read byte data(0x53, 0x37)
        # Convert the data to 10-bits
        zAccl = struct.unpack('<h', bytes([data0, data1]))[0]</pre>
        accel['z'] = zAccl / 256
        # Output data to screen
        print ("Ax Ay Az: %.3f %.3f %.3f" % (accel['x'], accel['y'], accel['z']))
        time.sleep(0.1)
except KeyboardInterrupt:
   print("Ctrl+C Break")
```



#### Results

\$ wget https://raw.githubusercontent.com/yachentw/yzucseiot/main/lec05/adxl345\_i2c.py

\$ python3 adxl345\_i2c.py

```
pi@rpi4-A00:~ $ python3 adxl345_i2c.py
ID: e5
Ax Ay Az: 0.043 0.016 1.000
Ax Ay Az: 0.043 0.016 0.996
Ax Ay Az: 0.039 0.012 0.996
Ax Ay Az: 0.039 0.016 0.996
Ax Ay Az: 0.039 0.012 1.000
Ax Ay Az: 0.035 0.012 1.000
Ax Ay Az: 0.043 0.012 0.996
Ax Ay Az: 0.043 0.012 1.004
Ax Ay Az: 0.039 0.012 1.000
Ax Ay Az: 0.035 0.012 1.000
Ax Ay Az: 0.035 0.012 0.992
```





# Register Map

| Address      |         |                |      |             |   |  |  |  |  |  |
|--------------|---------|----------------|------|-------------|---|--|--|--|--|--|
| Hex          | Dec     | Name           | Type | Reset Value | Description   |  |  |  |  |  |
| 0x00         | 0       | DEVID          | R    | 11100101    | Device ID   |  |  |  |  |  |
| 0x01 to 0x1C | 1 to 28 | Reserved       |      |             | Reserved; do not access                                   |  |  |  |  |  |
| 0x1D         | 29      | THRESH_TAP     | R/W  | 00000000    | Tap threshold   |  |  |  |  |  |
| 0x1E         | 30      | OFSX           | R/W  | 00000000    | X-axis offset   |  |  |  |  |  |
| 0x1F         | 31      | OFSY           | R/W  | 00000000    | Y-axis offset   |  |  |  |  |  |
| 0x20         | 32      | OFSZ           | R/W  | 00000000    | Z-axis offset   |  |  |  |  |  |
| 0x21         | 33      | DUR            | R/W  | 00000000    | Tap duration  |  |  |  |  |  |
| 0x22         | 34      | Latent         | R/W  | 00000000    | Tap latency   |  |  |  |  |  |
| 0x23         | 35      | Window         | R/W  | 00000000    | Tap window  |  |  |  |  |  |
| 0x24         | 36      | THRESH_ACT     | R/W  | 00000000    | Activity threshold  |  |  |  |  |  |
| 0x25         | 37      | THRESH_INACT   | R/W  | 00000000    | Inactivity threshold                                      |  |  |  |  |  |
| 0x26         | 38      | TIME_INACT     | R/W  | 00000000    | Inactivity time   |  |  |  |  |  |
| 0x27         | 39      | ACT_INACT_CTL  | R/W  | 00000000    | Axis enable control for activity and inactivity detection |  |  |  |  |  |
| 0x28         | 40      | THRESH_FF      | R/W  | 00000000    | Free-fall threshold                                       |  |  |  |  |  |
| 0x29         | 41      | TIME_FF        | R/W  | 00000000    | Free-fall time  |  |  |  |  |  |
| 0x2A         | 42      | TAP_AXES       | R/W  | 00000000    | Axis control for single tap/double tap                    |  |  |  |  |  |
| 0x2B         | 43      | ACT_TAP_STATUS | R    | 00000000    | Source of single tap/double tap                           |  |  |  |  |  |
| 0x2C         | 44      | BW_RATE        | R/W  | 00001010    | Data rate and power mode control                          |  |  |  |  |  |
| 0x2D         | 45      | POWER_CTL      | R/W  | 00000000    | Power-saving features control                             |  |  |  |  |  |
| 0x2E         | 46      | INT_ENABLE     | R/W  | 00000000    | Interrupt enable control                                  |  |  |  |  |  |
| 0x2F         | 47      | INT_MAP        | R/W  | 00000000    | Interrupt mapping control                                 |  |  |  |  |  |
| 0x30         | 48      | INT_SOURCE     | R    | 00000010    | Source of interrupts                                      |  |  |  |  |  |
| 0x31         | 49      | DATA_FORMAT    | R/W  | 00000000    | Data format control                                       |  |  |  |  |  |
| 0x32         | 50      | DATAX0         | R    | 00000000    | X-Axis Data 0   |  |  |  |  |  |
| 0x33         | 51      | DATAX1         | R    | 00000000    | X-Axis Data 1   |  |  |  |  |  |
| 0x34         | 52      | DATAY0         | R    | 00000000    | Y-Axis Data 0   |  |  |  |  |  |
| 0x35         | 53      | DATAY1         | R    | 00000000    | Y-Axis Data 1   |  |  |  |  |  |
| 0x36         | 54      | DATAZO         | R    | 00000000    | Z-Axis Data 0   |  |  |  |  |  |
| 0x37         | 55      | DATAZ1         | R    | 00000000    | Z-Axis Data 1   |  |  |  |  |  |
| 0x38         | 56      | FIFO_CTL       | R/W  | 00000000    | FIFO control  |  |  |  |  |  |
| 0x39         | 57      | FIFO_STATUS    | R    | 00000000    | FIFO status   |  |  |  |  |  |



| MASTER START SLAVE ADDRESS + WRITE |     | REGISTER ADDRESS |     | DATA                |      | STOP     |     |      |      |      |           |  |  |  |
|------------------------------------|-----|------------------|-----|---------------------|------|----------|-----|------|------|------|-----------|--|--|--|
| SLAVE                              |     |                  | ACK |                     | ACK  |          |     |      |      |      |           |  |  |  |
|                                    |     |                  |     |                     |      |          |     |      |      |      |           |  |  |  |
| MULTIPLE-BYTE WRITE                |     |                  |     |                     |      |          |     |      |      |      |           |  |  |  |
| MASTER START SLAVE ADDRESS + WRITE |     | REGISTER ADDRESS |     | DATA                |      | DATA     |     | STOP |      |      |           |  |  |  |
| SLAVE                              | ACK |                  | ACK |                     | ACK  |          | ACK |      |      |      |           |  |  |  |
|                                    |     |                  |     |                     |      |          |     |      |      |      |           |  |  |  |
| SINGLE-BYTE READ                   |     |                  |     |                     |      |          |     |      |      |      |           |  |  |  |
| MASTER START SLAVE ADDRESS + WRITE |     | REGISTER ADDRESS |     | START SLAVE ADDRESS | READ |          |     | NACK | STOP |      |           |  |  |  |
| SLAVE                              | ACK |                  | ACK |                     |      | ACK DATA |     |      |      |      |           |  |  |  |
|                                    |     |                  |     |                     |      |          |     |      |      |      |           |  |  |  |
| MULTIPLE-BYTE READ                 |     |                  |     |                     |      |          |     |      |      |      |           |  |  |  |
| MASTER START SLAVE ADDRESS + WRITE |     | REGISTER ADDRESS |     | START SLAVE ADDRESS | READ |          |     | ACK  |      |      | NACK STOP |  |  |  |
| SLAVE                              | ACK |                  | ACK |                     |      | ACK DATA |     |      |      | DATA |           |  |  |  |

#### NOTES

SINGLE-BYTE WRITE

- 1. THIS START IS EITHER A RESTART OR A STOP FOLLOWED BY A START.
- 2. THE SHADED AREAS REPRESENT WHEN THE DEVICE IS LISTENING.

Figure 41. I<sup>2</sup>C Device Addressing



## Using ADXL345 Library

\$ sudo pip3 install adafruit-circuitpython-adxl34x

\$ wget https://github.com/yachentw/yzucseiot/blob/main/lec05/ada\_i2c.py

```
import time
import board
import busio
import adafruit_adxl34x
i2c = busio.I2C(board.SCL, board.SDA)
accelerometer = adafruit_adxl34x.ADXL345(i2c)
while True:
    print("%f %f %f" % accelerometer.acceleration)
    time.sleep(1)
```

\$ python3 ada\_i2c.py

```
pi@rpi4-A00:~/iot/lec05 $ python3 ada_i2c.py
0.431493 0.117680 10.081236
0.470719 0.117680 10.042010
0.392266 0.117680 10.081236
0.000000 -3.961887 9.257478
0.000000 -8.119906 5.295591
0.000000 -8.237586 5.021005
0.470719 0.156906 10.081236
0.470719 0.156906 10.081236
```



### Open Source Driver

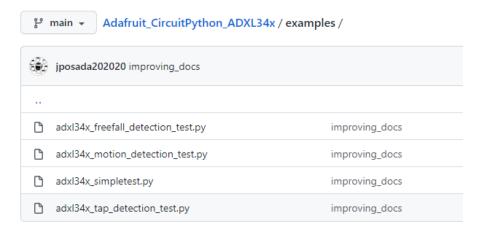
https://github.com/adafruit/Adafruit\_CircuitPython\_ADXL34x/blob/master/adafruit\_adxl34x.py

```
1 # SPDX-FileCopyrightText: 2018 Bryan Siepert for Adafruit Industries
    # SPDX-License-Identifier: MIT
     `adafruit adxl34x`
    _____
    A driver for the ADXL34x 3-axis accelerometer family
10
    * Author(s): Bryan Siepert
12
    Based on drivers by K. Townsend and Tony DiCola
14
15
    Implementation Notes
18
    **Hardware:**
19
20
    * Adafruit `ADXL345 Digital Accelerometer
      <https://www.adafruit.com/product/1231>`_ (Product ID: 1231)
22
23
    **Software and Dependencies:**
25
    * Adafruit CircuitPython firmware for the supported boards:
27
      https://circuitpython.org/downloads
28
    * Adafruit's Bus Device library: https://github.com/adafruit/Adafruit CircuitPython BusDevice
```



## Examples

https://github.com/adafruit/Adafruit\_CircuitPython\_ADXL34x/tree/main/examples



You can run the provided examples.

\$ wget https://raw.githubusercontent.com/adafruit/Adafruit\_CircuitPython\_ADXL34x/main/examples/adxl34x\_freefall\_detection\_test.py

\$ wget https://raw.githubusercontent.com/adafruit/Adafruit\_CircuitPython\_ADXL34x/main/examples/adxl34x\_motion\_detection\_test.py

\$ wget https://raw.githubusercontent.com/adafruit/Adafruit\_CircuitPython\_ADXL34x/main/examples/adxl34x\_simpletest.py

\$ wget https://raw.githubusercontent.com/adafruit/Adafruit\_CircuitPython\_ADXL34x/main/examples/adxl34x\_tap\_detection\_test.py



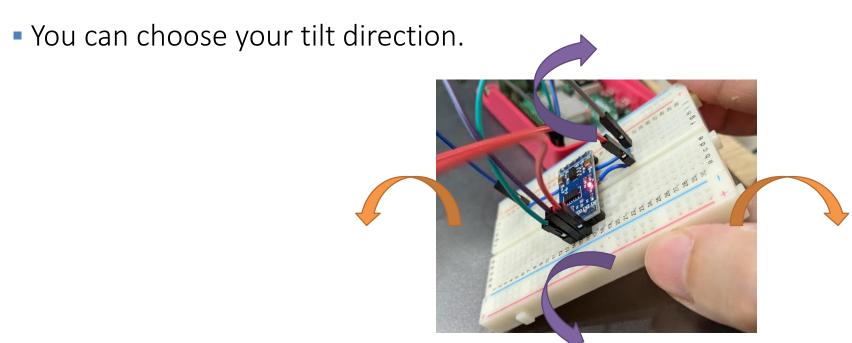
## Outline

- SPI
- **1**2C
- Lab



#### **CS348A**

- Change the sound pitch from the buzzer according to the tilt angle of the accelerometer where 0 degree is pitch C and 90 degree is pitch B.
  - e.g., 45 degree is pitch F.

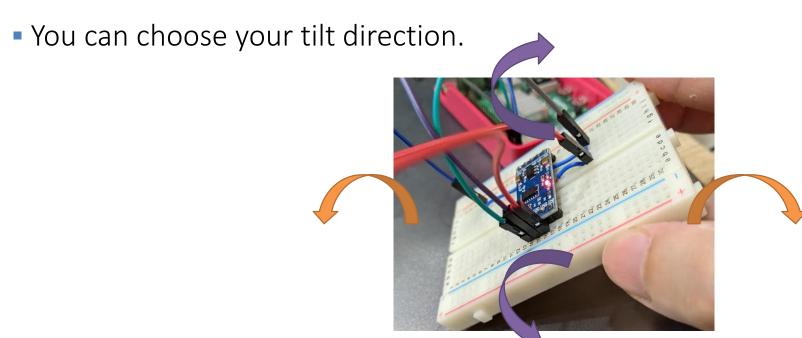


You can use I2C or SPI to communicate with ADXL345 by any library.



#### **CS348B**

- Change the brightness of the LED according to the tilt angle of the accelerometer where 0 degree is 0% brightness and 90 degree is 100% brightness.
  - e.g., 45 degree is about 50% brightness.



You can use I2C or SPI to communicate with ADXL345 by any library.