| CPU Model | CPU Frequency (GHZ) | Memory | Cache Memory | system.cp u.numcycl es | simsecond s | simTicks | finalTick |
|---------------------|---------------------------|-----------------------------|---|------------------------------|----------------|------------------|------------------|
| TimingSim pleCPU | 1 | Default | Default | 202425331 | | | |
| TimingSim pleCPU | 1 | Default | L1I = 16 kb, L1D= 32 kb, L2= 64 kb | 206635109 | | | |
| TimingSim pleCPU | 4 | Default | Default | 310930696 | | | |
| TimingSim pleCPU | 4 | Default | L1I = 16 kb, L1D= 32 kb, L2= 64 kb | 320345348 | | | |
| TimingSim pleCPU | 1 | LPDDR2_S4 _1066_1x3 2 | Default | | 0.217695 | 217694895 000 | 217694895 000 |
| TimingSim pleCPU | 4 | DDR3_1600 _8x8 | Default | | 0.077733 | 777326740 00 | 777326740 00 |