

80V N-Channel Power MOSFET

Description

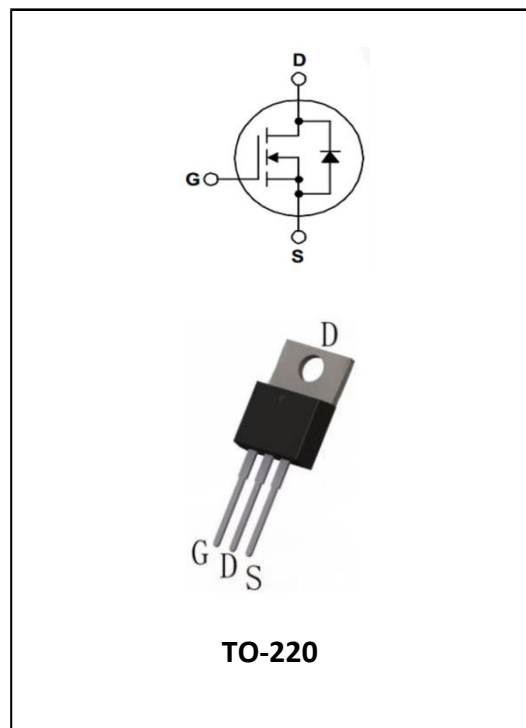
The P75NF75 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.

KEY CHARACTERISTICS

- ① $V_{DS} = 80V, I_D = 80A$
 $R_{DS(ON)} < 12m\Omega @ V_{GS}=10V$
- ② Special process technology for high ESD capability
- ③ High density cell design for lower $R_{DS(ON)}$
- ④ Fully characterized avalanche voltage and current
- ⑤ Good stability and uniformity with high EAS
- ⑥ Excellent package for good heat dissipation

Application

- ① Power switching application
- ② Hard switched and High frequency circuits
- ③ Uninterruptible power supply



Package Marking And Ordering Information:

Ordering Codes	Package	Product Code	Packing
P75NF75	TO-220	P75NF75	Tube

Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	80	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	80	A
Drain Current-Pulsed (Note 1)	I_{DM}	280	A
Maximum Power Dissipation($T_c=25^\circ C$)	P_D	147	W
Single pulse avalanche energy(Note 2)	E_{AS}	330	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ C$

Thermal Characteristic

Thermal Resistance,Junction-to-Case	$R_{\theta JC}$	1.02	$^\circ C/W$
Thermal Resistance,Junction-to-Ambient	$R_{\theta JA}$	62.5	$^\circ C/W$

Electrical Characteristics (TA=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	80	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=80V, V_{GS}=0V$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
On Characteristics						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	3	4	V
Drain-Source On-State Resistance ^(Note 3)	$R_{DS(ON)}$	$V_{GS}=10V, I_D=35A$	-	10	12	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=20V, I_D=35A$	-	90	-	S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V,$ $f=1.0MHz$	-	3950	-	pF
Output Capacitance	C_{oss}		-	280	-	pF
Reverse Transfer Capacitance	C_{rss}		-	210	-	pF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=40V, I_D=35A,$ $V_{GS}=10V, R_{GEN}=3\Omega$	-	130	-	nS
Turn-on Rise Time	t_r		-	200	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	350	-	nS
Turn-Off Fall Time	t_f		-	245	-	nS
Total Gate Charge	Q_g	$V_{DS}=64V, I_D=35A$ $V_{GS}=10V$	-	85	-	nC
Gate-Source Charge	Q_{gs}		-	24	-	nC
Gate-Drain Charge	Q_{gd}		-	28	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=1A$	-	-	1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. EAS condition :T j=25℃,L=0.5mH,VDD=50V,VG=10V, Rg=25 Ω .
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production.

Characteristics Curves

Figure 1 Output Characteristics

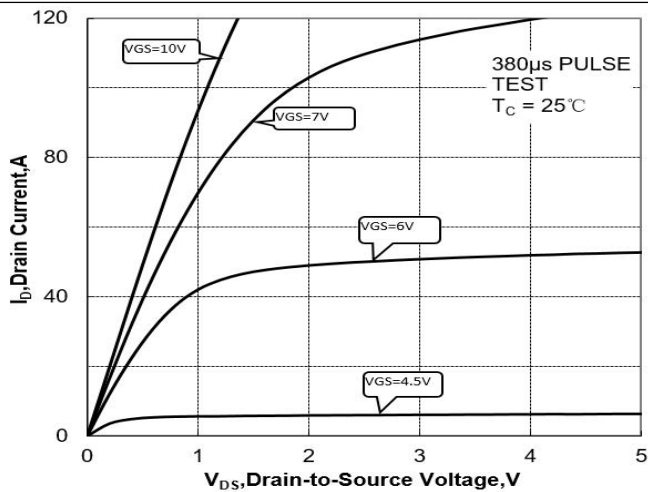


Figure 2 Transfer Characteristics

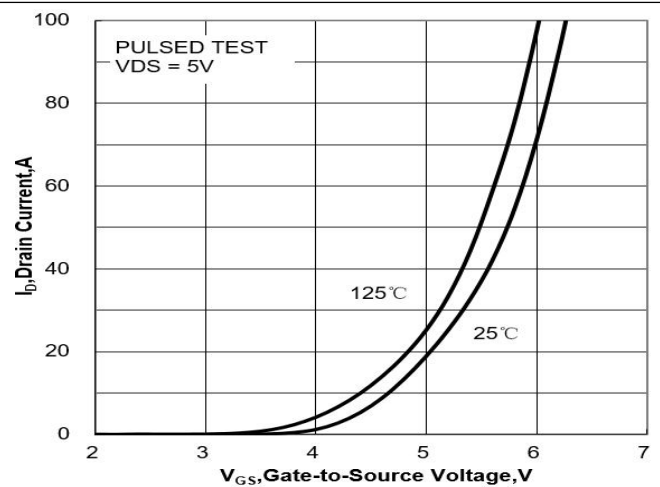


Figure 3 On-Resistance vs. I_D and V_{GS}

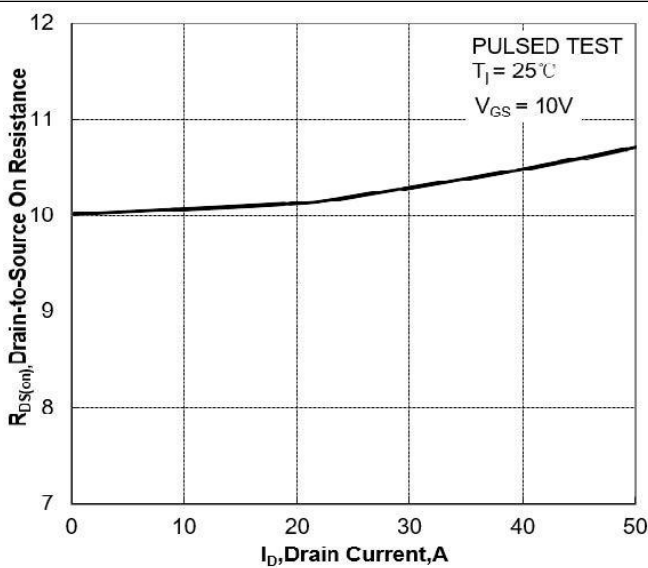


Figure 4 On-Resistance vs. Junction Temperature

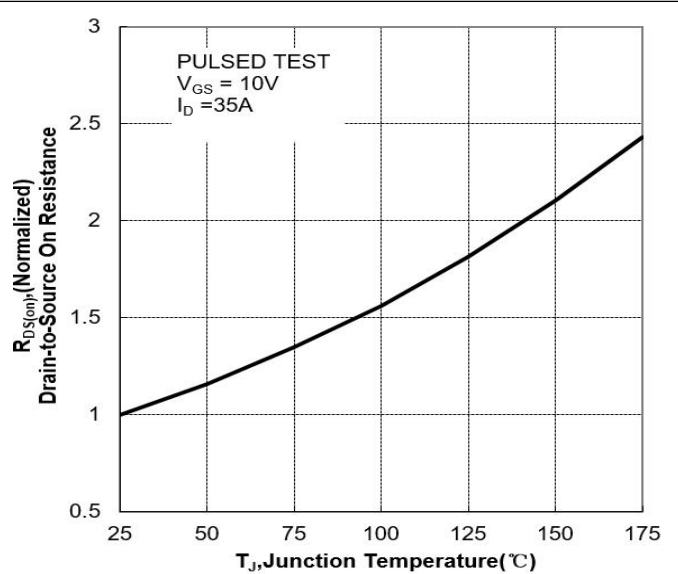


Figure 5 On-Resistance vs. V_{GS}

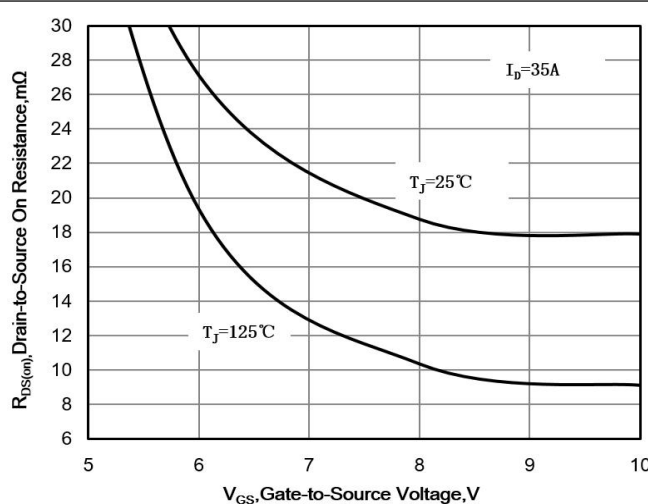


Figure 6 Body Diode Forward Voltage

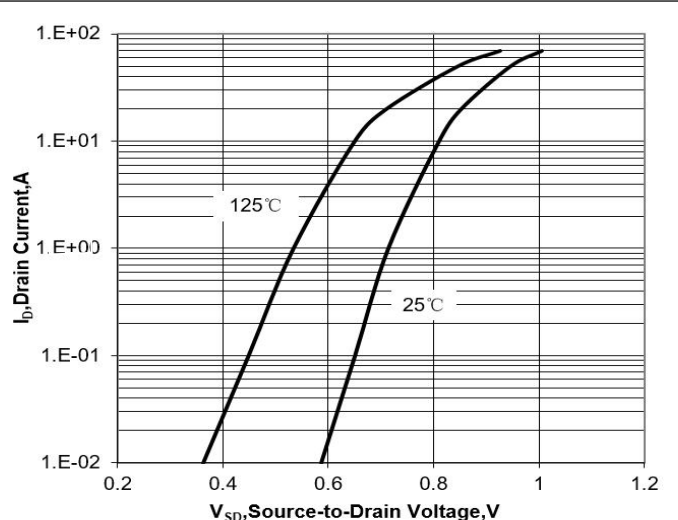


Figure 7 Gate-Charge Characteristics

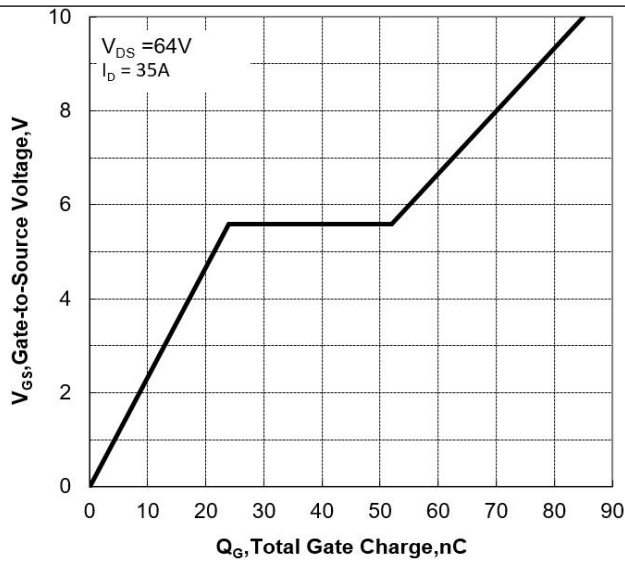


Figure 8 Capacitance Characteristics

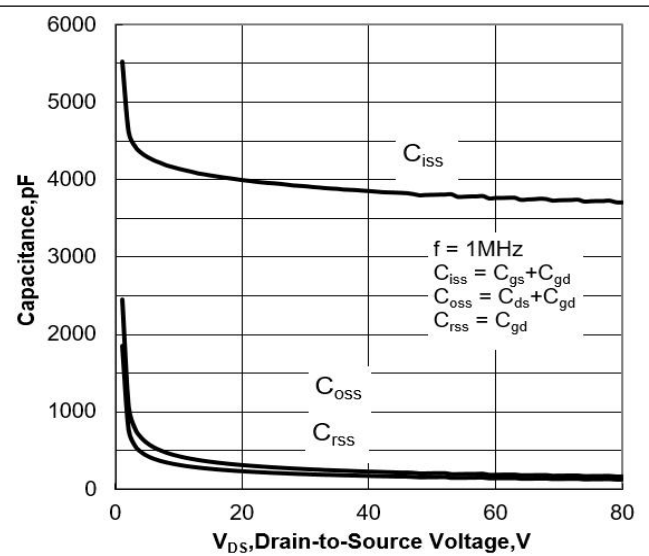


Figure 9 Maximum Forward Biased Safe Operation Area

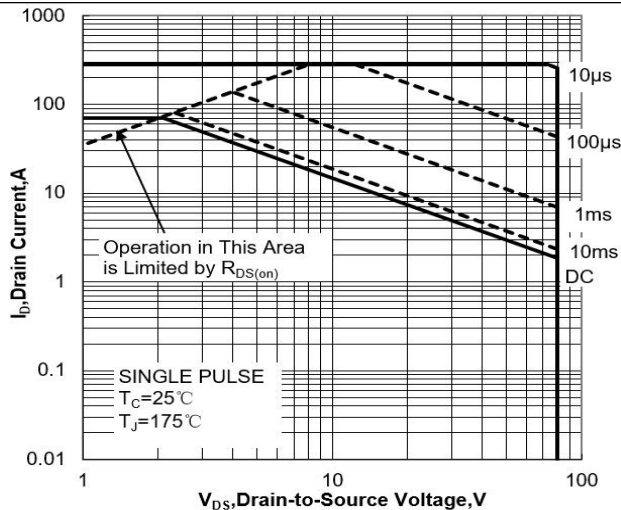


Figure 10 Single Pulse Power Rating Junction-to-Ambient

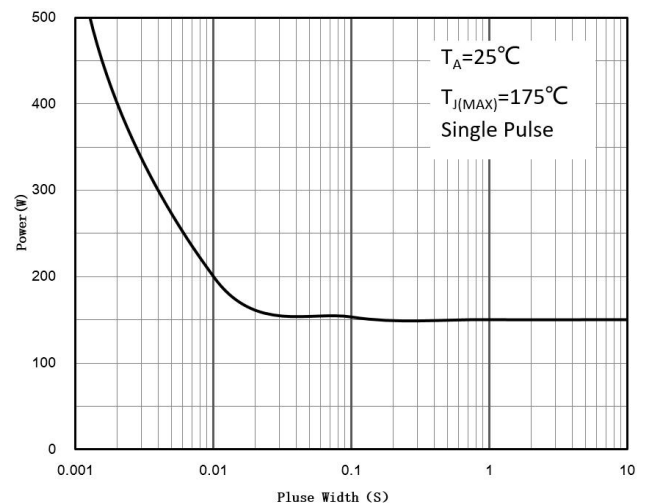
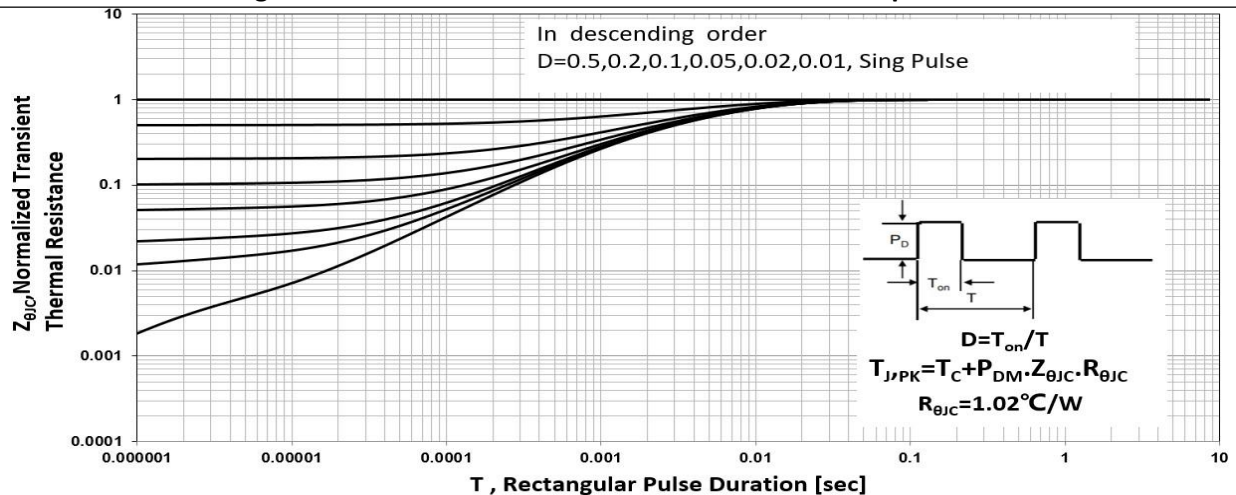
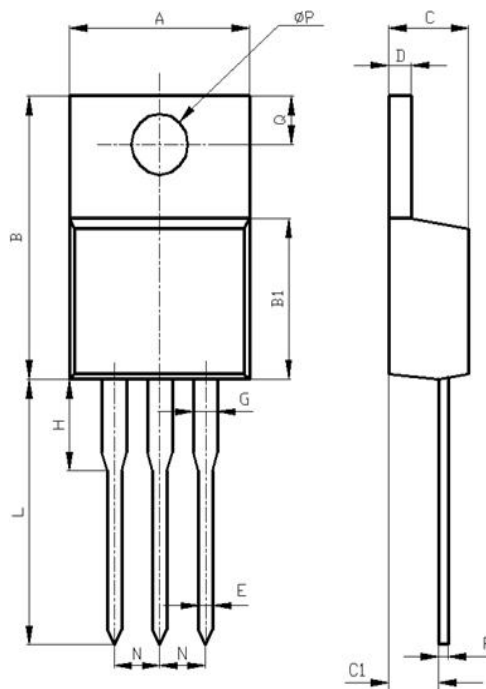


Figure 11 Normalized Maximum Transient Thermal Impedance



Package Description:



Items	Values(mm)	
	MIN	MAX
A	9.60	10.6
B	15.0	16.0
B1	8.90	9.50
C	4.30	4.80
C1	2.30	3.10
D	1.20	1.40
E	0.70	0.90
F	0.30	0.60
G	1.17	1.37
H	2.70	3.80
L	12.6	14.8
N	2.34	2.74
Q	2.40	3.00
φp	3.50	3.90

TO-220 package



迈诺斯科技

P75NF75

NOTE:

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. Shenzhen Minos reserves the right to make changes in this specification sheet and is subject to change without prior notice.

CONTACT:

深圳市迈诺斯科技有限公司（总部）

地址：深圳市福田区华富街道田面社区深南中路4026号田面城市大厦22B-22C

邮编：518025

电话：0755-83273777