

#### **Features**

- · Split Gate Trench MOSFET Technology
- Lower R<sub>DS(ON)</sub>
- · Lower Capacitance
- Halogen Free . "Green" Device (Note1)
- · Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

# **Maximum Ratings**

• Operating Junction Temperature Range: -55°C to +150°C

• Storage Temperature Range: -55°C to +150°C

• Thermal Resistance: 40°C/W Junction to Ambient(Note2)

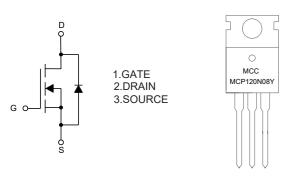
• Thermal Resistance: 0.7°C/W Junction to Case

Parameter		Symbol	Rating	Unit	
Drain-Source Voltage		V <sub>DS</sub>	80	V	
Gate-Source Volltage		V <sub>GS</sub>	±20	V	
Continuous Drain Current	T <sub>C</sub> =25°C		120	Α	
	T <sub>C</sub> =25°C T <sub>C</sub> =100°C	_ I <sub>D</sub>	76		
Pulsed Drain Current (Note3)	I <sub>DM</sub>	480	Α		
Total Power Dissipation (Note4)		P <sub>D</sub>	178	W	
Avalanche Energy (Note5)		E <sub>AS</sub>	702	mJ	

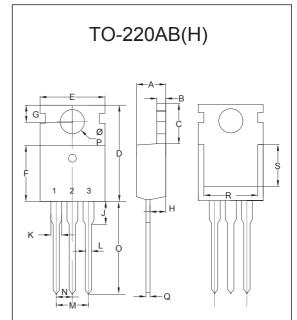
#### Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of  $R_{\theta,JA}$  is measured with the device mounted on  $1in^2$  FR-4 board with 2oz. Copper, in a still air environment with  $T_A$  =25°C.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4.  $P_D$  is based on max. junction temperature, using junction-case thermal resistance.
- 5.  $T_J$ =25°C,  $V_{DD}$ =50V,  $V_{GS}$ =10V, L=2mH

# **Internal Structure and Marking Code**



# N-CHANNEL MOSFET



DIMENSIONS					
DIM	INCHES		MM		NOTE
DIIVI	MIN	MAX	MIN	MAX	NOTE
Α	0.172	0.188	4.37	4.77	
В	0.049	0.057	1.25	1.45	
С	0.246	0.270	6.25	6.85	
D	0.594	0.634	15.10	16.10	
Е	0.382	0.406	9.70	10.30	
F	0.346	0.370	8.80	9.40	
G	0.102	0.118	2.60	3.00	
Н	0.087	0.102	2.20	2.60	
J		0.134		3.40	
K	0.046	0.058	1.17	1.47	
L	0.028	0.037	0.70	0.95	
M	0.200		5.08		TYP.
N	0.100		2.54		TYP.
0	0.502	0.543	12.75	13.80	
Р	0.134	0.150	3.40	3.80	Ф
Q	0.016	0.026	0.40	0.65	
R	0.276		7.00		
S	0.217		5.50		



# Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	80			V	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V			1	μΑ	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_D=250\mu A$	2.0	3.0	4.0	V	
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A		3.8	4.8	mΩ	
Gate Resistance	$R_G$	f=1MHz, Open drain		2.0		Ω	
Diode Characteristics							
Continuous Body Diode Current	Is				120	Α	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A		0.8	1.2	V	
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =50A, dI <sub>F</sub> /dt=460A/μs		37		ns	
Reverse Recovery Charge	$Q_{rr}$	17 0071, dip/dt 40077 po		150		nC	
Dynamic Characteristics							
Input Capacitance	C <sub>iss</sub>			5223			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =40V,V <sub>GS</sub> =0V,f=1MHz		836		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			18		1	
Total Gate Charge	Qg			72			
Gate-Source Charge	$Q_{gs}$	V <sub>DS</sub> =40V,V <sub>GS</sub> =10V, I <sub>D</sub> =50A		24		nC	
Gate-Drain Charge	$Q_{gd}$			17			
Turn-On Delay Time	t <sub>d(on)</sub>			16			
Turn-On Rise Time	t <sub>r</sub>	V <sub>DD</sub> =40V, V <sub>GS</sub> =10V,		97		ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_G=2.2\Omega$ , $I_D=50A$		41		113	
Turn-Off Fall Time	t <sub>f</sub>			14			



## **Curve Characteristics**

Fig. 1 - Typical Output Characteristics

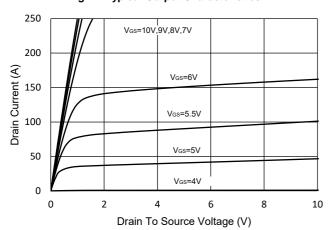


Fig.2 - Transfer Characteristic

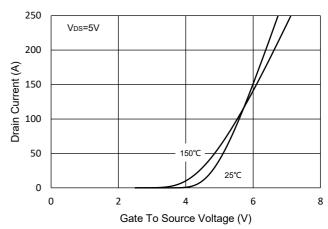


Fig.3 -  $R_{\rm DS(ON)}$  -  $V_{\rm GS}$ 

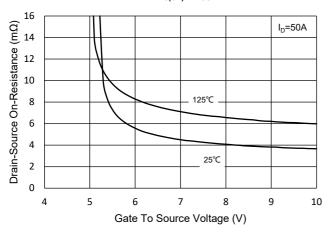


Fig.4 -  $R_{\rm DS(ON)}$  -  $I_{\rm D}$ 

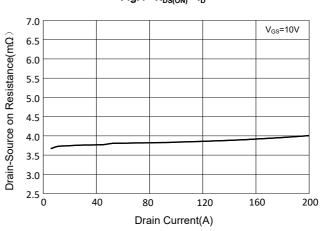


Fig.5 - Capacitance Characteristics

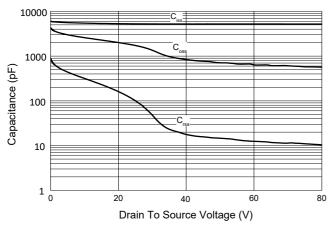
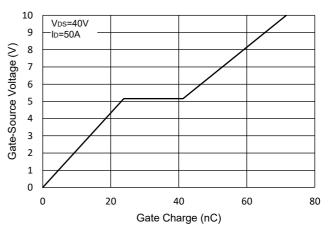
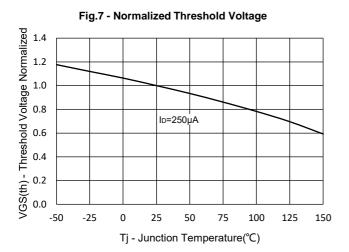


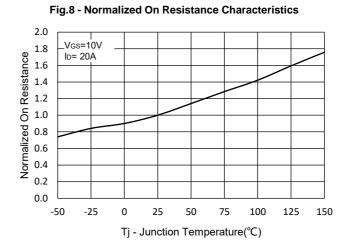
Fig.6 - Gate Charge

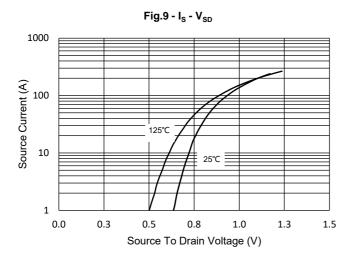


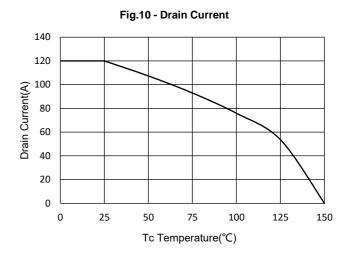


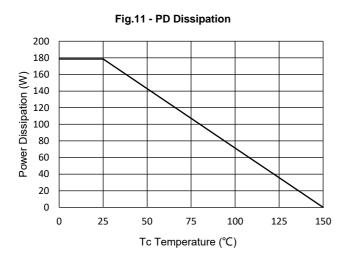
## **Curve Characteristics**













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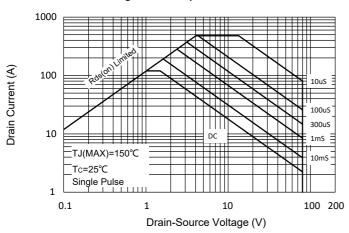
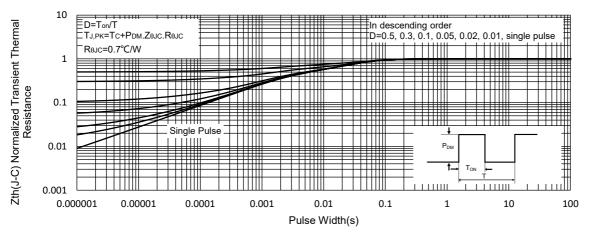


Fig.13 - Normalized Transient Thermal Impedance





## **Ordering Information**

Device	Packing	
Part Number-BP	Bulk:50pcs/Tube,1Kpcs/Box,5Kpcs/Carton	

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