

#### **Features**

- · Split Gate Trench MOSFET Technology
- · Fast Switching
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

# **Maximum Ratings**

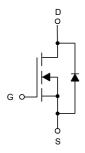
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 40°C/W Junction to Ambient(Note 2)
- Thermal Resistance: 1.5°C/W Junction to Case

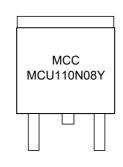
Parameter		Symbol	Rating	Unit	
Drain-Source Voltage		V <sub>DS</sub>	80	V	
Gate-Source Volltage		V <sub>GS</sub>	±20	V	
Continuous Drain Current	T <sub>C</sub> =25°C	1	110	Α	
	T <sub>C</sub> =100°C	- I <sub>D</sub> -	69		
Pulsed Drain Current (Note 3)		I <sub>DM</sub>	440	Α	
Total Power Dissipation (Note 4)		P <sub>D</sub>	83	W	
Single Pulse Avalanche Energy (Note 5)		E <sub>AS</sub>	361	mJ	

#### Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of  $R_{\theta,JA}$  is measured with the device mounted on  $1in^2$  FR-4 board with 2oz. Copper, in a still air environment with  $T_A$  =25°C.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4.P<sub>D</sub> is based on max. junction temperature, using junction-case thermal resistance.
- $5.T_J = 25$ °C,  $V_{DD} = 50$ V,  $V_{GS} = 10$ V,  $Rg = 25\Omega$ , L = 0.5mH.

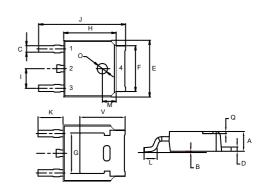
# **Internal Structure and Marking Code**





# N-CHANNEL MOSFET

# DPAK(TO-252)



- Gate
- 2,4. Drain
  - 3. Source

DIMENSIONS					
DIM INCHES		MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE
Α	0.087	0.094	2.20	2.40	
В	0.000	0.005	0.00	0.13	
С	0.026	0.034	0.66	0.86	
D	0.018	0.023	0.46	0.58	
Е	0.256	0.264	6.50	6.70	
F	0.201	0.215	5.10	5.46	
G	0.190		4.83		TYP.
Н	0.236	0.244	6.00	6.20	
ı	0.086	0.094	2.18	2.39	
J	0.386	0.409	9.80	10.40	
K	0.114		2.90		TYP.
L	0.047	0.069	1.20	1.75	
M	0.063		1.60		TYP.
0	0.043	0.051	1.10	1.30	
Q	0.000	0.012	0.00	0.30	
V	0.211		5.35		TYP.



# Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics				1			
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	80			V	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V			1	μΑ	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.4	1.8	2.5	V	
Drain-Source On-Resistance		V <sub>GS</sub> =10V, I <sub>D</sub> =55A	4.1 5.6		mΩ		
	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A		5.1	7	11122	
Gate resistance	$R_g$	f=1MHz, Open drain		1.9		Ω	
Diode Characteristics			"				
Continuous Body Diode Current	Is				110	Α	
Body Diode Voltage	V <sub>SD</sub>	I <sub>SD</sub> =55A, V <sub>GS</sub> =0V		0.9	1.2	V	
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =55A,di/dt=380A/μs		40		ns	
Reverse Recovery Charge	Q <sub>rr</sub>	1 <sub>F</sub> -33A,αι/αι-360Α/μ5		110		nC	
Dynamic Characteristics							
Input Capacitance	C <sub>iss</sub>			5200			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =40V,V <sub>GS</sub> =0V,f=1MHz		892		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			29			
Total Gate Charge	$Q_g$			88			
Gate-Source Charge	$Q_{gs}$	V <sub>DS</sub> =40V,V <sub>GS</sub> =10V,I <sub>D</sub> =55A		21		nC	
Gate-Drain Charge	$Q_{gd}$			19			
Turn-On Delay Time	t <sub>d(on)</sub>			12			
Turn-On Rise Time	t <sub>r</sub>	V <sub>DD</sub> =40V,I <sub>D</sub> =55A,		126		ns	
Turn-Off Delay Time	$t_{d(off)}$	$V_{GS}$ =10V, $R_{G}$ =2.2 $\Omega$		57			
Turn-Off Fall Time	t <sub>f</sub>			18			



## **Curve Characteristics**

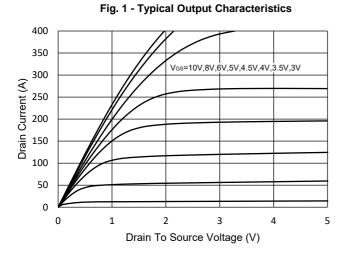


Fig.2 - Transfer Characteristic

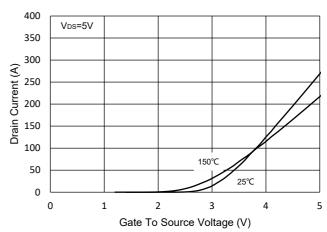


Fig.3 - R<sub>DS(ON)</sub> - V<sub>GS</sub>

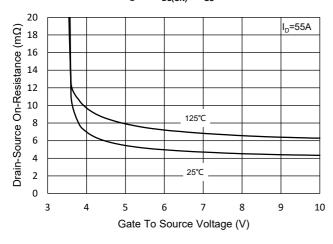


Fig.4 - R<sub>DS(ON)</sub> - I<sub>D</sub>

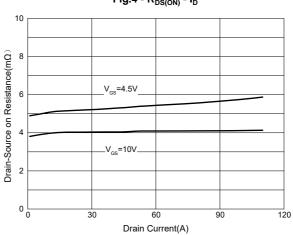


Fig.5 - Capacitance Characteristics

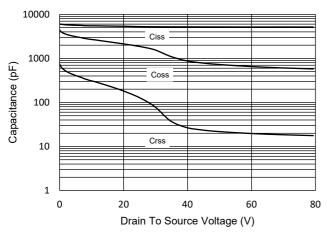
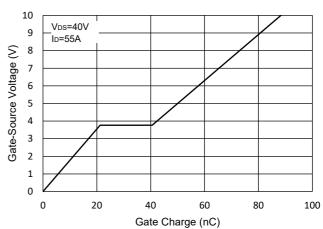


Fig.6 - Gate Charge





## **Curve Characteristics**

Fig.7 - Normalized Threshold Voltage

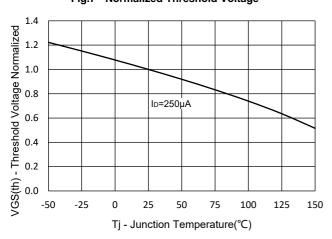


Fig.8 - Normalized On Resistance Characteristics

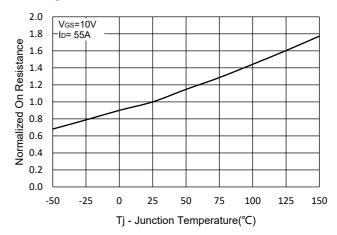


Fig.9 - I<sub>S</sub> - V<sub>SD</sub>

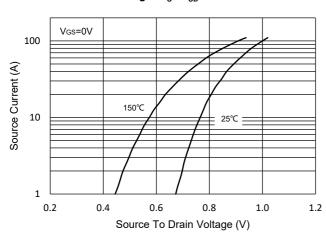


Fig.10 - Drain Current

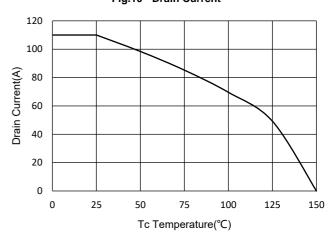
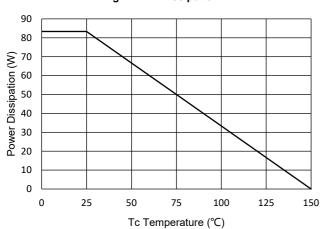
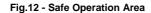


Fig.11 - PD Dissipation





## **Curve Characteristics**



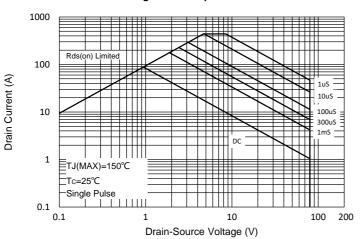
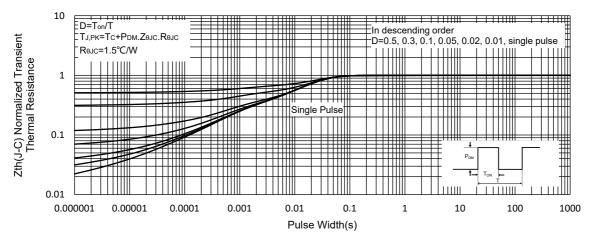


Fig.13 - Normalized Transient Thermal Impedance





# **Ordering Information**

Device	Packing	
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel	

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