

RJK1001DPP-A0

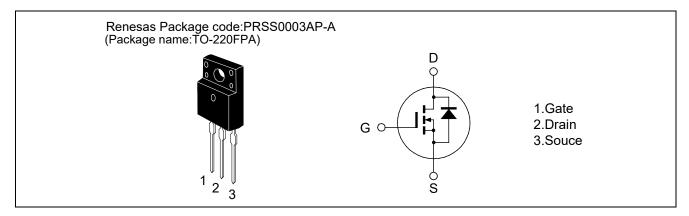
N-Channel MOSFET 100 V, 80 A, 5.5 m Ω

R07DS1444EJ0101 Rev.1.01 2020.1.9

Features

- · High speed switching
- Low drive current
- Low on-resistance $R_{DS(on)} = 4.4 \text{ m}\Omega$ typ. (at $V_{GS} = 10 \text{ V}$)
- Package TO-220FPAQuality Grade : Standard

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	100	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D Note1	80	Α
Drain peak current	I _{D (pulse)} Note2	240	Α
Body-drain diode reverse drain current	I _{DR}	80	Α
Avalanche current	I _{AP} Note3	40	Α
Avalanche energy	E _{AS} Note3	160	mJ
Channel dissipation	Pch Note1	30	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

Notes: 1. Tc = 25°C

- 2. $PW \le 10 \ \mu s$, duty cycle $\le 1\%$
- 3. Value at L = 100 μ H, Tch = 25°C, Rg \geq 50 Ω ,

Thermal Impedance

Item	Symbol	Max. Value Note4	Unit
Channel to case thermal impedance	θch-c	4.17	°C/W

Notes: 4. This data is the designed target maximum value on Renesas's measurement condition. (Not tested)

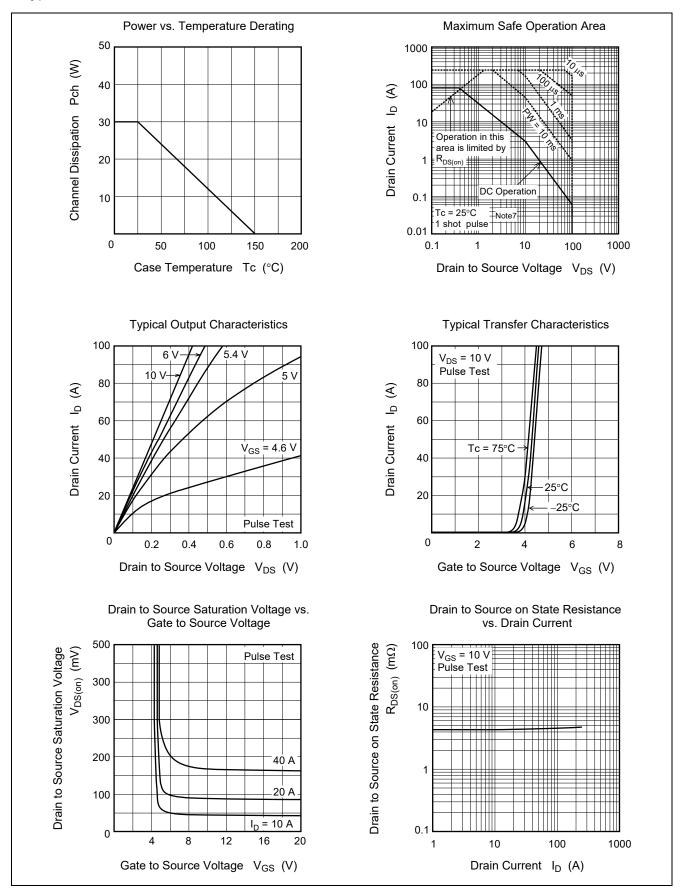
Electrical Characteristics

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	100			V	$I_D = 10 \text{mA}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μА	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μА	V _{DS} = 100 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	2.0	_	4.0	V	V _{DS} = 10 V, I _D = 1 mA
Static drain to source on state	R _{DS(on)}	_	4.4	5.5	mΩ	I _D = 40 A, V _{GS} = 10 V ^{Note5}
resistance						
Forward transfer admittance	y _{fs}		150		S	$I_D = 40 \text{ A}, V_D = 10 \text{ V}^{\text{Note5}}$
Input capacitance	Ciss	_	10000	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	1500	_	pF	V _{GS} = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	370	_	pF	
Gate Resistance	Rg	_	1.6	_	Ω	
Total gate charge	Qg	_	147	_	nC	V _{DD} = 50 V
Gate to source charge	Qgs	_	50	_	nC	V _{GS} = 10 V, I _D = 40 A
Gate to drain charge	Qgd	_	45	_	nC	
Turn-on delay time	t _{d(on)}	_	53	_	ns	V _{GS} = 10 V
Rise time	t _r	_	20	_	ns	$\begin{split} I_D &= 40 \text{ A} \\ V_{DD} &\cong 30 \text{ V} \\ Rg &= 4.7 \Omega \end{split}$
Turn-off delay time	t _{d(off)}	_	110	_	ns	
Fall time	t _f	_	22	_	ns	
Body-drain diode forward voltage	V_{DF}	_	0.85	1.5	V	I _F = 80 A, V _{GS} = 0 ^{Note5}
Body-drain diode reverse recovery time	t _{rr}	_	75	_	ns	I _F = 80 A, V _{GS} = 0 di _F /dt = 100 A/μs

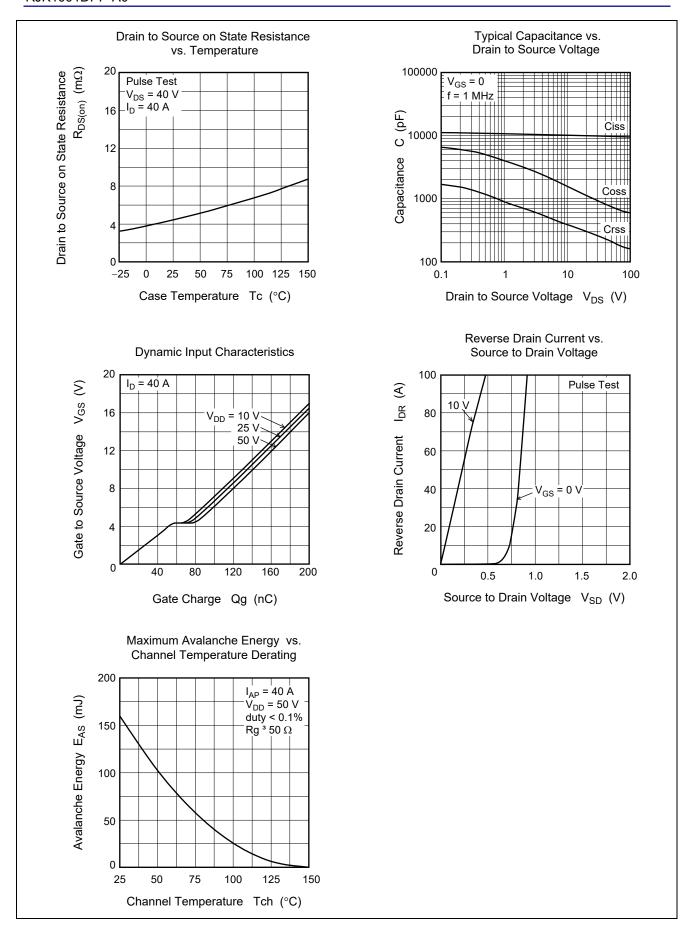
Notes: 5. Pulse test

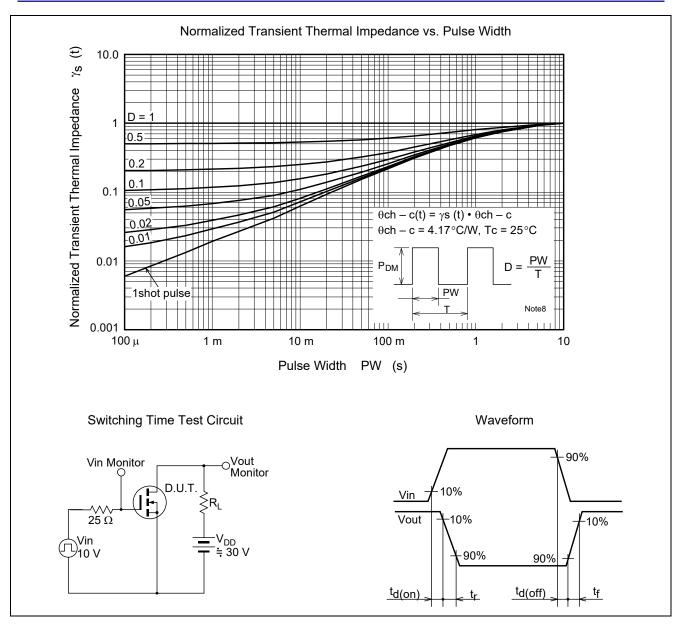
Typical Characteristics Note6



Notes: 6. Designed target value on Renesas measurement condition.

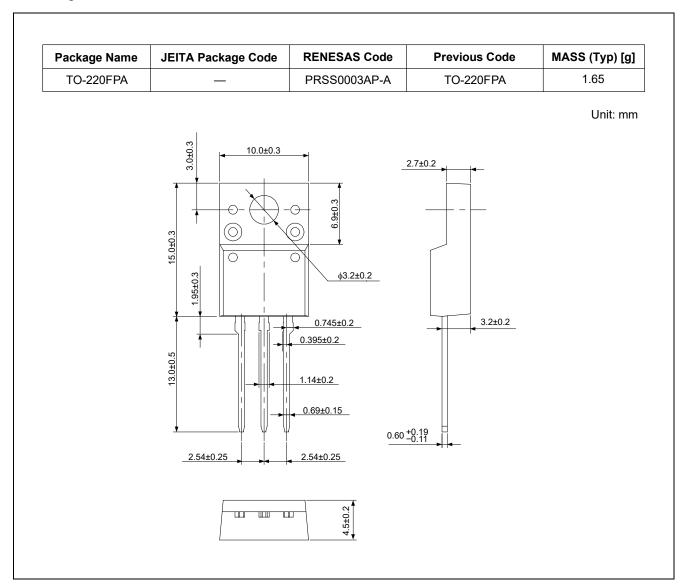
^{7.} This data is the designed value on Renesas's measurement condition. Renesas recommends that operating conditions are designed according to a document "Power MOSFET/IGBT Attention of Handling Semiconductor Devices (R07ZZ0010)".





Notes: 8. This data is the designed target maximum value on Renesas's measurement condition.

Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJK1001DPP-A0-T2	50 pcs	Magazine (Tube)

Note: The symbol of 2nd "-" is occasionally presented as "#".

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