

#### **Features**

- · Split Gate Trench MOSFET Technology
- · Excellent Stability and Uniformity
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device(Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

#### **Maximum Ratings**

Operating Junction Temperature Range : -55°C to +150°C

Storage Temperature Range: -55°C to +150°C

Thermal Resistance: 55°C/W Junction to Ambient (Note 2)

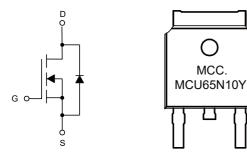
Thermal Resistance: 1.3°C/W Junction to Case

Thermal Resistance. 1.5 G/W sunction to Case				
Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V <sub>DS</sub>	100	V
Gate-Source Volltage		V <sub>GS</sub>	±20	V
Continuous Drain Current	T <sub>C</sub> =25°C	- I <sub>D</sub>	65	Α
	T <sub>C</sub> =100°C		41	
Pulsed Drain Current <sup>(Note 3)</sup>		I <sub>DM</sub>	260	Α
Total Power Dissipation (Note 4)		P <sub>D</sub>	96	W
Single Pulsed Avalanche Energy <sup>(Note 5)</sup>		E <sub>AS</sub>	169	mJ

#### Note:

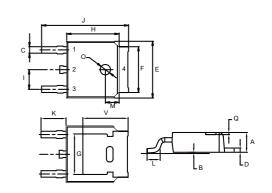
- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of  $R_{\rm BJA}$  is measured with the device mounted on 1in $^2$  FR-4 board with 2oz. Copper, in a still air environment with  $T_{\rm A}$  =25°C.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4.  $P_D$  is based on max. junction temperature, using junction-case thermal resistance.
- 5.  $T_J = 25$ °C,  $V_{DD} = 50$ V,  $V_{GS} = 10$ V,  $R_G = 25\Omega$ , L = 0.5mH.

## **Internal Structure and Marking Code**



# N-CHANNEL MOSFET

# **DPAK(TO-252)**



- 1. Gate
- 2,4. Drain
  - 3. Source

DIMENSIONS					
DIM INCHES		MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE
Α	0.087	0.094	2.20	2.40	
В	0.000	0.005	0.00	0.13	
С	0.026	0.034	0.66	0.86	
D	0.018	0.023	0.46	0.58	
Е	0.256	0.264	6.50	6.70	
F	0.201	0.215	5.10	5.46	
G	0.190		4.83		TYP.
Н	0.236	0.244	6.00	6.20	
ı	0.086	0.094	2.18	2.39	
J	0.386	0.409	9.80	10.40	
K	0.114		2.90		TYP.
L	0.055	0.067	1.40	1.70	
M	0.063		1.60		TYP.
0	0.043	0.051	1.10	1.30	
Q	0.000	0.012	0.00	0.30	
V	0.211		5.35		TYP.

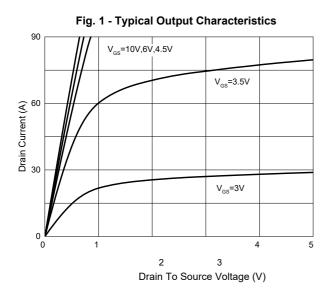


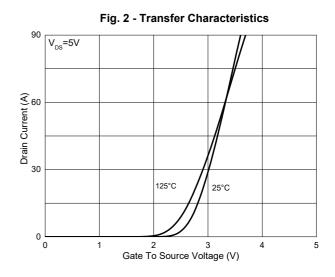
# Electrical Characteristics @ 25°C (Unless Otherwise Specified)

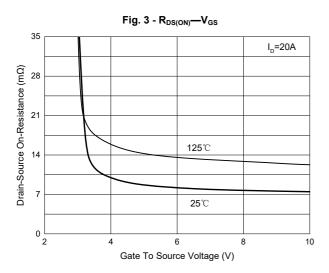
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics				1	1		
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	100			V	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V			1	μΑ	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.3	1.8	2.5	V	
Drain-Source On-Resistance	Б	V <sub>GS</sub> =10V, I <sub>D</sub> =20A		7.5	8.6	mΩ	
	$R_{DS(on)}$	V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A		9	11.5		
Gate Resistance	R <sub>g</sub>	f=1MHz, Open drain		0.9		Ω	
Diode Characteristics				•			
Continuous Body Diode Current	Is				65	Α	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A			1.2	V	
Reverse Recovery Time	t <sub>rr</sub>	L 004 H / H 4004/		52		ns	
Reverse Recovery Charge	Q <sub>rr</sub>	l <sub>F</sub> =20A, dl <sub>F</sub> /dt=100A/μs		74		nC	
Dynamic Characteristics				•			
Input Capacitance	C <sub>iss</sub>			2330			
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =50V, $V_{GS}$ =0V,f=1MHz		916		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			17.6			
Total Gate Charge	Q <sub>g</sub>			36			
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =50V,V <sub>GS</sub> =10V,I <sub>D</sub> =25A		6.6		nC	
Gate-Drain Charge	$Q_{gd}$			4.8			
Turn-On Delay Time	t <sub>d(on)</sub>			9.6			
Turn-On Rise Time	t <sub>r</sub>	V <sub>GS</sub> =10V, V <sub>DD</sub> =50V,I <sub>D</sub> =25A		3.5		no	
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_{GEN}$ =2.2 $\Omega$		25.5		ns	
Turn-Off Fall Time	t <sub>f</sub>			4.2			

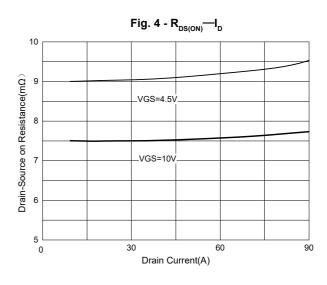


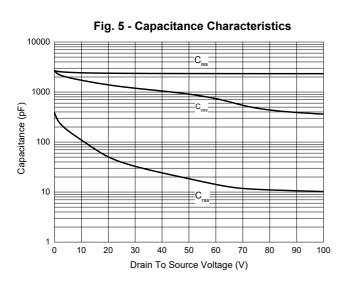
#### **Curve Characteristics**

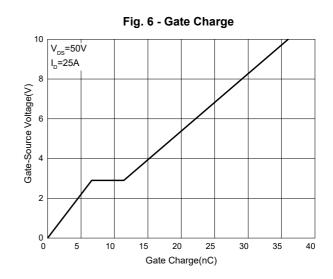






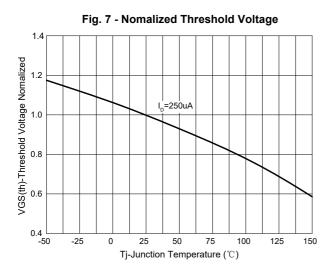


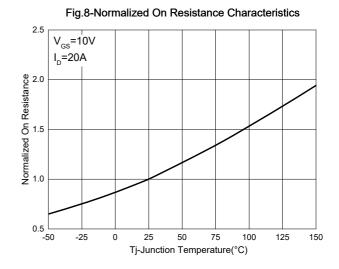


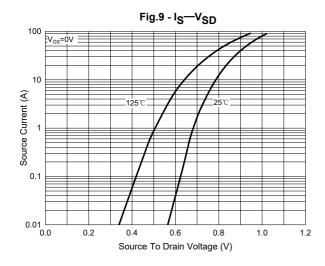


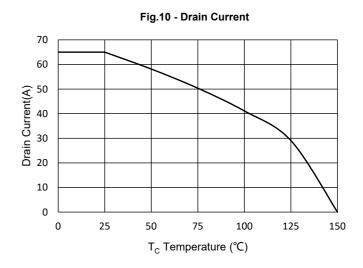


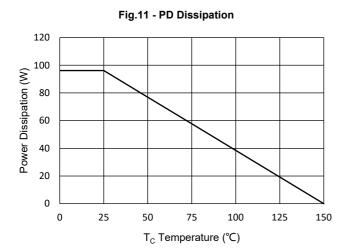
## **Curve Characteristics**













#### **Curve Characteristics**



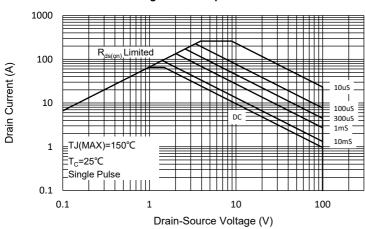
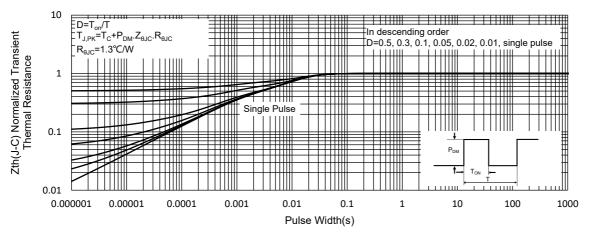


Fig.13 - Normalized Transient Thermal Impedance





## **Ordering Information**

Device	Packing	
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel	

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