

# Polar<sup>™</sup> HiPerFET Power MOSFET

# **Electrically Isolated Tab**

N-Channel Enhancement Mode Avalanche Rated Fast Recovery Diode

# **IXTR 200N10P**

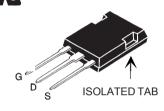
 $V_{DSS} = 100 V$   $I_{D25} = 120 A$   $R_{DS(on)} \le 8 m\Omega$ 



Symbol	Test Conditions	Maximum Ratings		
V <sub>DSS</sub>	T <sub>J</sub> = 25° C to 175° C	100	V	
<b>V</b> <sub>DGR</sub>	$T_J = 25^{\circ} \text{C to } 175^{\circ} \text{C}; R_{GS} = 1 \text{ M}\Omega$	100	V	
V <sub>gs</sub>		±20	V	
V <sub>GSM</sub>		±30	V	
I <sub>D25</sub>	T <sub>C</sub> = 25° C	120	А	
I <sub>D(RMS)</sub>	External lead current limit	75	Α	
I <sub>DM</sub>	$\rm T_{_{\rm C}}$ = 25° C, pulse width limited by $\rm T_{_{\rm JM}}$	400	Α	
I <sub>AR</sub>	T <sub>C</sub> =25°C	60	Α	
E <sub>AR</sub>	T <sub>C</sub> =25°C	100	mJ	
E <sub>as</sub>	T <sub>C</sub> =25°C	4	J	
dv/dt	$I_{S} \leq I_{DM}$ , di/dt $\leq 100$ A/ $\mu$ s, $V_{DD} \leq V_{DSS}$ , $T_{J} \leq 150^{\circ}$ C, $R_{G} = 4$ $\Omega$	10	V/ns	
$P_{D}$	T <sub>C</sub> =25°C	300	W	
T <sub>J</sub>		-55 +175	°C	
$T_{JM}$		175	°C	
T <sub>stg</sub>		-55 +150	°C	
V <sub>ISOL</sub>	50/60 Hz, RMS, 1 minute	2500	V~	
F <sub>c</sub>	Mounting Force	20120/4.620	Nm/lb	
Weight		5	g	

<b>Symbol</b> (T <sub>J</sub> = 25° C, t	Test Conditions unless otherwise specified)			aracteri Typ.	stic Va Max	
BV <sub>DSS</sub>	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$		100			V
$V_{\rm GS(th)}$	$V_{DS} = V_{GS}$ , $I_{D} = 500 \mu A$		3.0		5.0	V
I <sub>GSS</sub>	$V_{GS} = \pm 30 V_{DC}, V_{DS} = 0$				±100	nA
I <sub>DSS</sub>	$V_{DS} = V_{DSS}$ $V_{GS} = 0 V$ $V_{GS} = 0 V$	T <sub>J</sub> = 150° C T <sub>J</sub> = 175° C			25 250 1000	μΑ μΑ μΑ
R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_{D} = 60 \text{ A}$ $V_{GS} = 15 \text{ V}, I_{D} = 400 \text{A}$			5.5	8.0	mΩ $m$ Ω

## ISOPLUS 247<sup>™</sup> (IXTR) E153432



G = Gate D = Drain S = Source

#### **Features**

- Silicon chip on Direct-Copper-Bond substrate
  - High power dissipation
  - Isolated mounting surface
  - 2500V electrical isolation
- Low drain to tab capacitance(<30pF)
- <sup>1</sup> Avalanche voltage rated
- <sup>1</sup> Fast recovery intrinsic diode

### **Applications**

- DC-DC converters
- Battery chargers
- Switched-mode and resonant-mode power supplies
- DC choppers
- <sup>I</sup> AC motor control

### **Advantages**

- Easy assembly
- Space savings
- <sup>1</sup> High power density



Symbo	ol	Test Conditions (T = 2			ristic Values ise specified)
			Min.	Тур.	Max.
$g_{fs}$		$V_{DS}$ = 10 V; $I_{D}$ = 100 A, Note 1	60	97	S
$\mathbf{C}_{iss}$	)			7600	pF
C <sub>oss</sub>	}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		2900	pF
C <sub>rss</sub>	J			860	pF
t <sub>d(on)</sub>	)			30	ns
t <sub>r</sub>		$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 \text{ V}_{DSS}, I_{D} = 60 \text{ A}$		35	ns
$\mathbf{t}_{d(off)}$	(	$R_{_{\rm G}}$ = 3.3 $\Omega$ (External)		150	ns
t,	J			90	ns
$\mathbf{Q}_{g(on)}$	)			235	nC
$\mathbf{Q}_{gs}$	}	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 \text{ V}_{DSS}, I_{D} = 100 \text{ A}$		50	nC
$\mathbf{Q}_{\mathrm{gd}}$	J			135	nC
R <sub>thJC</sub>					0.5 ° C/W
$R_{\text{thCS}}$				0.15	° C/W

#### **Source-Drain Diode**

Characteristic Values (T<sub>1</sub> = 25° C, unless otherwise specified)

Symbol	Test Conditions	Min.	Тур.	Max.	
I <sub>s</sub>	V <sub>GS</sub> = 0 V			200	Α
I <sub>SM</sub>	Repetitive			400	Α
V <sub>sD</sub>	$I_F = I_S$ , $V_{GS} = 0$ V, Note 1			1.5	V
t <sub>rr</sub>	$I_F = 25 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$		100		ns

ISOPLUS247 (IXTR) Outline					
		A I		- S - 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
	INCH	IF C	1471 1 71	TETEDO	
MYZ	MIN	MAX	MIN	METERS MAX	
	190	.205	4.83	5.21	
A1	.090	.100	2.29	2.54	
A2	.075	.085	1.91	2.16	
b	.045	.055	1.14	1.40	
b1	.075	.084	1.91	2.13	
b2	.115	.123	2.92	3.12	
C	.024	.031	0.61	0.80	
l <del>ŏ</del>	.819	.840	20.80	21.34	
F	.620	.635	15.75	16.13	
e	.215		5.45		
	.780	.800	19.81	20.32	
<u>L</u> 1	.150	.170	3.81	4.32	
0	.220	.244	5.59	6.20	
R	.170	.190	4.32	4.83	
S	.520	.540	13.21	13.72	
l T	.620	.640	15.75	16.26	
Ü	.065	.080	1.65	2.03	
1 — GATE     2 — DRAIN (COLLECTOR)     3 — SOURCE (EMITTER)     4 — NO CONNECTION  NOTE: This drowing will meet all dimensions requirement of JEDEC outline TO-247AD except screw hole.					

Notes: 1. Pulse test, t  $\leq$ 300  $\mu$ s, duty cycle d  $\leq$  2 %



Fig. 1. Output Characteristics @ 25°C

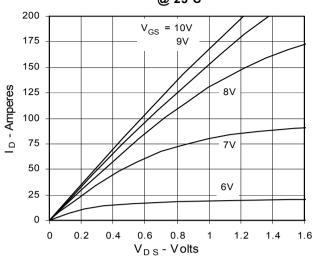


Fig. 3. Output Characteristics @ 150°C

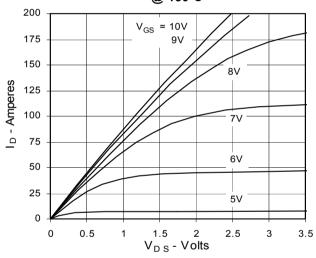


Fig. 5.  $R_{DS(on)}$  Normalized to  $I_D = 100A$ Value vs. Drain Current

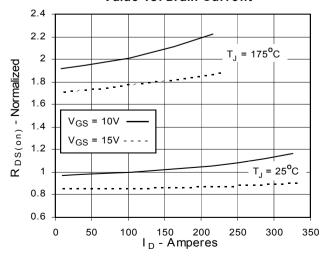


Fig. 2. Extended Output Characteristics
@ 25°C

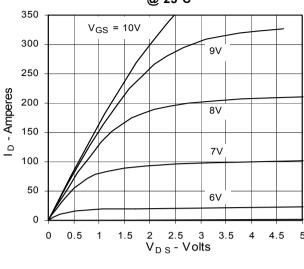


Fig. 4.  $R_{DS(on)}$  Normalized to  $I_D = 100A$ Value vs. Junction Temperature

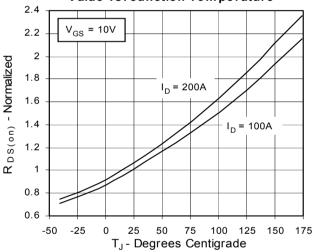
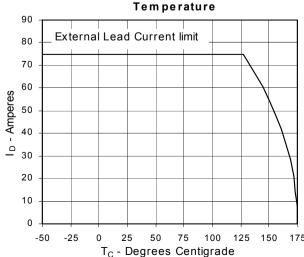
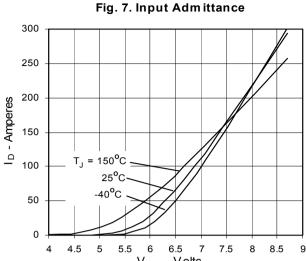
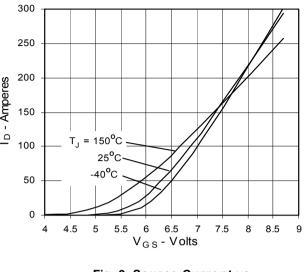


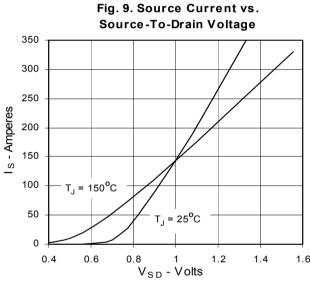
Fig. 6. Drain Current vs. Case
Temperature

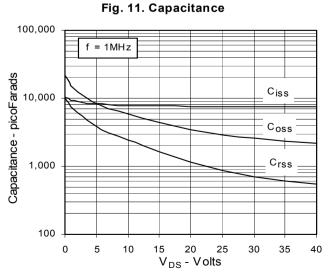


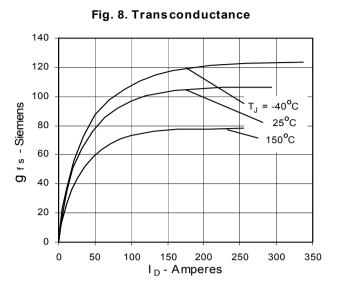


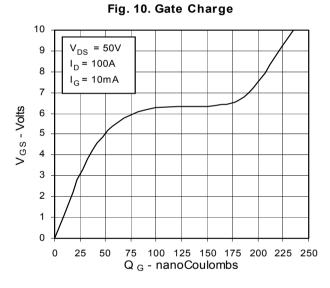


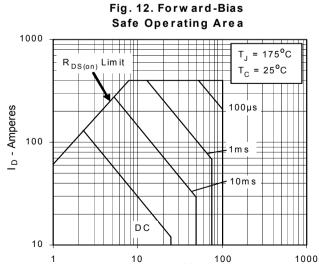












V<sub>DS</sub> - Volts

IXYS reserves the right to change limits, test conditions, and dimensions.



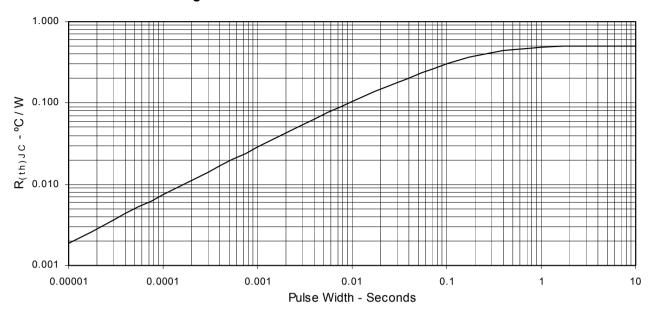


Fig. 13. Maximum Transient Thermal Resistance

