

Features

- Excellent Stability and Uniformity
- Split Gate Trench Mosfet Technology
- Lower $R_{DS(ON)}$
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- Moisture Sensitivity Level 1

Maximum Ratings

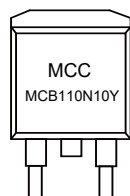
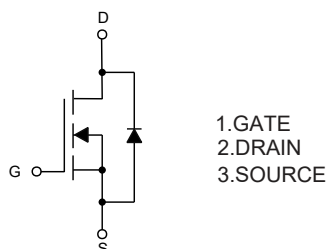
- Operating Junction Temperature Range: -55°C to $+150^{\circ}\text{C}$
- Storage Temperature Range: -55°C to $+150^{\circ}\text{C}$
- Thermal Resistance: 0.48°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain -Source Voltage	V_{DS}	100	V
Gate -Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	$T_C=25^{\circ}\text{C}$	A
		$T_C=100^{\circ}\text{C}$	
Drain Current-Pulse(Note2)	I_{DM}	440	A
Power Dissipation	P_D	260	W
Single Pulsed Avalanche Energy(Note3)	E_{AS}	400	mJ

Notes:

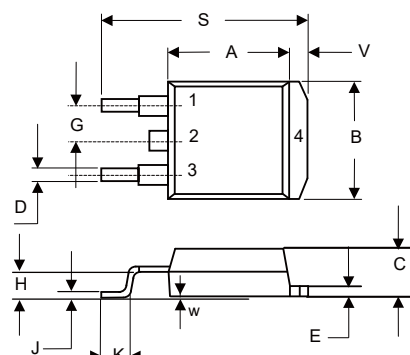
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. Pulse Width Limited by Maximum Junction Temperature.
3. EAS Condition: $T_J=25^{\circ}\text{C}$, $V_{DD}=50\text{V}$, $R_g=25\Omega$, $L=2\text{mH}$, $I_{AS}=31\text{A}$.

Internal Structure and Marking Code



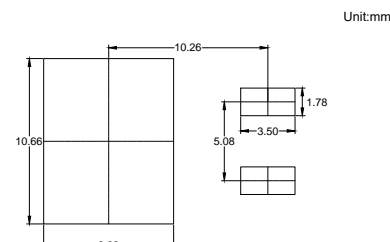
N-CHANNEL MOSFET

D²-PAK



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.331	0.370	8.40	9.40	
B	0.378	0.417	9.60	10.60	
C	0.165	0.189	4.20	4.80	
D	0.027	0.037	0.68	0.94	
E	0.045	0.055	1.14	1.40	
G	0.10		2.54		TYP.
H	0.096	0.134	2.43	3.40	
J	0.011	0.025	0.28	0.64	
K	0.071	0.131	1.80	3.32	
S	0.575	0.625	14.60	15.87	
V	0.042	0.058	1.07	1.47	
W	0.000	0.010	0.00	0.25	

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	100			V
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V, T_J=25^\circ C$			1	μA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0	2.8	4.0	V
Drain-Source On-Resistance ^(Note4)	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$		4.5	5	m Ω
Gate resistance	R_G	$V_{GS}=0V, f=1MHz$		0.9		Ω
Body Diode Voltage	V_{SD}	$I_{SD}=20A, V_{GS}=0V$			1.3	V
Dynamic Characteristics ^(Note 5)						
Input Capacitance	C_{iss}	$V_{DS}=50V, V_{GS}=0V, f=1MHz$		4600		pF
Output Capacitance	C_{oss}			1250		
Reverse Transfer Capacitance	C_{rss}			43		
Total Gate Charge	Q_g	$V_{DS}=50V, V_{GS}=10V, I_D=20A$		66		nC
Gate-Source Charge	Q_{gs}			23		
Gate-Drain Charge	Q_{gd}			6.6		
Reverse Recovery Charge	Q_{rr}	$I_F=20A, di/dt=100A/\mu s$		93		ns
Reverse Recovery Time	t_{rr}			63		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=50V, I_D=20A, V_{GS}=10V, R_G=2.2\Omega$		17.6		
Turn-On Rise Time	t_r			30.2		
Turn-Off Delay Time	$t_{d(off)}$			33.6		
Turn-Off Fall Time	t_f			39.6		

Note 4. Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.

5. Guaranteed by Design, Not Subject to Production Testing.

Curve Characteristics

Fig. 1 - Typical Output Characteristics

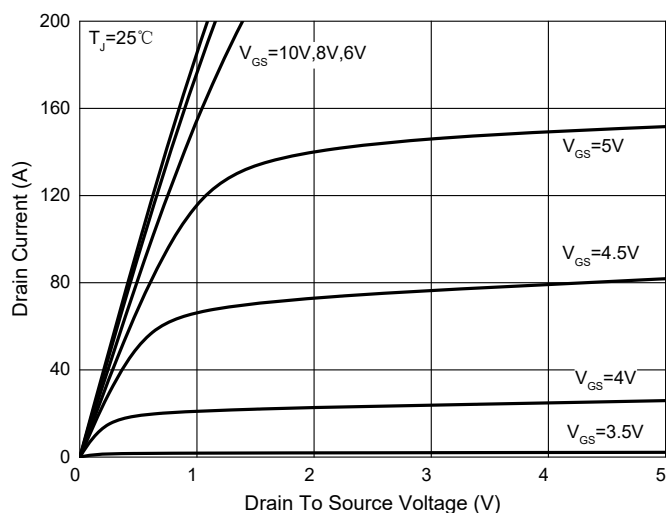


Fig. 2 - Transfer Characteristics

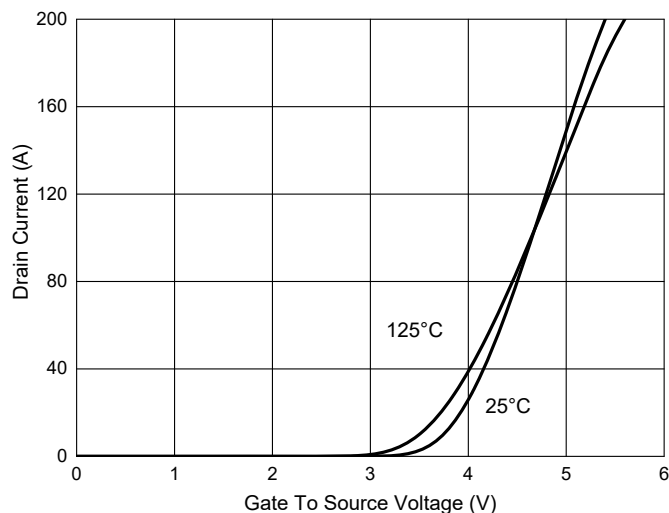


Fig. 3 - $R_{DS(ON)} - V_{GS}$

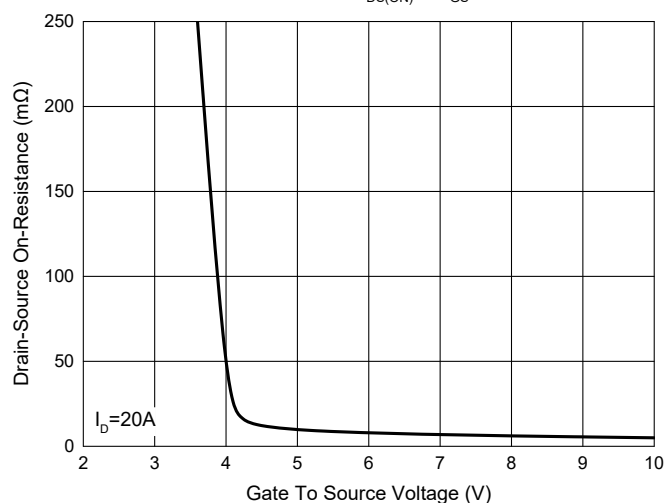


Fig. 4 - Normalized On Resistance Characteristics

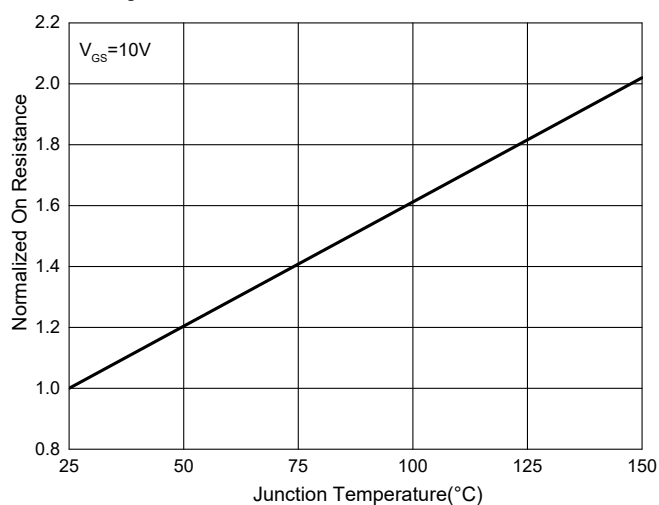


Fig. 5 - Capacitance Characteristics

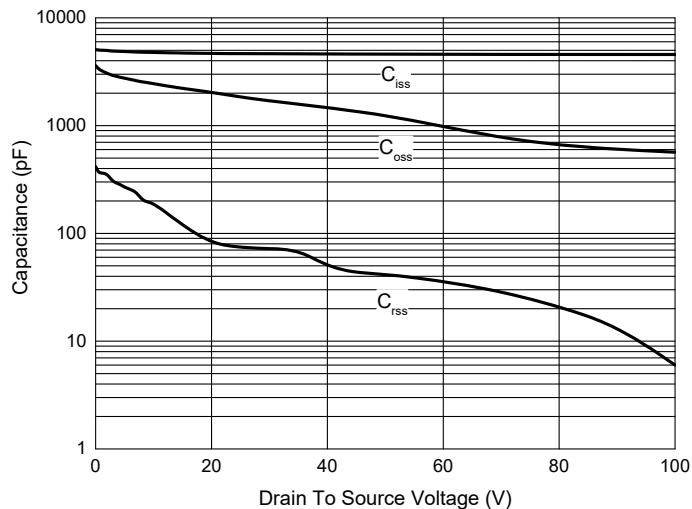
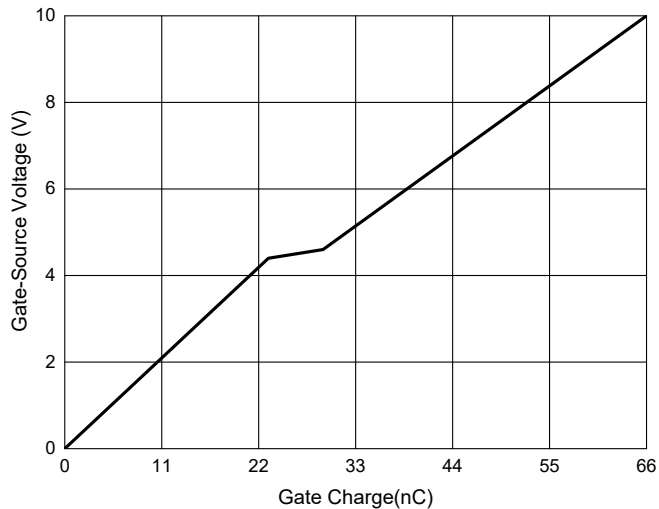


Fig. 6 - Gate Charge



Curve Characteristics

Fig. 7 - Safe Operation Area

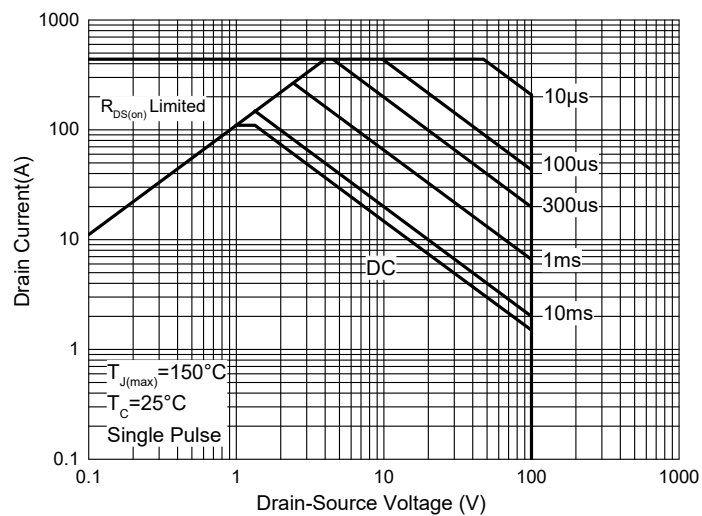
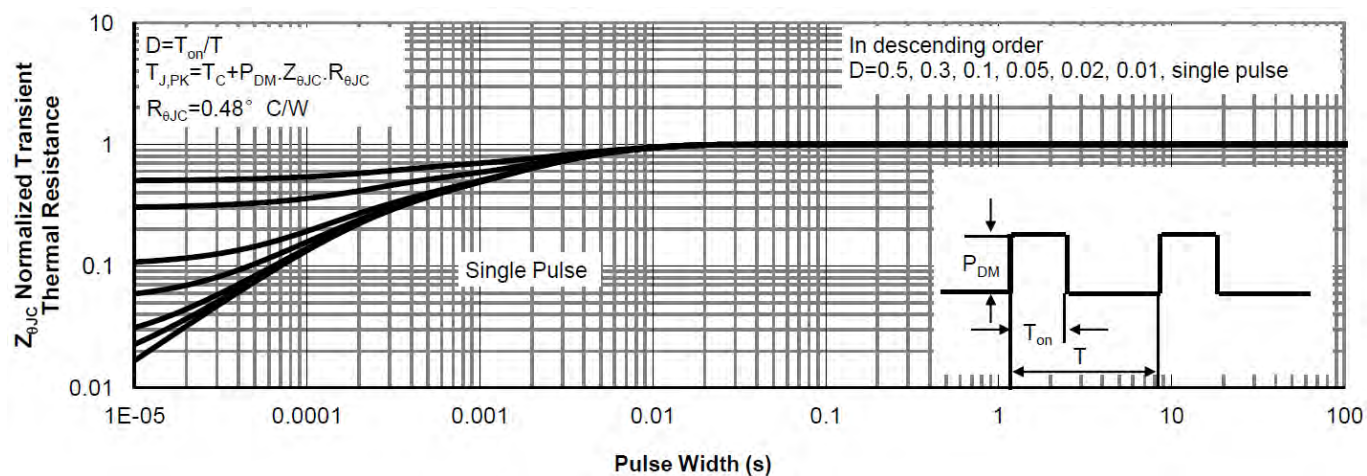


Fig.8 - Normalized Maximum Transient thermal impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 800pcs/Reel

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