

N-Channel Enhancement Mode Power MOSFET

Description

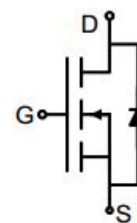
The GT023N10Q uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.

General Features

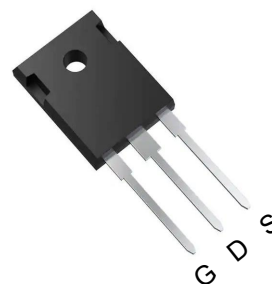
- V_{DS} 100V
- I_D (at $V_{GS} = 10V$) 226A
- $R_{DS(ON)}$ (at $V_{GS} = 10V$) < 2.7m Ω
- 100% Avalanche Tested
- RoHS Compliant

Application

- Power switch
- DC/DC converters



Schematic diagram



TO-247

Ordering Information

Device	Package	Marking	Packaging
GT023N10Q	TO-247	GT023N10	30pcs/Tube

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	100	V
Continuous Drain Current	I_D	226	A
Pulsed Drain Current (note1)	I_{DM}	904	A
Gate-Source Voltage	V_{GS}	± 20	V
Power Dissipation	P_D	250	W
Single pulse avalanche energy (note2)	E_{AS}	600	mJ
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 To 175	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62	$^\circ\text{C/W}$
Maximum Junction-to-Case	R_{thJC}	0.5	$^\circ\text{C/W}$

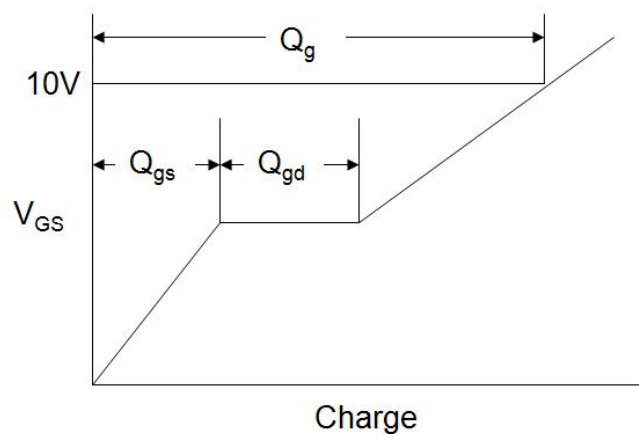
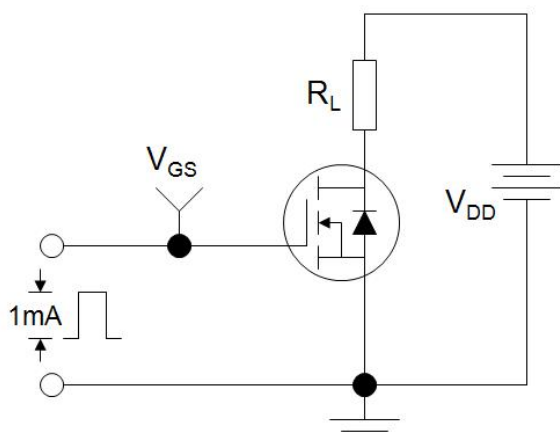
Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Parameters						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	100	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 100V, V_{GS} = 0V$	--	--	1	μA
Gate-Source Leakage	I_{GSS}	$V_{GS} = \pm 20V$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.0	3.0	4.0	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 80A$	--	2.1	2.7	m Ω
Forward Transconductance	g_{FS}	$V_{GS} = 5V, I_D = 80A$	--	126	--	S
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{GS} = 0V,$ $V_{DS} = 50V,$ $f = 0.5MHz$	--	8488	--	pF
Output Capacitance	C_{oss}		--	3436	--	
Reverse Transfer Capacitance	C_{rss}		--	97	--	
Total Gate Charge	Q_g	$V_{DD} = 50V,$ $I_D = 80A,$ $V_{GS} = 10V$	--	121	--	nC
Gate-Source Charge	Q_{gs}		--	36	--	
Gate-Drain Charge	Q_{gd}		--	26	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 50V,$ $I_D = 80A,$ $R_G = 5\Omega$	--	24	--	ns
Turn-on Rise Time	t_r		--	30	--	
Turn-off Delay Time	$t_{d(off)}$		--	94	--	
Turn-off Fall Time	t_f		--	74	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	$T_C = 25^{\circ}C$	--	--	226	A
Body Diode Voltage	V_{SD}	$T_J = 25^{\circ}C, I_{SD} = 80A, V_{GS} = 0V$	--	--	1.2	V
Reverse Recovery Charge	Q_{rr}	$I_F = 80A, V_{GS} = 0V$ $di/dt=100A/us$	--	297	--	nC
Reverse Recovery Time	T_{rr}		--	94	--	ns

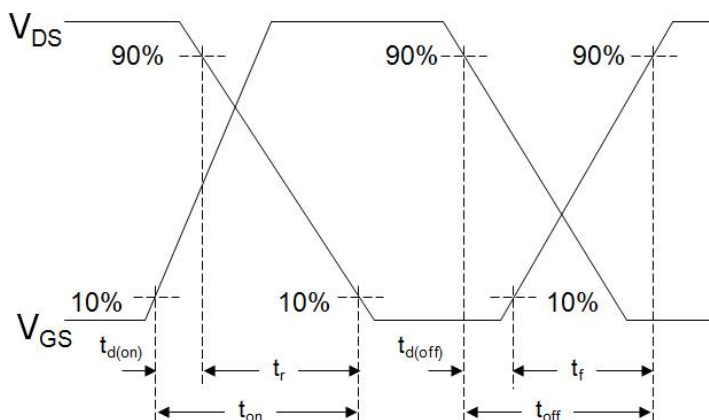
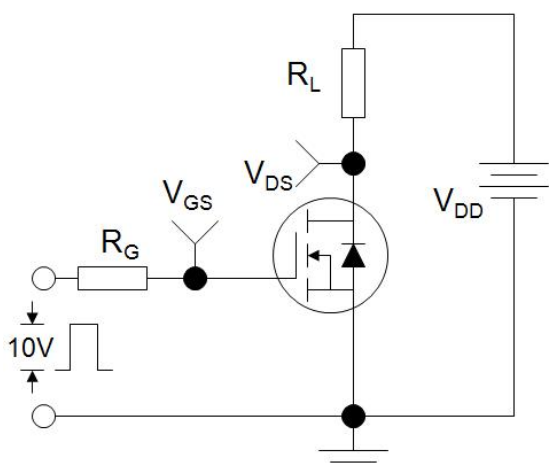
Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. EAS condition : $T_J = 25^\circ\text{C}$, $V_{DD} = 50V$, $V_{GS} = 10V$, $L = 0.5mH$, $R_G = 25\Omega$
3. Identical low side and high side switch with identical R_G

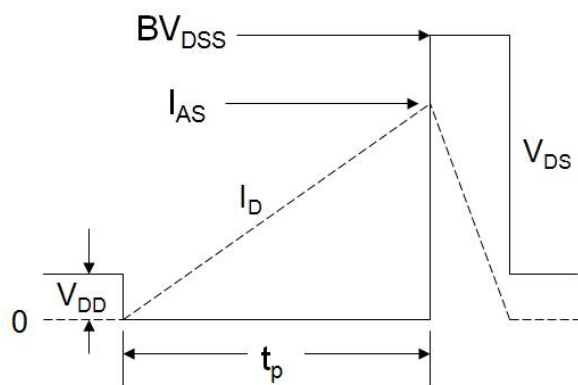
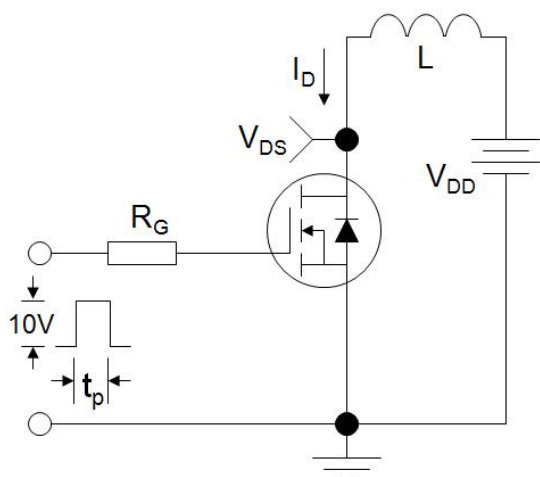
Gate Charge Test Circuit



Switch Time Test Circuit



EAS Test Circuit



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

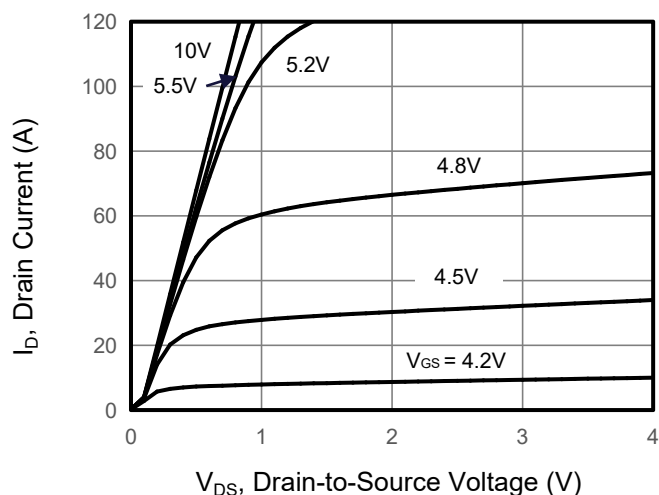


Figure 2. Transfer Characteristics

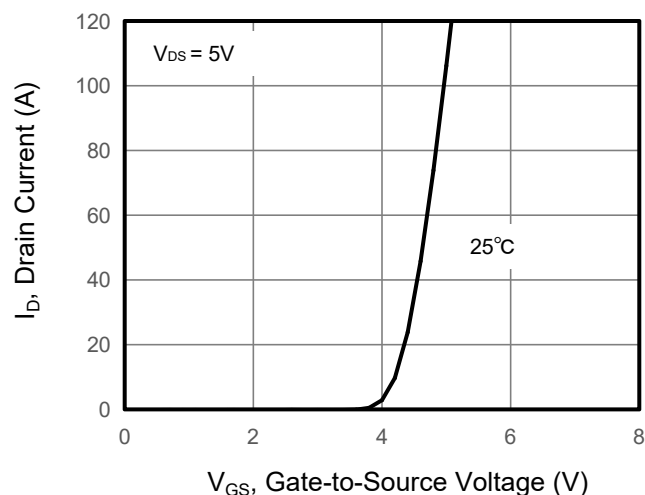


Figure 3. Drain Source On Resistance

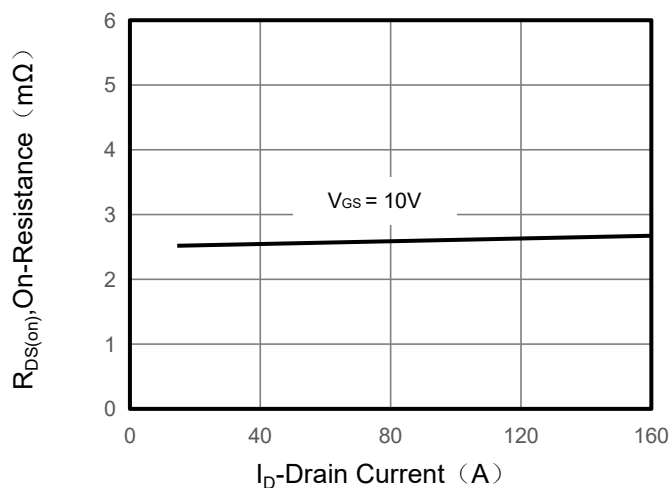


Figure 4. Gate Charge

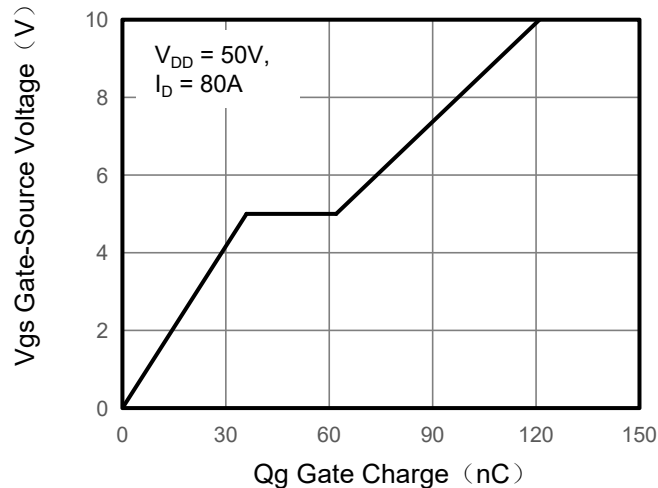


Figure 5. Capacitance

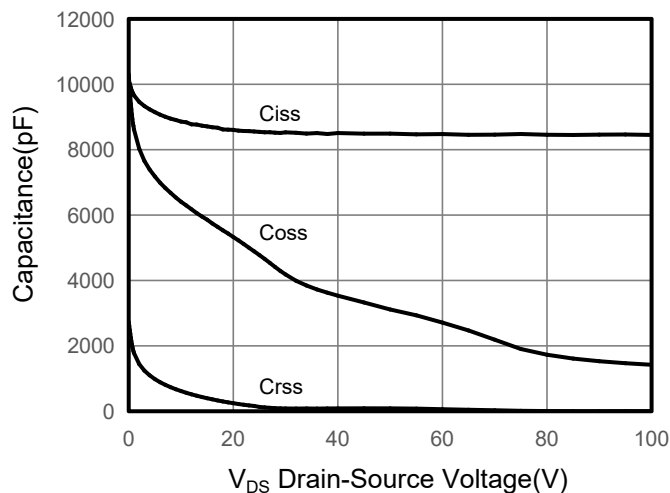
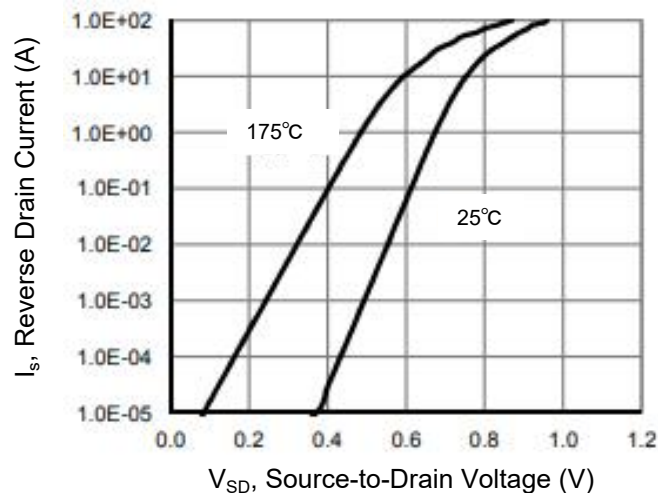


Figure 6. Source-Drain Diode Forward



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Figure 7. Drain-Source On-Resistance

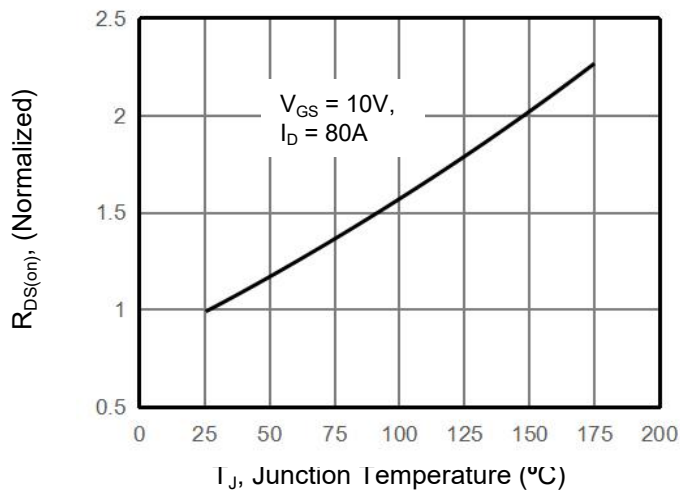


Figure 8. Safe Operation Area

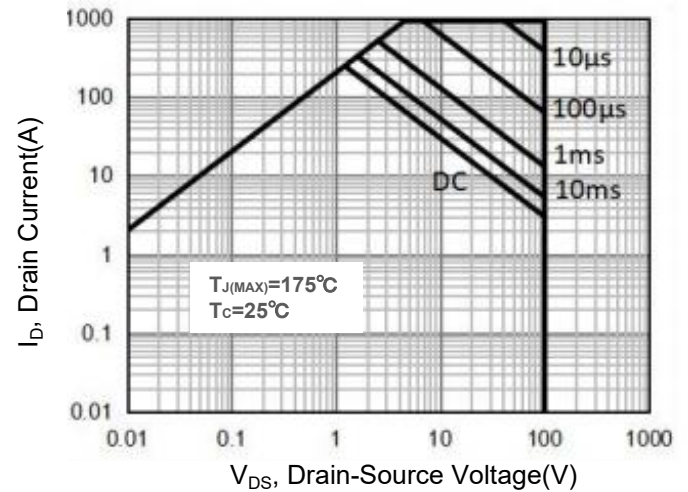
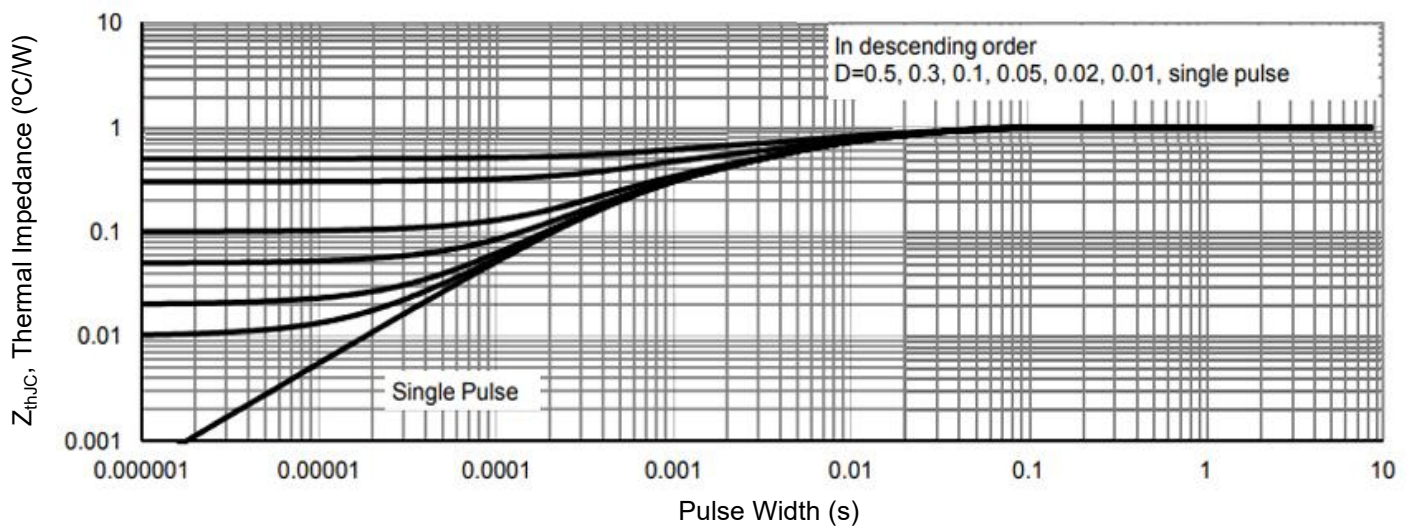
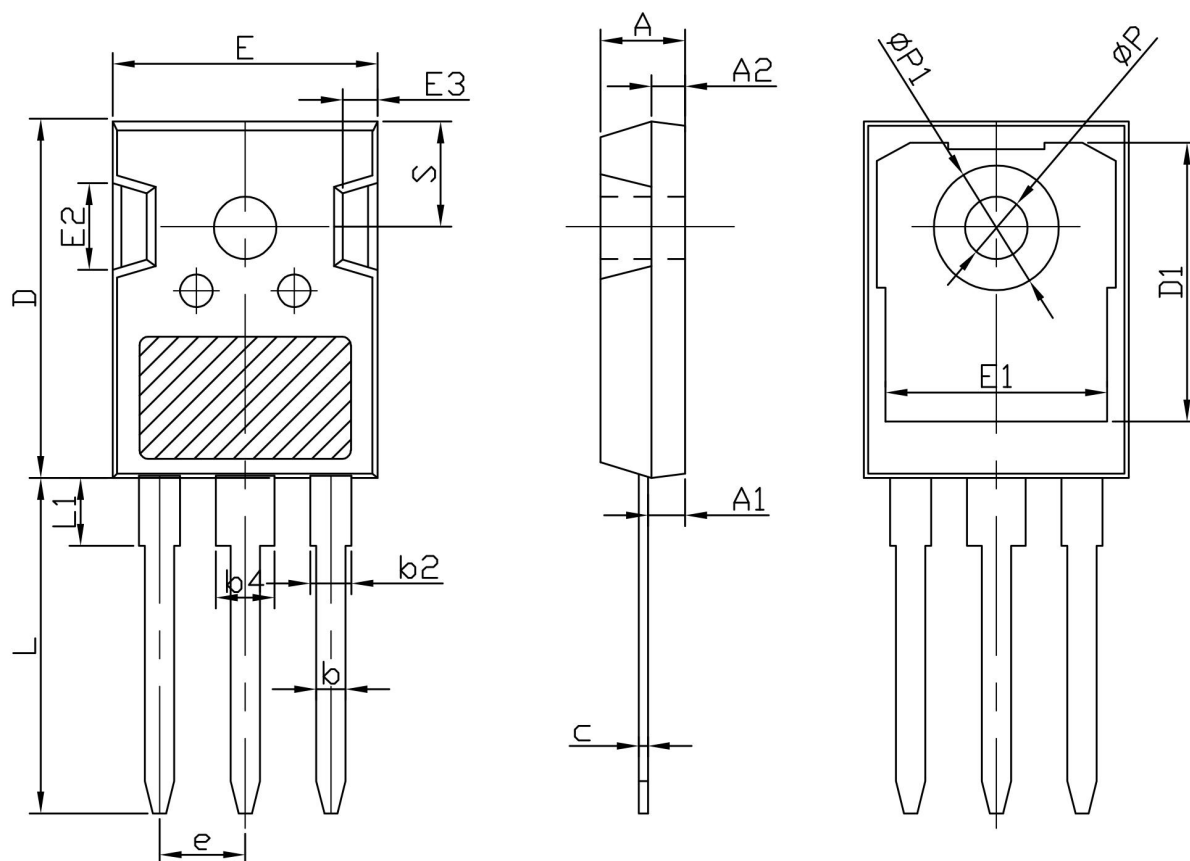


Figure 9. Normalized Maximum Transient Thermal Impedance



TO-247 Package Information



SYMBOL	mm		
	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.21	2.41	2.59
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
c	0.51	0.61	0.75
D	20.70	21.00	21.30
D1	16.25	16.55	16.85
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30	2.50	2.70
e	5.44BSC		
L	19.62	19.92	20.22
L1	—	—	4.30
ØP	3.40	3.60	3.80
ØP1	—	—	7.30
S	6.15BSC		