

# Digital Systems Design and Laboratory

## [ 2. Boolean Algebra ]

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# Outline

## ☒ Introduction

- ☐ Basic Operation
- ☐ Boolean Expressions and Truth Tables
- ☐ Basic Theorems
- ☐ Commutative, Associative, Distributive, and DeMorgan's Laws
- ☐ Simplification Theorems
- ☐ Multiplying Out and Factoring
- ☐ Complementing Boolean Expressions

# Introduction

## □ Boolean algebra

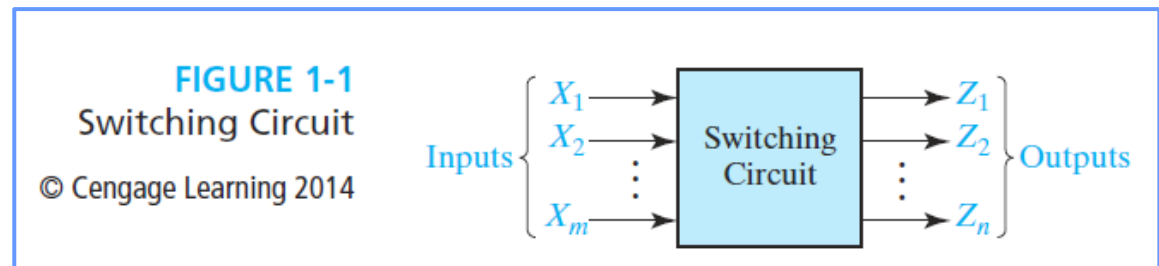
- Is the basic mathematics for logic design of digital systems

## □ History

- George Boole developed Boolean algebra in 1847 and used it to solve problems in mathematical logic
- Claude Shannon first applied Boolean algebra to the design of switching circuits in 1939
  - Master's thesis (21 years old)

## □ Switching devices we will use are essentially two-state devices

- Represent an input or output by a Boolean variable
- 1/0 for High/Low or True/False or Yes/No or Closed/Open
  - Just symbols
  - No numeric value



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☒ **Basic Operation**

☐ Boolean Expressions and Truth Tables

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☐ Simplification Theorems

☐ Multiplying Out and Factoring

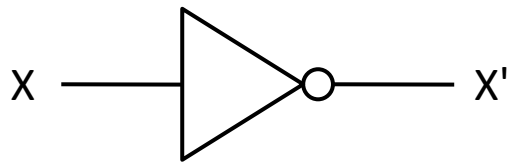
☐ Complementing Boolean Expressions

# Logic NOT

❑ Complement = Inverse = Negate = NOT ( ' ;  $\bar{\phantom{x}}$  ;  $\sim$  ;  $\neg$  )

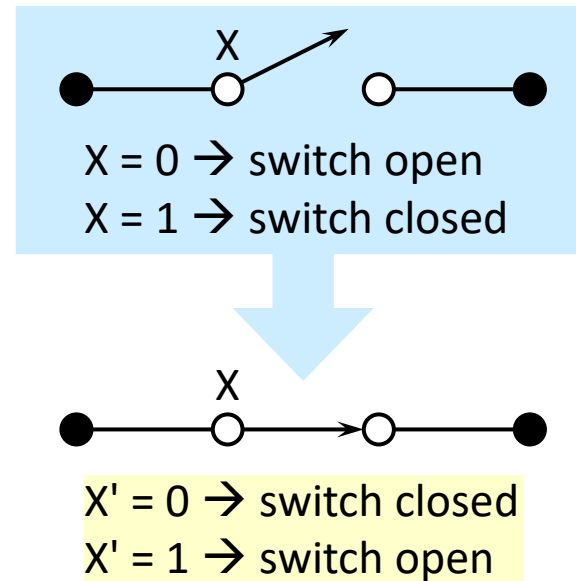
➤  $0' = 1, 1' = 0$

➤ Symbol (NOT gate, inverter)



➤ Truth table

X (Input)	X' (Output)
0	1
1	0

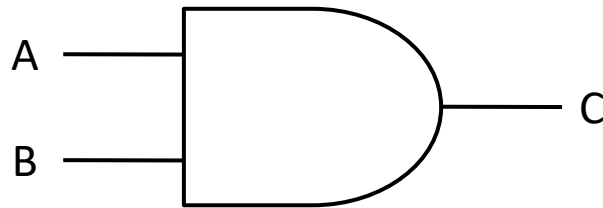


# Logic AND

## □ AND ( $\bullet$ ; $\wedge$ ; sometimes omitted)

➤  $0 \bullet 0 = 0, 0 \bullet 1 = 0, 1 \bullet 0 = 0, 1 \bullet 1 = 1$

➤ Symbol (AND gate)



➤ Truth table

A	B	$C = A \bullet B$
0	0	0
0	1	0
1	0	0
1	1	1



$C = 0 \rightarrow$  open circuit between 1 and 2

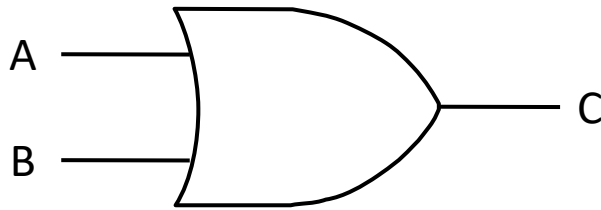
$C = 1 \rightarrow$  closed circuit between 1 and 2

# Logic OR

## □ OR ( + ; ∨ )

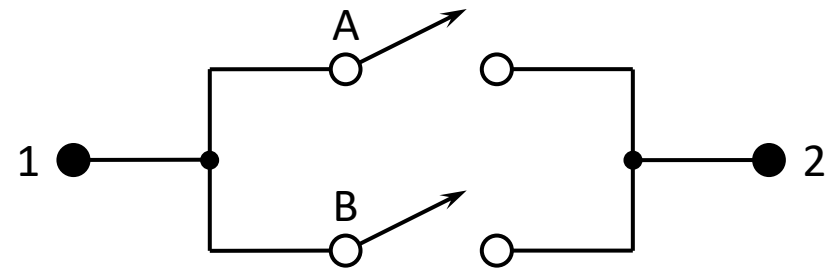
➤  $0 + 0 = 0, 0 + 1 = 1, 1 + 0 = 1, 1 + 1 = 1$

➤ Symbol (OR gate)



➤ Truth table

A	B	$C = A + B$
0	0	0
0	1	1
1	0	1
1	1	1



$C = 0 \rightarrow$  open circuit between 1 and 2

$C = 1 \rightarrow$  closed circuit between 1 and 2

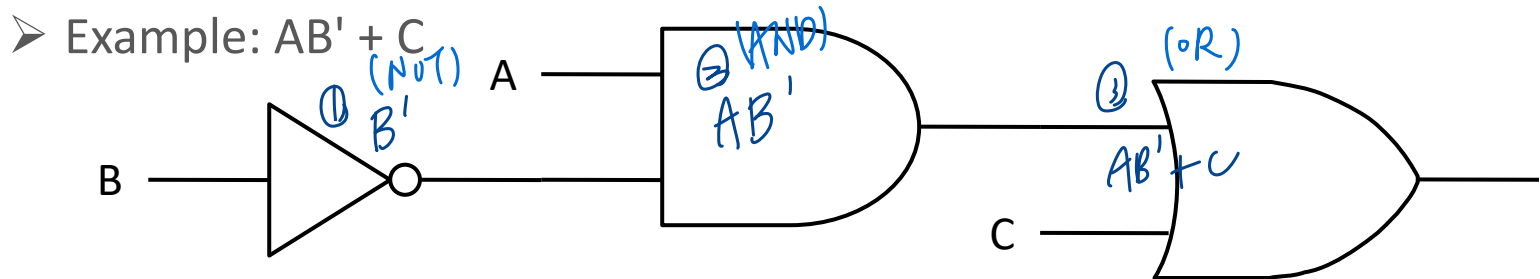
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- ☐ Complementing Boolean Expressions

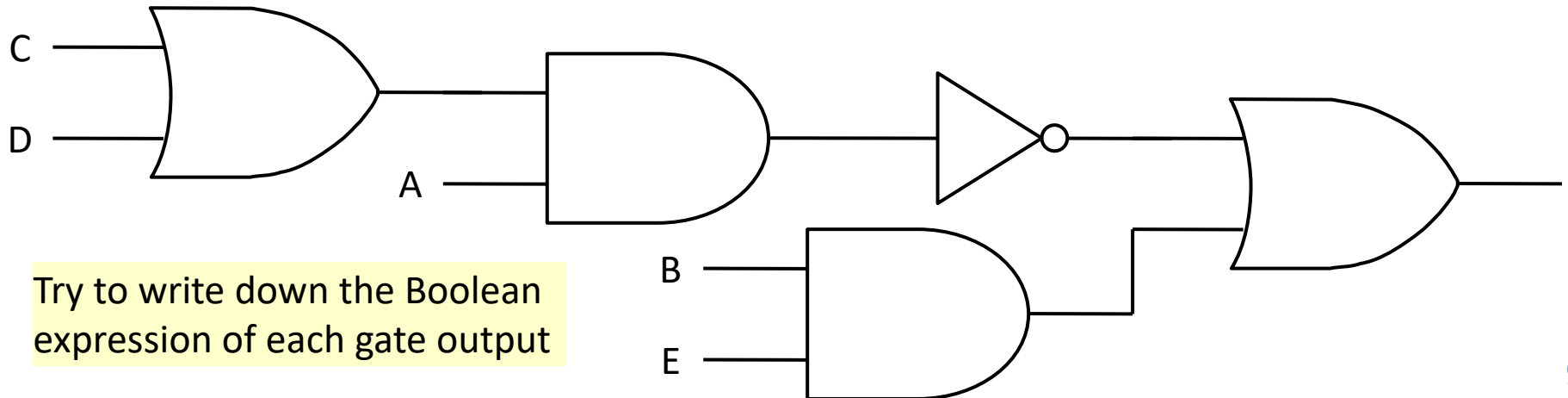


# Boolean Expressions vs. Logic Gates

- ❑ A Boolean expression is formed by basic operations on constants or variables, e.g., 0, 1, X, Y'
- ❑ Realize a Boolean expression by a circuit of logic gates
  - Perform operations in order: parentheses  $\rightarrow$  NOT  $\rightarrow$  AND  $\rightarrow$  OR



- Example:  $[A(C + D)]' + BE$



Try to write down the Boolean expression of each gate output

# Boolean Expressions vs. Truth Tables

↪ 検査 equivalence

□ A truth table specifies the output values of a Boolean expression for all possible combinations of input values

➤ How to check the equivalence between two expressions?

➤ Example:  $AB' + C = (A + C)(B' + C)$

A	B	C	B'	AB'	LHS	A+C	B'+C	RHS
0	0	0	1	0	0	0	1	0
0	0	1	1	0	1	1	1	1
0	1	0	0	0	0	0	0	0
0	1	1	0	0	1	1	1	1
1	0	0	1	1	1	1	1	1
1	0	1	1	1	1	1	1	1
1	1	0	0	0	0	1	0	0
1	1	1	0	0	1	1	1	1

相同 → 代表  $AB' + C$   $(A+C)(B'+C)$  equivalent

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# Basic Theorems

## □ Operations with 0 and 1

➤  $X + 0 = X$

➤  $X \bullet 1 = X$

➤  $X + 1 = 1$

➤  $X \bullet 0 = 0$

## □ Idempotent laws

➤  $X + X = X$   $\begin{cases} 0+0=0 \\ 1+1=1 \end{cases}$

➤  $X \bullet X = X$   $\begin{cases} 0 \cdot 0 = 0 \\ 1 \cdot 1 = 1 \end{cases}$

## □ Involution law

➤  $(X')' = X$   $\begin{cases} (1')' = (0)' = 1 \\ (0')' = (1)' = 0 \end{cases}$

## □ Laws of complementarity

➤  $X + X' = 1$   $\because X \text{ 和 } X' \text{ 必有一个为 } 1$

➤  $X \bullet X' = 0$   $\because X \text{ 和 } X' \text{ 必有一个为 } 0$

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# Commutative and Associative Laws

## Commutative laws for AND and OR

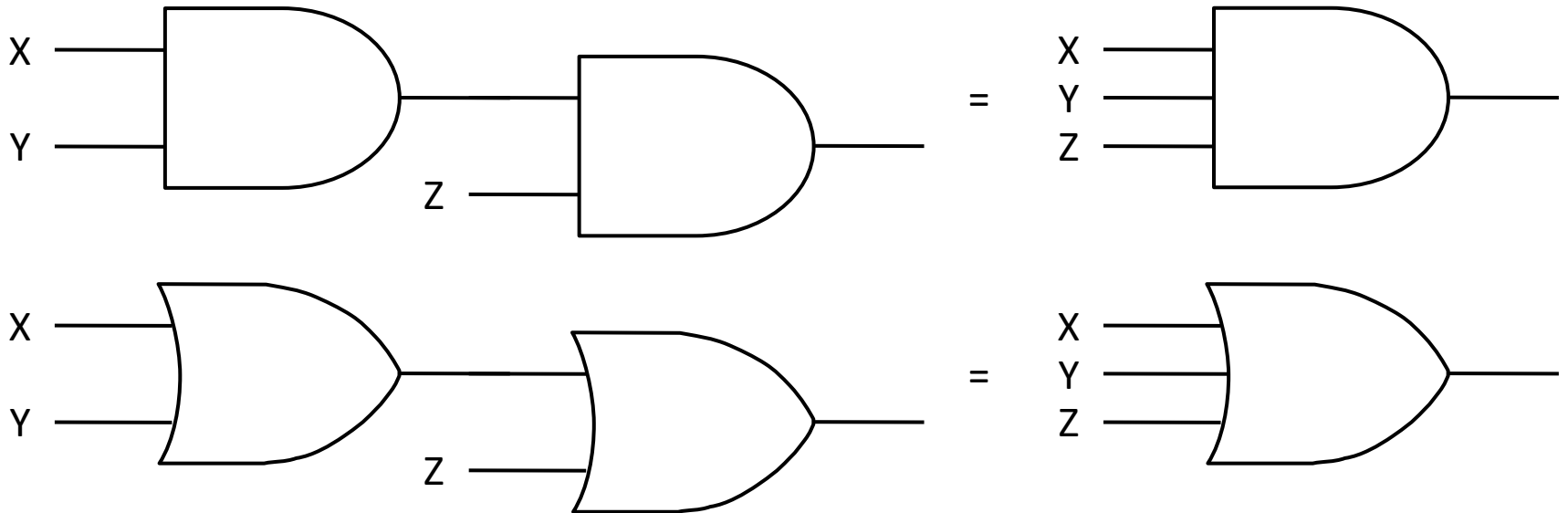
➤  $XY = YX$

➤  $X + Y = Y + X$

## Associative laws for AND and OR

➤  $(XY)Z = X(YZ) = XYZ$

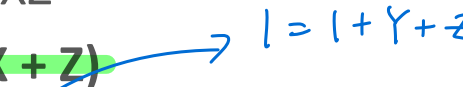
➤  $(X + Y) + Z = X + (Y + Z) = X + Y + Z$



# Distributive and DeMorgan's Laws

## □ Distributive laws

➤ Ordinary one :  $X(Y + Z) = XY + XZ$

➤ Second one:  $X + YZ = (X + Y)(X + Z)$    $1 = 1 + Y + Z$

- $X + YZ = X \bullet 1 + YZ = X (1 + Y + Z) + YZ = X + XY + XZ + YZ = XX + XY + XZ + YZ$   
 $= XX + XZ + YX + YZ = X (X + Z) + Y (X + Z) = (X + Y)(X + Z)$

- You can also use a truth table to prove it

## □ DeMorgan's laws

➤  $(X + Y)' = X'Y'$

➤  $(XY)' = X' + Y'$

# Duality (1/2)

- ❑ The dual of a Boolean expression is obtained by
  - Interchanging the constants 0 and 1
  - Interchanging the operations of AND and OR
  - Leaving variables and complements unchanged
- ❑ Given a Boolean identity, another identity can be obtained by taking the dual of both sides of the identity



# Duality (2/2)

## □ Laws of Boolean algebra

Dual: ① 0,1 交换 ② AND  $\leftrightarrow$  OR 交换 ③ variable complement 不变.

- Operations with 0 and 1
  - [1]  $X + 0 = X$  [1D]  $X \bullet 1 = X$
  - [2]  $X + 1 = 1$  [2D]  $X \bullet 0 = 0$
- Idempotent laws
  - [3]  $X + X = X$  [3D]  $X \bullet X = X$
- Involution law
  - [4]  $(X')' = X$
- Laws of complementarity
  - [5]  $X + X' = 1$  [5D]  $X \bullet X' = 0$
- Commutative laws
  - [6]  $X + Y = Y + X$  [6D]  $XY = YX$
- Associative laws
  - [7]  $(X + Y) + Z = X + (Y + Z) = X + Y + Z$
  - [7D]  $(XY)Z = X(YZ) = XYZ$
- Distributive laws
  - [8]  $X(Y + Z) = XY + XZ$
  - [8D]  $X + YZ = (X + Y)(X + Z)$
- DeMorgan's laws
  - [9]  $(X + Y)' = X'Y'$
  - [9D]  $(XY)' = X' + Y'$

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# Simplification Theorems

## □ Uniting

dual  $\hookrightarrow$   $\textcolor{green}{XY + XY' = X}$   $\quad \overset{1}{X(Y+Y')} = X \cdot 1 = X$

$\hookrightarrow (X + Y)(X + Y') = X$

## □ Absorption

dual  $\hookrightarrow$   $\textcolor{green}{X + XY = X}$   $\quad X \cdot 1 + X \cdot Y = X \cdot (1 + Y) = X \cdot 1 = X$

$\hookrightarrow X(X + Y) = X$

## □ Elimination

$\hookrightarrow \textcolor{green}{X + X'Y = X + Y}$   $\quad \begin{cases} X=0 & X+X'Y = 0+1 \cdot Y = Y = 0+Y = X+Y \\ X=1 & X+X'Y = 1+0 \cdot Y = 1 = 1+Y = X+Y \end{cases}$

$\hookrightarrow X(X' + Y) = XY$

## □ Consensus

$\hookrightarrow \textcolor{green}{XY + X'Z + YZ = XY + X'Z}$   $\quad \swarrow \text{可直接去掉.}$

$\hookrightarrow (X + Y)(X' + Z)(Y + Z) = (X + Y)(X' + Z)$

Absorption

$z=1 \quad z+yz = 1+y=1$   
 $z=0 \quad z+yz = 0+0=0$

if  $x=0$ :

(左)  $XY + X'Z + YZ = z + Yz = z$

(右)  $XY + X'Z = z$

if  $x=1$ :

(左)  $XY + X'Z + YZ = Y + Yz = Y$

(右)  $XY + X'Z = Y$

# Simplification Practices

□ Simplify  $Z = A'BC + A'$

$$A'BC + A' = A'BC + A' \cdot 1 = A'(BC + 1) = A' \quad \text{X}$$

□ Simplify  $Z = [A + B'C + \underline{D + EF}][A + B'C + \underline{(D + EF)'}]$

$$= [A + B'C + D + EF][A + B'C + D'(EF)'] = [A + B'C + D + EF][A + B'C + D'(E'F')]$$

□ Simplify  $Z = (\underline{AB + C})(\underline{B'D + C'E'}) + (\underline{AB + C})' = \text{X}$

$$= (AB + C)' + (B'D + C'E')$$

$$X + X'Y = X + Y \quad (\text{elimination})$$

$$= \overset{X}{[A + B'C + D + EF]} \overset{Y}{[A + B'C + D'E' + D'F']} \overset{Z}{=} (X + Y)(X + Z) = X + YZ$$

$$= (A + B'C) + (D + EF)(D'E' + D'F') = A + B'C \quad \square$$

$D'(E' + F') = (D + EF)'$

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# Multiplying Out

- ❑ Use the distributive laws to multiply out an expression to obtain a sum-of-products (SOP) form

➤ Ordinary distributive law:  $X(Y + Z) = XY + XZ$

➤ Second distributive law:  $X + YZ = (X + Y)(X + Z)$

- ❑ Example: multiply out  $(\underbrace{A}_x + \underbrace{BC}_y)(\underbrace{A}_x + \underbrace{D + E}_z)$

➤ Use the ordinary distributive law

$$\begin{aligned} \bullet (A + BC)(A + D + E) &= A + AD + AE + ABC + BCD + BCE \\ &= A(1 + D + E + BC) + BCD + BCE \\ &= \underline{A + BCD + BCE} \end{aligned}$$

$$\begin{aligned} (x+y)(x+z) \\ &= x + yz \end{aligned}$$

$$\begin{aligned} &\rightarrow A + BC \cdot (D + E) \\ &= A + BCD + BCE \end{aligned}$$

➤ Use the second distributive law *sum of products*

$$\bullet (A + BC)(A + D + E) = A + BC(D + E) = \underline{A + BCD + BCE}$$

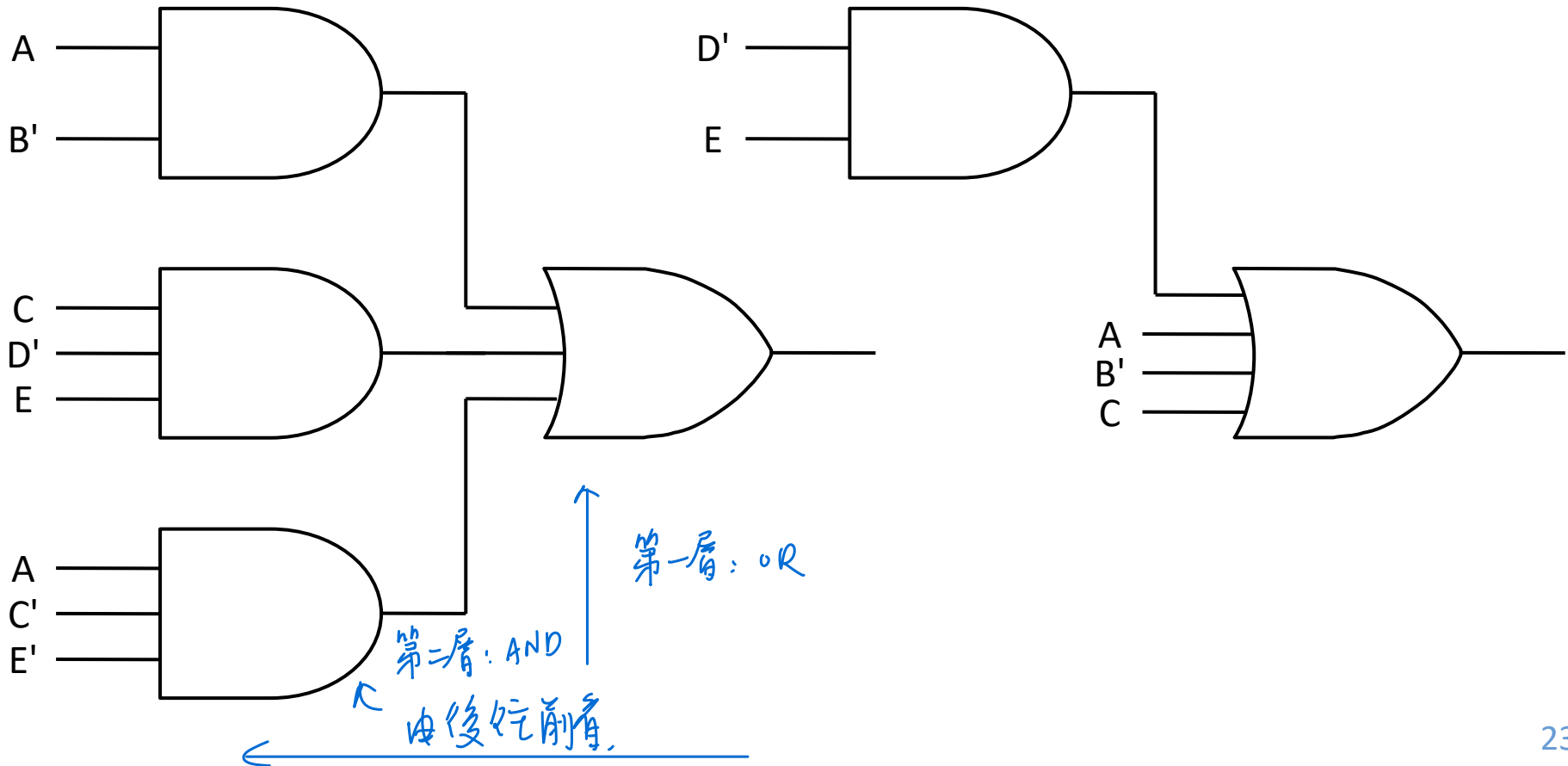
# SOP vs. Logic Gates

\* SOP 對應一個兩層的 AND-OR circuit

## □ Realize SOPs by two-level circuits (AND-OR)

➤  $AB' + CD'E + AC'E'$

➤  $A + B' + C + D'E$



# Factoring

- ❑ Use the second distributive law to factor an expression to obtain a **product-of-sums** (POS) form

➤  $\underline{X} + YZ = (X + Y)(X + Z)$

- ❑ Example: factor  $A + \underline{B'}\underline{C}D$

$$= (A + B')(A + CD) = (A + B')(A + C)(A + D)$$

- ❑ Example: factor  $AB' + C'D$

$$= (AB' + C')(AB' + D) = (A + C')(B' + C')(A + D)(B' + D)$$

- ❑ Example: factor  $C'D + C'E' + G'H$

$$\begin{aligned} &= C'(D + E') + G'H = [C'(D + E') + G'] [C'(D + E') + H] \\ &= (C' + G')[(D + E') + G'] (C' + H)[(D + E') + H] \\ &= (C' + G')(D + E' + G')(C' + H)(D + E' + H) \end{aligned}$$

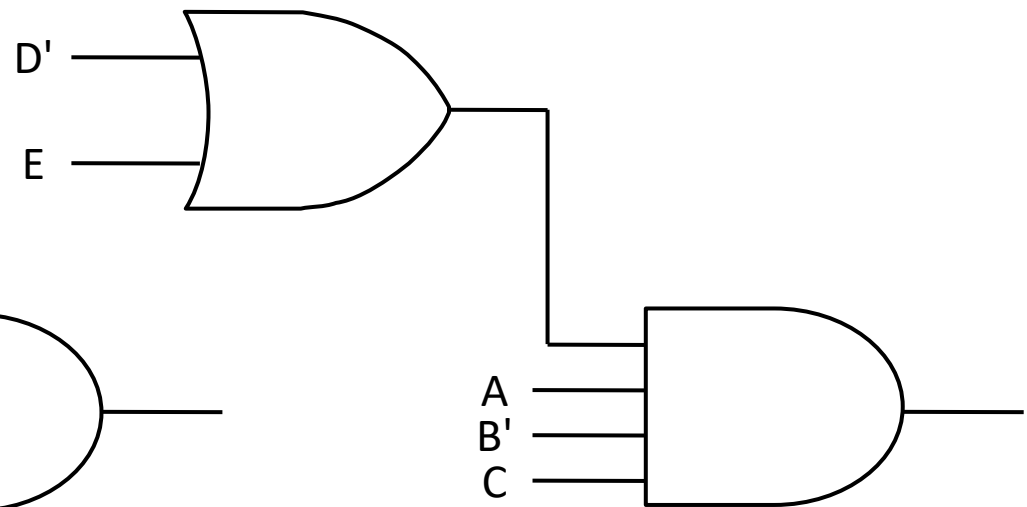
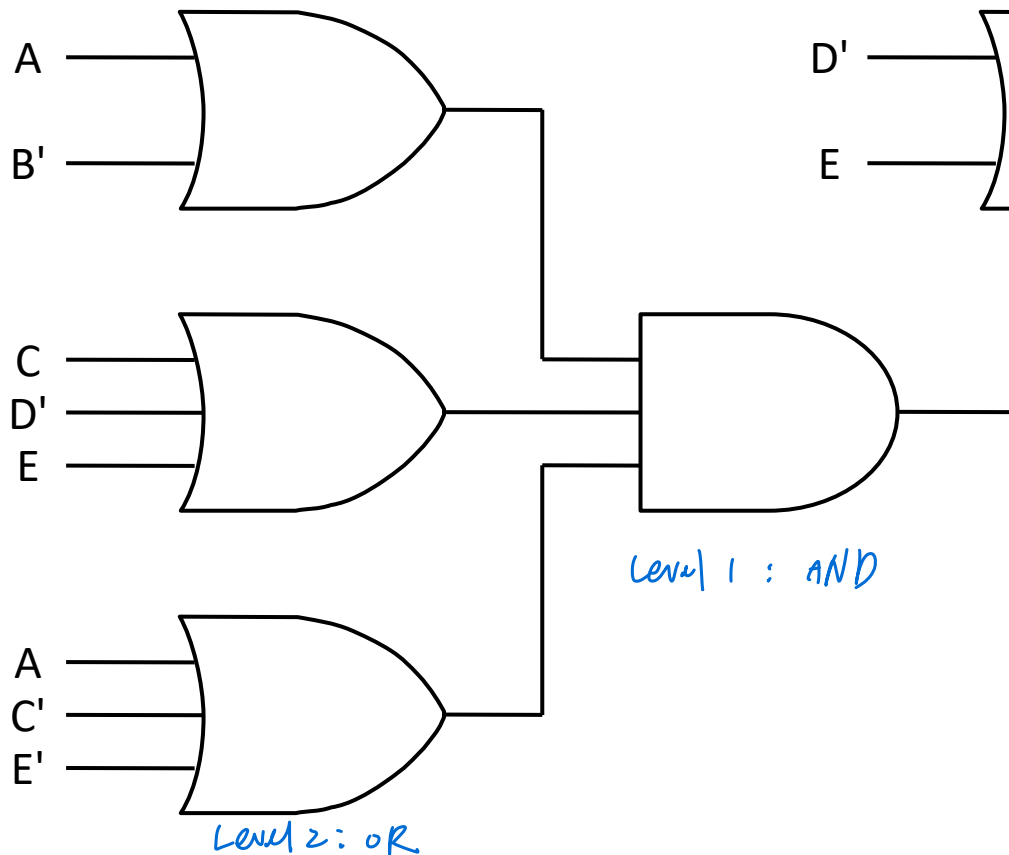


# POS vs. Logic Gates

## □ Realize POSs by two-level circuits (OR-AND)

➤  $(A + B')(C + D' + E)(A + C' + E')$

➤  $AB'C(D' + E)$



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# Complementing Boolean Expressions

## □ DeMorgan's laws with n variables

➤  $(X_1 + X_2 + \dots + X_n)' = X_1' X_2' \dots X_n'$

➤  $(X_1 X_2 \dots X_n)' = X_1' + X_2' + \dots + X_n'$

## □ Complement an expression by iteratively applying DeMorgan's laws

➤ Example: complement  $(AB' + C)D' + E$  so that NOT is applied only to single variables

$$\begin{aligned} \bullet \quad & [(A \bullet B' + C) \bullet D' + E]' = [(A \bullet B' + C) \bullet D']' \bullet E' \\ & = [(A \bullet B' + C)' + D] \bullet E' \\ & = [(A \bullet B')' \bullet C' + D] \bullet E' \\ & = [(A' + B) \bullet C' + D] \bullet E' \end{aligned}$$

# Q&A