



Ahsanullah University of Science & Technology

Department of Computer Science & Engineering

Course No : CSE3108
Course Title : Microprocessors Sessional
Assignment No : 04

Date of Submission :19-07-2021

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Section : A

Assignment - 4.

DSEG SEGMENT 'DATA'

Dats DB 00001000b, 01111111b, 01111111b,
00001000b ; 23-21-23

DSEG ENDS

SSSEG SEGMENT STACK 'STACK'

DW 100h DUP(?)

ENDS

CSSEG SEGMENT 'CODE'

START PROC FAR

PUSH DS

MOV AX, 0

PUSH AX

MOV AX, DSEG

MOV DS, AX

MOV ES, AX

MOV DX, 2000h

MOV BX, 0

MAIN LOOP

MOV SI, 0

MOV CX, 5

NEXT:

MOV AL, Dots [BX][SI]

OUT DX, AL

INC SI

INC DX

CMP SI, 5

LOOPNE NEXT

ADD BX, 5

CMP BX, 40

RET

START ENDP

CSEG ENDS

END START