

Ahsanullah University of Science & Technology

Department of Computer Science & Engineering

Course No : CSE2214

Course Title : Assembly Language Programming Sessional

Assignment No : 01

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Submitted To : Ms. Tahsin Aziz & Ms. Samiha Sharmin Shimmi

Submitted By-

Group : A_2

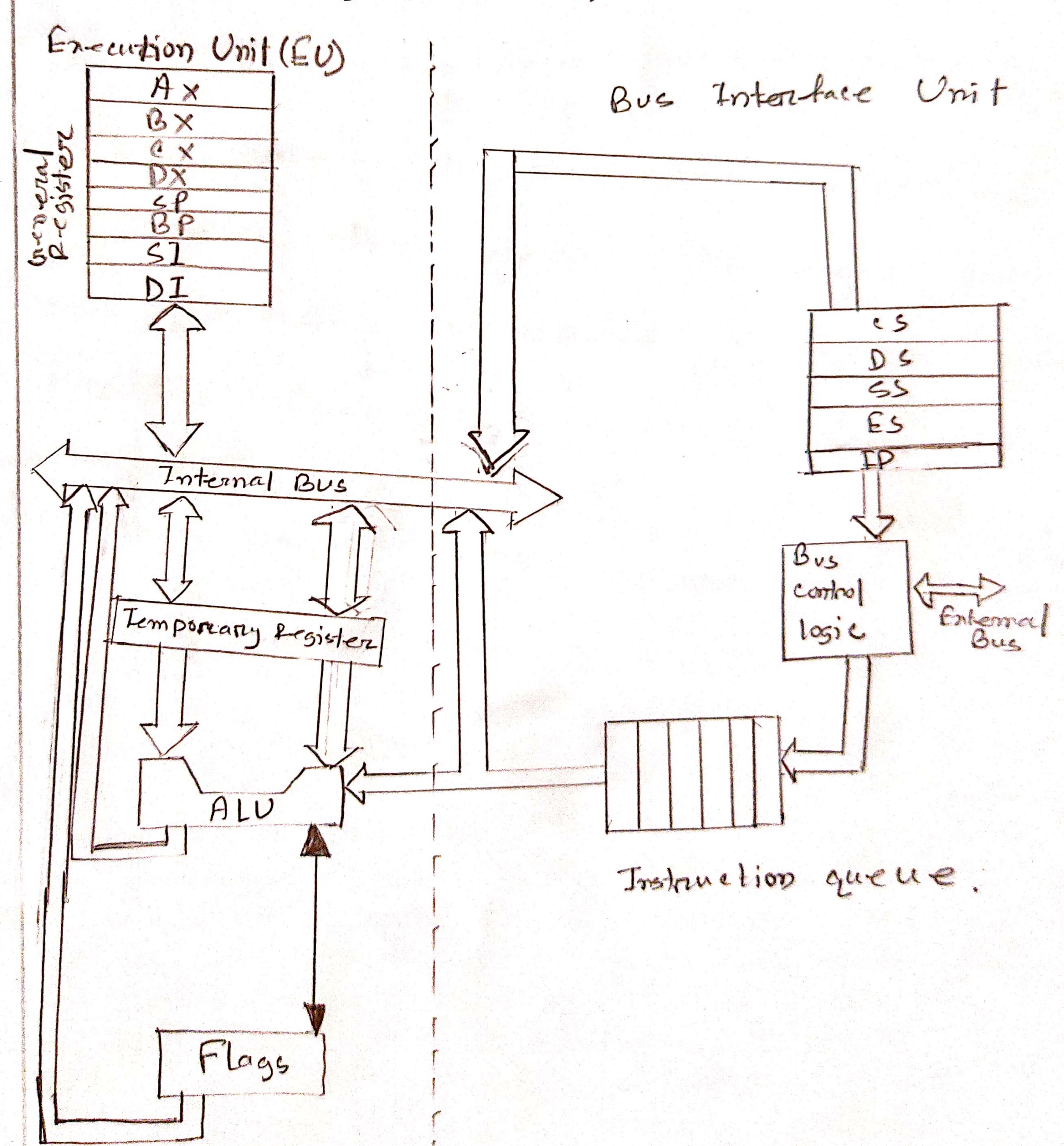
Name : Minhajul Islajm

Id : 17.01.04.001

Section : A

Question-No-1. Draw the diagram of the intel8086 Microprocesson organization.

Ans. The dragram of Intel 8086 Microprocessore Origonization is given below.



guestion-2. Consider a machine language instruction that moves a copy of the contents of magister Ax into Epu CPU to a memony wand. What happens during the fetch eyele and lea eeution eyele.

Ans: In order to execute an instruction a mane machine goes through two eyeles. They are,

- 1. Fetch eycle.
- ?, Ene cu-tion endry cycle.

Fetch cycle:The fetch is an instruction from memory. The first estep the CPU cannies out is to tetch some data and instructions throw main memory then stone them is it's own internal temporary memory areas This is called the betch part of the cycle.

Execution eyete: This is the part of

the cycle when data processing cictually

takes place. The instruction is carried

out upon the data. The result of

this processing is stoned in yet another

negister. Once the eyele is complete

another eyele, once more.